IPMI Metrics

Metric Name	Description	Unit
Ambient_Temp	Node ambient temperature	°C
CPU_Core_Temp_1,...,CPU_Core_Temp_24	Core temperature	°C
CPU_Diode_1, CPU_Diode_2	Package temperature (Diode)	°C
CPU1_Temp, CPU2_Temp	Package temperature	°C
DIMM1_Temp,...,DIMM32_Temp	DIMMs temperature	°C
GPU_Temp_1,...,GPU_Temp_4	GPU temperature	°C
Mem_Buf_Temp_1,...,Mem_Buf_Temp_8	Memory temperature (Centaur)	°C
CPU_VDD_Curr	CPU current	A
Fan_1,...,Fan_4	Fan speed	RPM
CPU_VDD_Volt	CPU Voltage	V
Fan_Power	Fan power	W
GPU_Power	GPU power	W
Mem_Cache_Power	Memory power (Centaur)	W
Mem_Proc0_Pwr, Mem_Proc1_Pwr	DIMMs power	W
PCIE_Proc0_Pwr, PCIE_Proc1_Power	PCIExpress power	W
Proc0_Power, Proc1_Power	CPU Power	W
System_Power	Node total power	W

PSU Metrics

Power Supply Units

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Metric Name	Description	Unit
LITEON-PSC-MIB::pscBatteryCapacity_0	Battery capacity	%
LITEON-PSC-MIB::pscBatteryPower_0	Battery power	W
LITEON-PSC-MIB::pscBatteryTemp_0	Battery temperature	°C
LITEON-PSC-MIB::pscBatteryVolt_0	Battery voltage	V
LITEON-PSC-MIB::pscPSOutputCurr_0	Power Supply Output current	A
LITEON-PSC-MIB::pscPSOutputEfficiency_0	Power Supply Output efficiency	%
LITEON-PSC-MIB::pscPSOutputLoad_0	Power Supply Output load	%
LITEON-PSC-MIB::pscPSOutputPower_0	Power Supply Output power	W
LITEON-PSC-MIB::pscPSOutputVolt_0	Power Supply Output voltage	V
LITEON-PSC-MIB::pscPSPhaseRInputCurr_1	Power Supply phase R Input current	A
LITEON-PSC-MIB::pscPSPhaseRInputFreq_1	Power Supply phase R Input frequency	Hz
LITEON-PSC-MIB::pscPSPhaseRInputPower_1	Power Supply phase R Input power	W
LITEON-PSC-MIB::pscPSPhaseRInputVolt_1	Power Supply phase R Input voltage	V
LITEON-PSC-MIB::pscPSPhaseSInputCurr_1	Power Supply phase S Input current	A
LITEON-PSC-MIB::pscPSPhaseSInputFreq_1	Power Supply phase S Input frequency	Hz
LITEON-PSC-MIB::pscPSPhaseSInputPower_1	Power Supply phase S Input power	W
LITEON-PSC-MIB::pscPSPhaseSInputVolt_1	Power Supply phase S Input voltage	V
LITEON-PSC-MIB::pscPSPhaseTInputCurr_1	Power Supply phase T Input current	A
LITEON-PSC-MIB::pscPSPhaseTInputFreq_1	Power Supply phase T Input frequency	Hz
LITEON-PSC-MIB::pscPSPhaseTInputPower_1	Power Supply phase T Input power	W
LITEON-PSC-MIB::pscPSPhaseTInputVolt_1	Power Supply phase T Input voltage	V

Amester (OCC) Metrics

Metric Name	Occ	Cmp	Id	Description	Unit
CMBW_P0	1,2	CORE	0,..,11	Average Memory Band width for core <id> on this processor	GBs
CUR_VDD0	1,2	PROC	0	Current consumption at Processor's Vdd Regulator Output	A
FREQ_P0	1,2	CORE	0,..,11	Requested frequency from OCC for Core <id>	MHz
FREQ_P0	1,2	PROC	0	Average of all core frequencies for Processor	MHz
FREQA_P0	1,2	CORE	0,..,11	Average/actual frequency for this processor, Core <id> based on OCA data	MHz
IPS_P0	1,2	CORE	0,..,11	Instructions per second for core <id> on this Processor	MIP
IPS_P0	2	PROC	0	Vector sensor that takes the average of all the cores in Processor	MIP
M4RD_MEM	1,2	MEM	0,..,11	Memory cached (L4) read requests per sec for Processor's MC <occ>, Centaur <id> (MBA01/MBA23)	GBs
M4WR_MEM	1,2	MEM	0,..,11	Memory cached (L4) write requests per sec for Processor's MC <occ>, Centaur <id> (MBA01/MBA23)	GBs
MRD_P0	1,2	MEM	0,1,4,5	Memory read requests per sec for Processor's Memory Controller <id>	GBs
MWR_P0	1,2	MEM	0,1,4,5	Memory write requests per sec for Processor's Memory Controller <id>	GBs
NOTBZE_P0	1,2	CORE	0,..,11	Not Busy (stall) cycles counter for core <id> on this Processor	cyc
NOTFIN_P0	1,2	CORE	0,..,11	Not Finished (stall) cycles counter for core <id> on this Processor	cyc
PWR_APSS	1	SYS	1,..,15	Power Provided by APSS channel <id>	W
PWR_FAN	1	SYS	0	Power consumption of the system fans - Master only sensor	W
PWR_GPU	1	SYS	0	Power consumption of the GPU	W
PWR_IO	1	SYS	0	Power consumption of the IO subsystem (including storage digital 5Volt or less rail) - Master only sensor	W

PWR_MEM	1,2	PROC	0	Power consumption for Memory for this Processor	W
PWR_null	1	SYS	0	Bulk power of the system - Master only sensor	W
PWR_P0	1,2	PROC	0	Power consumption for this Processor	W
PWR_STORE	1	SYS	0	Power consumption of the storage subsystem (storage 12Volt rail) - Master only sensor	W
PWR_VCS0	1,2	PROC	0	Power consumption for this Processor's Vcs Regulator Input (12Volt)	W
PWR_VDD0	1,2	PROC	0	Power consumption for this Processor's Vdd Regulator Input (12Volt)	W
PWRPX_P0	1,2	CORE	0,..,11	Power Proxy sensor for Core <id> on this processor	W
SLEEPCNT_P0	1,2	PROC	0	Count the number of cores that are sleep in this processor	Num
TEMP_P0	1,2	CORE	0,..,11	Average temperature of DTS sensors for Processor's Core <id>: 100% corresponds to nominal frequency	C
TEMP_P0	1,2	PROC	0	Vector sensor that is the average of all core temperatures for this Processor	C
TEMP_P0PEAK	1,2	PROC	0	Vector sensor that is the peak of all core temperatures for this Processor	C
UTIL_P0	1,2	CORE	0,..,11	Utilization of this Processor's Core <id> (where 100% = fully utilized): NOTE: per thread HW counters are combined as appropriate to give this core level utilization sensor	Per
UTIL_P0	1,2	PROC	0	Average of all core utilizations for this Processor (where 100% = fully utilized)	Per
VOLT_V0	1,2	VRM	0	Voltage request for this Processor's Vdd voltage rail	mV
VOLT_V1	1,2	VRM	0	Voltage request for this Processor's Vcs voltage rail	mV
WINKCNT_P0	1,2	PROC	0	Count the number of cores that are winkled in this processor	Num