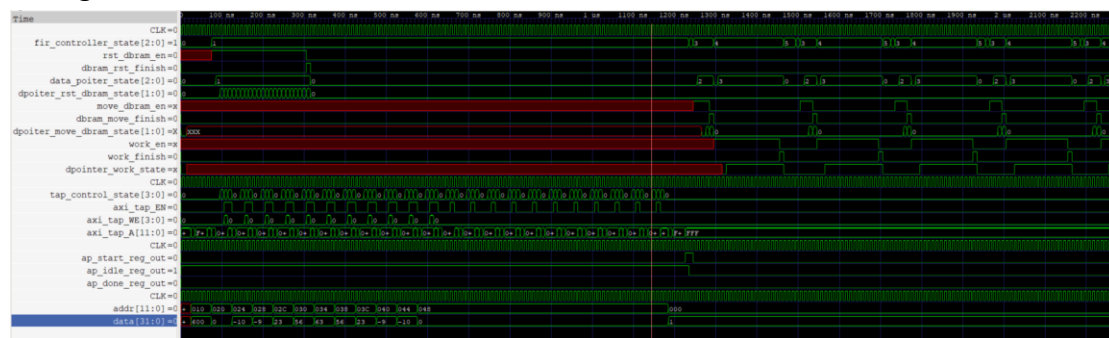


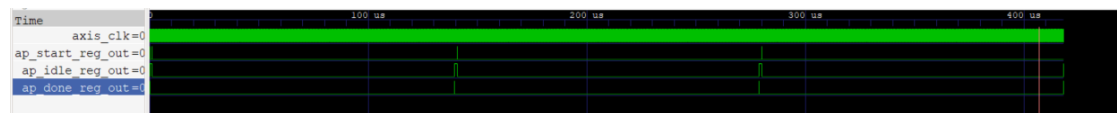
➤ Configuration write



➤ ap_start, ap_done

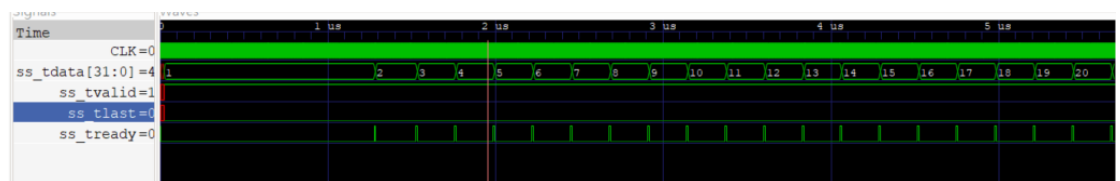
測三次的圖。

measure # of clock cycles from ap_start to ap_done: $(278585\text{ns} - 140575\text{ns}) / 10 = 13801$ clock cycles



➤ Xn stream-in, and Yn stream-out

1. Data-in stream-in



2. Data-out stream-out

