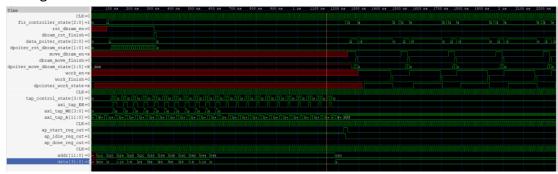
- ▶ 我把大部分截圖與 Report 放一起,這邊是放有何外說明需要在 github 附上的部份。
- Configuration write



ap\_start, ap\_done

測三次的圖。

measure # of clock cycles from ap\_start to ap\_done: (278585ns-140575ns)/10=13801 clock cycles



- > Xn stream-in, and Yn stream-out
  - 1. Data-in stream-in



2. Data-out stream-out

