PLACEMENT AND INTERNSHIP EXPERIENCES

**Chennupati Mahathy
Synopsys**

COMPANY REQUIREMENTS

ELIGIBLE BRANCHES

BTech ECE, EEE

CG CUTOFF

>=6.5CGPA, 10th and 12th >=70% or >=7CGPA

SELECTION PROCEDURE

OT, Two rounds of technical interviews

INTERN EXPERIENCE TALK

BRANCH :-

Electrical and Electronics Engineering

INTERNSHIP SEASON :-

6 Months intern +FTE

DESCRIPTION OF ROLE :-

ASIC Digital Design Engineer

JOB LOCATION :-

Hyderabad

DESCRIPTION OF ONLINE TEST

Mostly Digital Electronics, Verilog language, very basic of Analog Electronics

DESCRIPTION OF TECHNICAL INTERVIEW

I was asked to write the basic flow of code in the projects i put in my resume. Also, i was asked some questions from STA and other basic questions from digital electronics.

DESCRIPTION OF HR ROUND

NA

Resources

Samir Palnitkar book for verilog, Morris mano book for Digital Electronics, Unacademy crash courses for analog