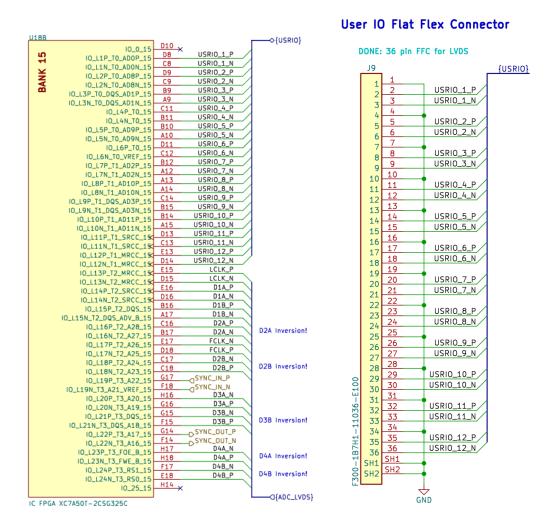
Hi all,

It's been three months since the end of the campaign, oh how time flies! I've had to dig back a bit to try to cover everything, but I think this is everything up to now.

From XDMA to LiteX, a few months early!

Thanks to <u>@NateMeyer</u>'s diligent work on the LitePCIe <u>Windows/Linux</u> drivers and <u>library</u>, the project is switching over to the LiteX gateware a lot earlier than anticipated! This came just in time for the Rev 5 design to drop DDR3 completely, instead of designing in DDR3 memory for the dev edition and then later depopulating it for production.

Now you may be asking "but isn't DDR3 supposed to be the benefit of a dev edition unit?". That was the idea, but there really isn't much you can do with DDR3 other than store the samples before transfer. Since this is never necessary with the LitePCle stack (and a fast enough host), we decided to add something to make these units live up to the "dev edition" name:



We've exposed 12 LVDS lanes on a 36 pin flat flex connector, all capable of being used standalone or clocked in/out synchronously with the ADC samples!

If you are interested in using ThunderScope as a DAQ system, this provides you a path to move to a custom ADC and frontend while still using our streaming architecture and software stack. It doesn't even need to be an ADC per se, many high-speed-image-sensors have parallel LVDS out and generate a LOT of data to stream to a host.

There's been some interest in MSO functionality for ThunderScope as well. This was out of scope for the main design, but with synchronous sampling to the ADC it should be possible to design a logic analyzer pod to use this expansion connector.

Another interesting possibility would be to connect this connector to the LVDS connector on a <u>Glasgow revC</u> and have sample by sample control over its inputs and outputs, which could be used for anything requiring precision timing and a lot of IO at voltage levels other than the 2.5V the expansion connector uses.

These are just a few ideas we've had for applications using this connector, we're looking forward to seeing how you all use it! And yes, "you all" - since something this cool shouldn't be limited to just the dev units, we're adding it to all ThunderScopes we ship!

"I'll make the switch to KiCad after I deliver all the campaign units, I promise."

Oops, I guess I broke that promise! Due to some delays in production and shipping, I had a few weeks of "hurry up and wait" ahead of me, so I decided to give the KiCad Altium import a go and see how hard it would be to port the design over. And it was... A lot of work, but I'm really happy that the project is fully switched over to KiCad now! I have some more thoughts on making the switch over on <u>mastodon</u>, but you can just check out the <u>schematic</u> if you want to skip the rant.

ThunderScope's new look - a thrilling conclusion to the anti-slip saga

The compression moulded parts came back and they look great, fit great and stop the thing from slipping - A shockingly good result for my first custom moulded part!



I also redesigned the end caps, which now have much sturdier probe compensation terminals, an expansion connector slot, as well as a grounding lug and maximum input voltage warnings.



A non-Intel option for PCIe tunneling via USB4?

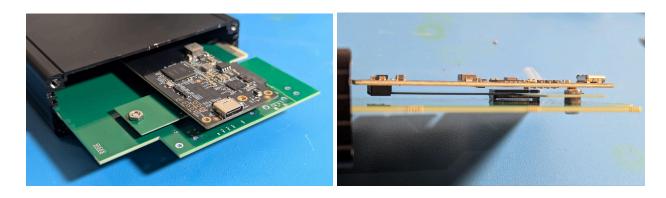
It exists and it's called the ASM2464PD! I got nerd sniped and started <u>reverse engineering</u> a board based on it, only to realize that it doesn't work with any of my TBT3 hosts. Seems to be a known issue with these parts unfortunately, so I've ruled out using it to avoid some very confused support calls. "No, it works with TBT3 hosts, but only newer TBT3 hosts" - statements made by the utterly deranged.

Multi-Sourcing Adaptors

With no viable alternative to the Intel based adaptors, I decided to take another look at sourcing them in bulk for production and found a better vendor than the one I initially bought 100 from. I ordered some samples from them and quickly found a problem. These adaptors have a different spacing from USB connector to M.2 mounting hole than the existing ones, so our existing method of mounting it to the mainboard will not work with both at once.

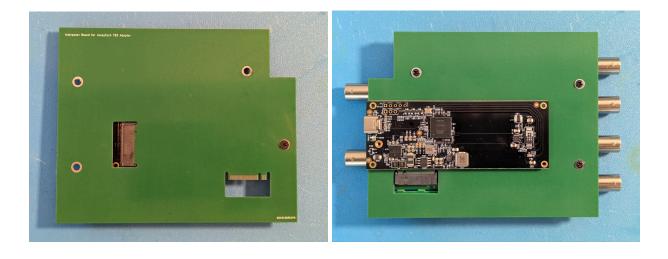


To address this, I moved all the adaptor dependent mounting dimensions to the interposer board, so that every adaptor would need a new interposer but the mainboard would stay the same.

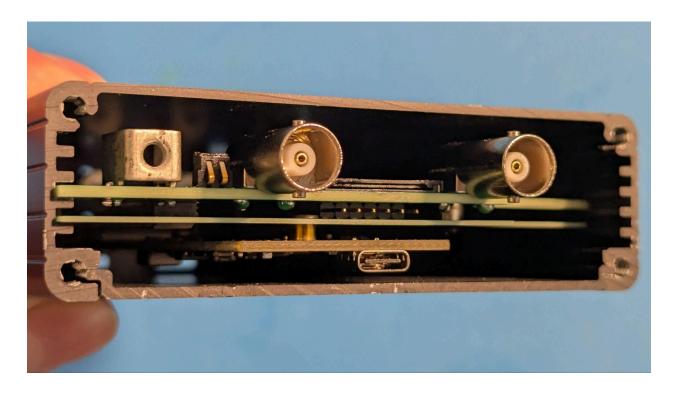


It worked, but I wasn't happy with the amount of flex the solution would have in the vertical direction (it looks like a diving board and acts much like one too). It would also impart some of the force of inserting and removing a USB C cable onto the M.2 connector, which it isn't really made for.

For the next rev, I made the interposer board the same size as the main board, so that it can sit on the board guides of the extrusion and rest against the endcaps. The assembly process here is to connect the thunderbolt adapter to one of the interposer's M.2 edge connectors, screw it into place, then connect the other edge connector to the main board's M.2 connector and screw the interposer into the mainboard standoffs. This secures the whole PCB sandwich together.

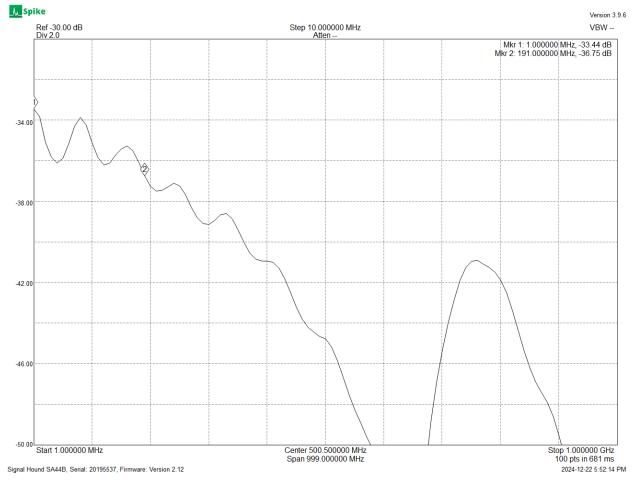


And oh boy, does it end up looking very sandwich-ey! It's super sturdy though, and I'm sure the front end won't mind the extra shielding from the thunderbolt adaptor.

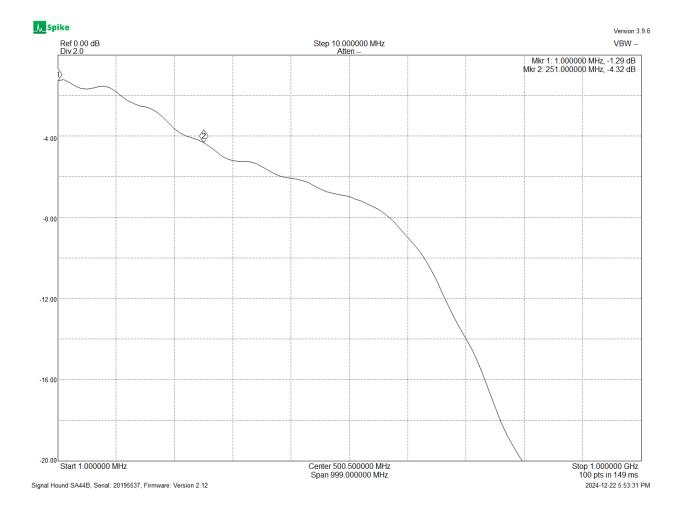


More Front-End Testing

I spun up another test board with the attenuator fix from last update and ground plane cutouts to reduce the capacitance on each path (I had them before, but removed them thinking it was part of how noise was getting into the signal). Everything looked good except the attenuator path, which didn't seem to appreciate the ground plane cutouts:



I'll remove the cutouts under the attenuator for Rev 5 and that should get us back to what we saw last update. The decrease in capacitance on the 1x path (below) is more important anyway, as the attenuator presents less capacitance than the buffer (when its response is not wiggling about like above).



PCI-SIG Membership: Step One for Signed Drivers

We're a PCI-SIG member company now! As a member, we get our <u>own unique VID</u>, which we need to have in order to get our drivers signed. The next step is to get our own EV cert, and to join some Microsoft and Apple programs for Windows and macOS drivers.

Progress on Mac Support

Speaking of macOS, <u>@NateMeyer</u> has been working on getting the <u>mac driver</u> up and running as well, while <u>@macaba</u> has been playing around with TS.NET on the Apple M4 processor, with blazing fast results.

Sourcing

- Sourced 1000 Extrusions from Hammond Manufacturing
- Sourced 2000 Silicone Bezels from RevPart
- Sourced all of the campaign probe orders from Testec plus overage (270 kits)
- Sourced 450 Pelican 1120 cases from Cases Unlimited

- Getting first articles from our foam vendor soon
- Foam will be provided along with cardstock insert, quick start guide and case sticker to Cases Unlimited, who will be assembling the case interiors

Support for Software Development

- Made our first donation of \$5k to <u>ngscopeclient</u>
- Contracted with <u>@whitequark</u> to complete thunderscope-rs. Catherine will start on this shortly.
- Supplied all primary devs with <u>mac minis</u> and <u>function generators</u> for work on mac support and ThunderScope calibration

Woof, I'll be doing these updates on a monthly schedule from now on to avoid a huge pile-up of topics like this one. Thanks for keeping up with the project and I hope to have some more good news next time!

-Aleksa