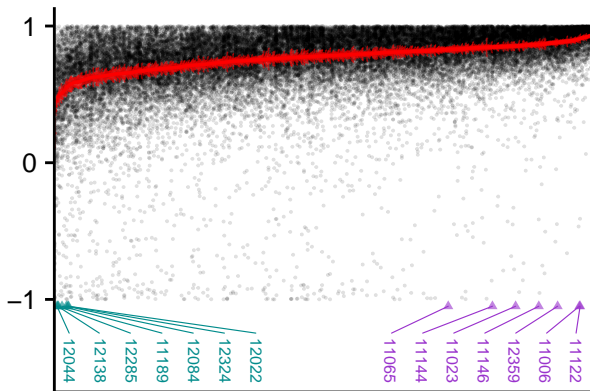


**a.**

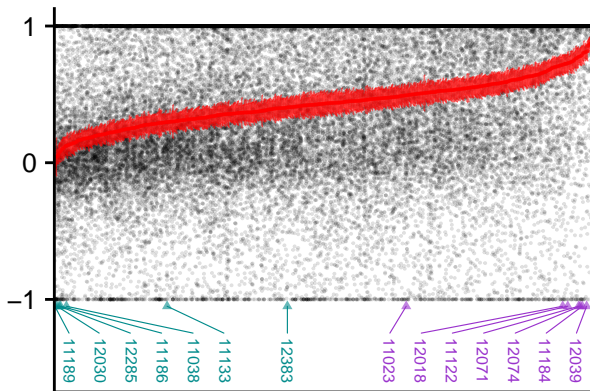
Learning PI



RILs

**b.**

Memory PI



RILs