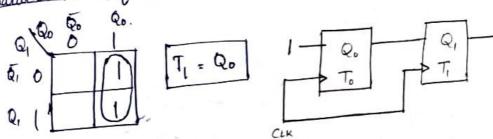
## 2-bit Synchronous Counter: (7-flip-flop).

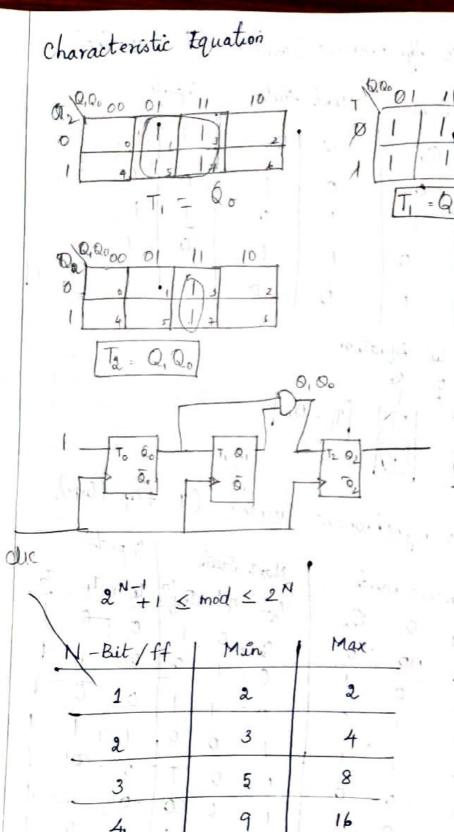
Present	State	Next	State	înpu	to	•
Q,	Q.	Q,	00	T,	To	
0	0	0	1	0	1	
D	l	1	٥	1	1	
1	0	1	1	0	1	
1	1	0	0	111	1	7.1

Characteristic Equation:



3-bit Synchronous Counter: (T-flup flop)

	0.1	1	Next State   Inputs	
Pres	ent St	ate	1 + + 0 + 1 Ti Ti	
Q,	Q,	Q.	Q2 Q, Q6 0 0 1	
•	0	0	100011	
0	D		0 1 20 0 1	
0		0		
0	į	1	1 0 0 3 1	
	D	0	1 0 50 1 1	
ì	0	e) }	1 1 60 0 1	
1	(	0	00 +1 11	
1	(	(		



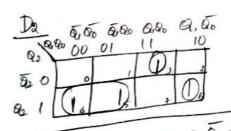
Design a mod 5. Synchronous Counter using D. flip flop.

			/							
Present State			Next State			9nputs				
	02	Q,	Qo	Q2+	Q,+	Qo+	Da	D,	Do	1 ,
	0	D	0	D	0	1 -	0	0	1	
	0	0	1	0	l	0	0	1	D	
	0	1	0	0	1 ;	1	0		_1	
	D	1	1	1	0	0	t	0	0	
	1	0	0	1	0	1.	1	0	1	
	1	0	1	1	1	0	t	1	0	
		(	0	į į	1	. 1	. 1	l	1	
	1	l	1	D	0	0	0	0 K-m	0 ap -	Present &
										29.0

 $\mathcal{D}_0 = \overline{\mathcal{Q}_0}$ 

1	1000 1000	01	, Q, Q. [[	Q,Q0
& 0	6		<b>4</b> 2	(1)
Q 1.	41	U,	7	U6

$$D_1 = \overline{Q_1}Q_0 + \overline{Q_1}Q_0'$$

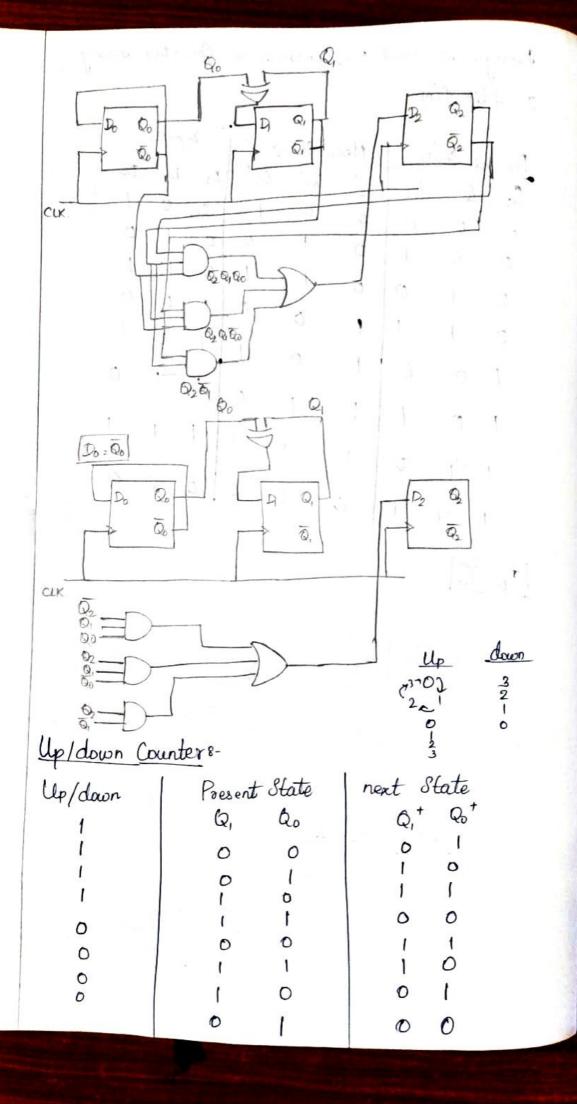


$$\mathcal{D}_{a} = \overline{Q_{a}Q_{1}Q_{0}} + \overline{Q_{a}Q_{1}Q_{0}} + \overline{Q_{2}Q_{1}}$$

$$Q_{1}(\bar{Q_{2}}Q_{0}+Q_{2}\bar{Q_{0}})$$
 $Q_{1}(\bar{Q_{2}}BQ_{0})+Q_{2}\bar{Q_{1}}$ 

$$0_2$$
 $0_0$ 
 $0_1$ 
 $0_1$ 
 $0_2$ 

ō,



3-bit Sync Counter Using JK Flip Flop:-Present State | Next State | Inputs X Inputs Present State Next State Qo+ J, Ja ka Q,+ Q,+ 00 Q, Qa 0 0 0 X 0 0 0 X X X 0 0 0 0 X X 0 Ó 0 o X 0 0 0 XX. D 0 X X X 0 0 0 ko\_ Q Q10000 01 11 K0 = 1 Jo = 1 Q, Qo

