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## TECHNICAL SKILLS

Programming in Java

Intermediate level - Python , Verilog

Frontend Development

## LANGUAGES

English  
Full Professional Proficiency

French  
Elementary Proficiency

## SOFT SKILLS

Problem solving

Critical thinking

Adaptability

Collaboration

# Suprita T

## Pre-final year student

I am an aspiring Software Developer and VLSI Engineer with a strong background in Digital Design. I have good knowledge of Java and Verilog and am currently learning Python and JavaScript.

## EDUCATION

### University

SRM Institute of Science and Technology

09/2022 - 05/2026

Tiruchirapalli, India

Courses

- BTech - Electronics and Communcation
- CGPA - 9.80

### High School

Montfort School

06/2016 - 05/2022

Tiruchirapalli, India

Percentage

- Class 10 - 91 %
- Class 12 - 85 %

## INTERNSHIP

### Intern

VI MICROSYSTEM

12/2023 - 12/2023

Chennai, India

Embedded Systems Intern

- Acquired knowledge and hands-on experience in embedded systems and basic concepts of the Internet of Things (IoT).

### Intern

NIT T - National Institute Of Science and Technology, Tiruchirappali

06/2024 - 07/2024

Trichy, India

Digital Design Circuits using Verilog

- Gained knowledge in digital circuit design and Verilog programming while developing basic digital circuits to enhance my understanding of logic design.

### Fellowship

Samsung under ISWDP - Cohort 4

02/2025 - Present

Tasks

- Selected for hands-on training in semiconductor devices under the Industry Skill Workforce Development Program (ISWDP) by Samsung.

### Intern

Infosys Springboard

Angular Web Development

- I am developing a health monitoring web application using Angular.

## CERTIFICATES

INFOSYS

Frontend Development



## CERTIFICATES

NPTEL

*Digital System Design*

SCALAR

*Java*

CODECHEF

*Java, Frontend Development*



## ACHIEVEMENTS

EPICS in IEEE Grant Awardee (01/2025 - Present)

*Sign Language-Based Multimodal Aid for Special Children, was selected for **EPICS IEEE** and received \$986 in funding*