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CMR INSTITUTE OF TECHNOLOGY, BENGALURU.

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CMR Institute of Technology, Bengaluru
Department of ECE Organizing

5 Days Online Hands-On FDP on

"Mastering Cadence Tools for Next-Generation Semiconductor Design"

Important Dates:

**January 06, 2025, to January 10, 2025
(6PM – 9PM Everyday)**

About the Program

The 5-Day Online Hands-On FDP on "Mastering Cadence Tools for Next-Generation Semiconductor Design" is focused on providing participants with in-depth training and practical experience in using Cadence tools, a leading platform for designing cutting-edge semiconductor technologies. This program aims to bridge the gap between theoretical semiconductor design concepts and real-world applications by enabling participants to master tools essential for next-generation electronic systems design. Through live demonstrations and hands-on sessions, attendees will gain knowledge in designing, simulating, and verifying semiconductor devices. The program is specifically tailored for faculty, research scholars, and industry professionals looking to enhance their skills in electronic design automation (EDA) for modern semiconductor technologies.

cādence™

Registration Starts: 01-11-2024
Registration Ends: 05-01-2025

Payment

Registration Amount: Rs.500/-

***Participants need to pay the registration amount using the following details**

Account Details

Beneficiary Name: CMRIT HOD EC Dept.
Account Number: 843410110003987
Bank: Bank of India
Branch: AECS Layout, Bangalore-560037
IFSC Code: BKID0008434

Patrons
Shri K. C. Ramamurthy
Chairman, CMRGI
Dr. K. C. Sabitha Ramamurthy
President, CMRJT
Shri. K.R. Jayadeep
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HOD, Department of ECE

Conveners
Dr. Venkatesh M
Assistant Professor, ECE, CMRIT
Dr. Parthasarathy P
Assistant Professor, ECE, CMRIT

Point of Contact
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About the Institution
CMR Institute of Technology (CMRIT) is a premier Engineering institution affiliated with Visvesvaraya Technological University, Belagavi, approved by AICTE, New Delhi and recognized by the Government of Karnataka. Recently accredited by NAAC with an A++ grade. The institution was established in the year 2000 by CMR Jnanadhara Trust.

About the Department
The Department of Electronics and Communication Engineering at CMRIT came into existence in the year 2000. The department has well-qualified and experienced faculties dedicated to teaching and research. The department has a recognized Research Centre affiliated with Visvesvaraya Technological University, Belagavi. The Department is actively involved in research and has also been recognized as a Centre of Excellence, Audio & Video under VGST and Govt. of Karnataka. The department has been accredited by the National Board of Accreditation for three years.

Speakers
Experts are from IITs, VITs, Industry and Foreign Universities

Session Details

Day	Time	Session Topic
06-01-2025	6:00 – 7:30	Introduction to Semiconductor Design with Cadence Tools
	7:30 – 9:00	Designing Analog Circuits with Cadence Virtuoso
07-01-2025	6:00 – 7:30	Inverter design from schematic to post layout simulation
	7:30 – 9:00	Designing Low-Noise RF Circuits with Cadence
08-01-2025	6:00 – 7:30	GaN Technology & its application in automotive industry
	7:30 – 9:00	Ring VCO and LC VCO Design
09-01-2025	6:00 – 7:30	LNA Design Theory
	7:30 – 9:00	LNA Design Practical
10-01-2025	6:00 – 7:30	Effective DRC and LVS Verification in Semiconductor Designs
	7:30 – 9:00	Machine Learning & Nanoscale Semiconductor Device Modelling