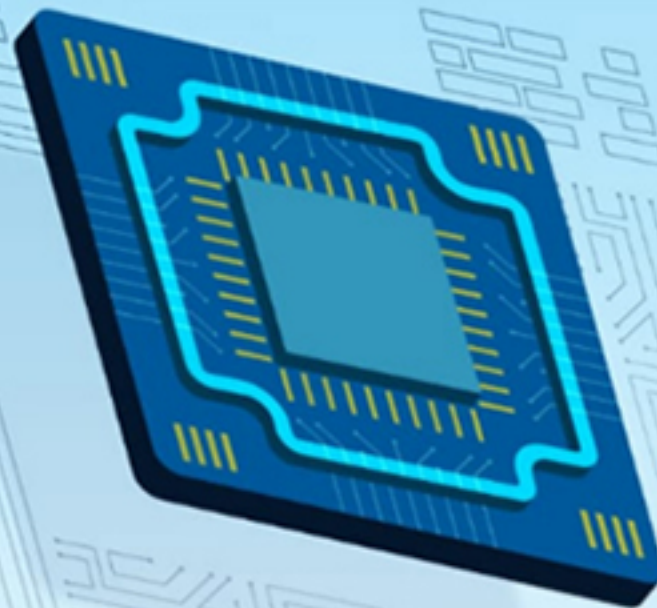




## Digital India RISC-V (DIR-V) Symposium - 2025

Be a part of India's journey toward semiconductor innovation and excellence!



### About the Symposium

The Digital India RISC-V Symposium, hosted by IIT Madras, is a two-day event celebrating India's journey into the future of electronics through the RISC-V pathway.

Open to All  
Limited Seats

Rs:  
**500/-**

**Register Now**

### Event Highlights

- Tech Talks by industry leaders and academicians.
- Interactive Exhibitions featuring indigenous RISC-V processors.
- Hackathon to showcase innovative RISC-V-based solutions.
- Networking Opportunities with industry experts and investors.

**02<sup>nd</sup>-03<sup>rd</sup> March 2025** | **09.00 am**  
Onwards IST

### Venue:

IITM Research Park, Chennai

<https://dirv.iitm.ac.in/>

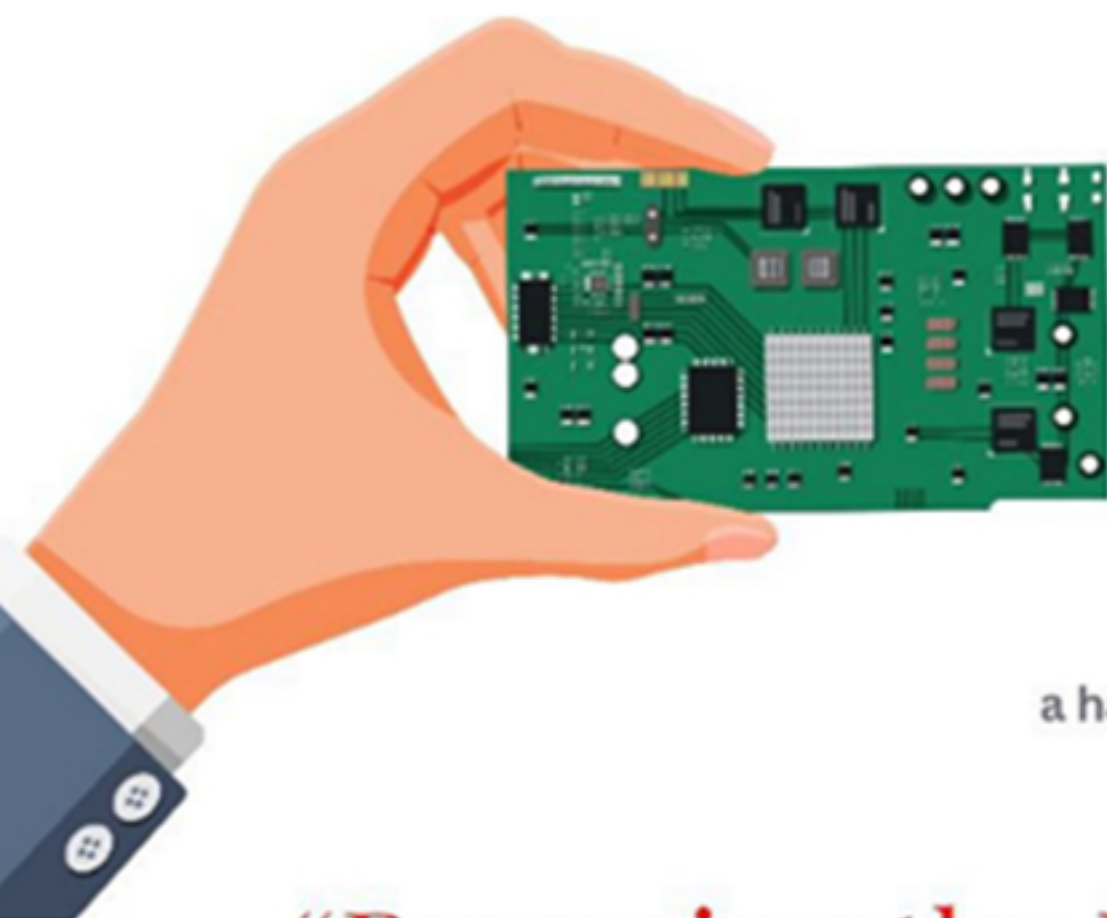
**SCAN ME!**







इलेक्ट्रॉनिक्स एवं  
सूचना प्रौद्योगिकी विभाग  
MINISTRY OF  
ELECTRONICS AND  
INFORMATION TECHNOLOGY



# 2nd Digital India RISC-V (DIR-V) Roadshow 2025

a hands-on workshop series designed to give you practical exposure to  
the world of RISC-V and empower your hardware design skills

## “Powering the Future of Bharat, through Semiconductors”

### Why Attend?

- Gain hands-on experience with RISC-V technology and development boards.
- Learn practical design skills using industry tools.
- Network with industry experts and innovators.
- Discover career pathways in VLSI and semiconductor design.



**Mansar Building, IIT Jammu.**  
**09:00 AM Onwards**

### Event Sponsors







MINISTRY OF  
ELECTRONICS &  
INFORMATION TECHNOLOGY  
GOVERNMENT OF INDIA



# 2nd Digital India RISC-V (DIR-V) Symposium

## Roadshow

IIT Jammu  
27 Jan 2025

IIT Gujarat  
16 Jan 2025

IIT Bhubaneswar  
14 Feb 2025

IIT Madras  
2 Mar 2025