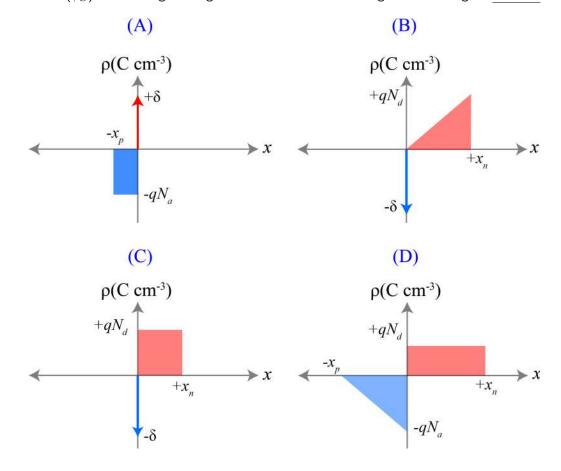
ISD 2023 - Week 6 Assignment

There are 10 questions for a total of 20 marks.

- 1. (2 marks) Which of the following is defined as the work function (ϕ_m) of a material?
 - A. The energy required to move an electron from E_c to the vacuum level.
 - B. The energy required to move an electron from E_v to the vacuum level.
 - C. The energy required to move an electron from $E_{\it F}$ to the vacuum level.
 - D. The energy required to move an electron from E_F to E_c .
 - E. The energy required to move an electron from E_F to E_v .

For Q2-Q3: Answer the following questions regarding the charge configurations in a Schottky diode.

2. (2 marks) Consider a Schottky diode made from a metal and a n-type semiconductor. The doping of the semiconductor follows an abrupt profile, and the work function of metal (ϕ_M) is greater than that of the semiconductor (ϕ_S) . The charge configuration of such a device is given in the figure

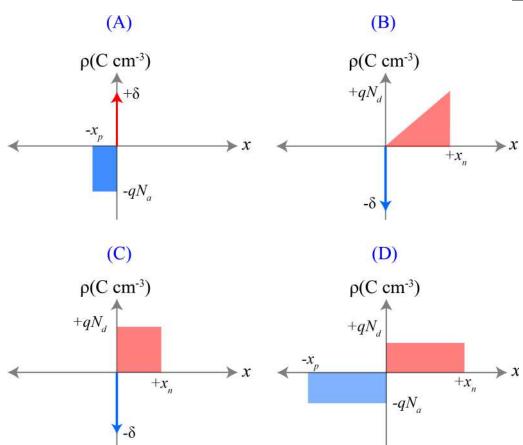


A. A

- B. B
- C. C
- D. D

Reflect and remember: It is important to understand the distinction between a one-sided junction and a Schottky barrier, though they possess many similar properties. An important point is that there is no field inside the metal, so there is a discontinuity in the electric field at the interface. This implies that there is a delta charge accumulation at the metal-semiconductor junction.

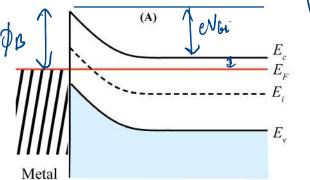
3. (2 marks) Consider a Schottky diode made from a metal and a n-type semiconductor. The doping of the semiconductor follows a linearly graded profile, and the work function of metal (ϕ_M) is greater than that of the semiconductor (ϕ_S) . The charge configuration of such a device is given in the figure _____.

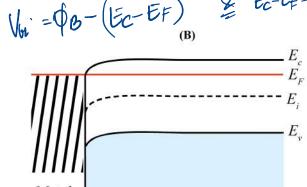


- A. A
- B. B
- C. C
- D. D

Reflect and remember: The doping of the semiconductor need not always have an abrupt profile. The charge configuration, and hence the electric field, of the Schottky diode will change according to the doping profile of the semiconductor. An important point to remember is that the procedure to determine the charge and field profiles is the same as the PN junctions. Students are encouraged to use Poisson's equation to determine the field profiles for the Schottky barrier and verify from the standard textbooks.

For Q4-Q8: The equilibrium energy band diagram for an ideal metal-semiconductor contact is shown in the figure below. Answer the following questions based on the band diagrams





4. (2 marks) The relation between metal and semiconductor work function, and the type of contact formed, for the band diagram in Figure A is _____.

- A. $\Phi_M < \Phi_s$, rectifying
- B. $\Phi_M < \Phi_s$, ohmic
- C. $\Phi_M > \Phi_s$, rectifying
- D. $\Phi_M > \Phi_s$, ohmic
- E. $\Phi_M < \Phi_s$, flatband

(2 marks) The relation between metal and semiconductor work function and the type of contact formed for the band diagram in Figure B is ______.

- A. $\Phi_M < \Phi_s$, ohmic
- B. $\Phi_M > \Phi_s$, rectifying
- C. $\Phi_M > \Phi_s$, ohmic
- D. $\Phi_M < \Phi_s$, rectifying

E.
$$\Phi_M < \Phi_s$$
, flatband

- 6. (2 marks) Copper is deposited on n-type silicon substrate to form an ideal Schottky diode. The work function of copper is $\Phi_m=4.65~eV$ and the electron affinity of silicon is $\chi=4.03~eV$. The Schottky barrier height (Φ_B) is eV.
 - $\phi_{\rm B} = \phi_{\rm m} \chi = 4.65 4.03 = 0.62eV$ A. 0.20
 - **B.** 0.62
 - C. 0.26
 - D. 4.65
 - E. 4.03
- 7. (2 marks) The built-in potential (V_{bi}) in the copper Schottky diode is V.

(Take $N_D=10^{16}~cm^{-3}$, $E_q=1.1~eV$, $n_i=10^{10}~cm^{-3}$, $\epsilon_s=11.8~and~kT=26~meV$.)

- $V_{Ai} = \phi_B (E_c E_F) = \phi_B [E_{g/2} (E_F E_i)]$ A. 0.76
- B. 0.62
- $= 0.62 \left[\frac{11}{2} \frac{1}{at} \ln \left(\frac{N_0}{n}\right)\right]$ C. 0.16
- $= 0.62 \left[0.55 0.026 \ln \left(\frac{10\%}{10\%}\right)\right] = 0.429 \vee$ D. 4.65
- **E.** 0.43

: Vm ~ 0.45V 8. (2 marks) For a zero applied voltage, the depletion width in the semiconductor due to the formation of a copper-Si Schottky junction is

- **A.** 0.237
- $W = \sqrt{\frac{2 \text{Esi Vin}}{9 \text{ND}}} = \sqrt{\frac{2 \times 11.8 \times 8.85 \times 10^{-14} \times 0.43}{16 \times 10^{-19} \times 10^{16}}} \times 0.43$
- B. 23.7 C. 46.4

= 2.369×10 mm

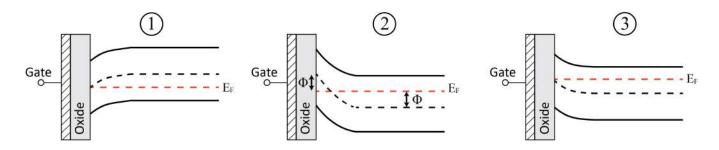
D. 0.464

Wa 0.237 MM

E. 0.361

Reflect and remember: The depletion width in a Schottky barrier will lie completely inside the semiconductor and has an inverse square root dependency on the carrier density.

For Q9-Q10: Simplified band diagrams for a MOSCAP are shown in the figure below. Match the energy band diagrams labelled from 1 o 3, with the corresponding substrate type, applied gate voltage V_g , and the bias condition.



- 9. (2 marks) For each of the band diagrams given in the figure, identify the type of MOSCAP (PMOS or NMOS) and the substrate used (p-type or n-type) for the MOSCAP.
 - A. $(1) \rightarrow p$ -type, PMOS
- $(2) \rightarrow p$ -type, PMOS
- $\textcircled{3} \rightarrow \mathsf{p}\text{-type}, \, \mathsf{PMOS}$
- $\text{B. } \textcircled{1} \rightarrow \text{n-type, NMOS} \qquad \textcircled{2} \rightarrow \text{n-type, NMOS} \qquad \textcircled{3} \rightarrow \text{n-type, NMOS}$
- $\text{C. } \textcircled{1} \rightarrow \text{n-type, NMOS} \qquad \textcircled{3} \rightarrow \text{n-type, PMOS} \qquad \textcircled{3} \rightarrow \text{p-type, PMOS}$
- $\textbf{D. }\textcircled{1}\rightarrow \textbf{p-type, NMOS} \qquad \textcircled{2}\rightarrow \textbf{n-type, PMOS} \qquad \textcircled{3}\rightarrow \textbf{n-type, PMOS}$
- $\hbox{E. (1)} \rightarrow \hbox{p-type, NMOS} \qquad \hbox{(2)} \rightarrow \hbox{n-type, PMOS} \qquad \hbox{(3)} \rightarrow \hbox{p-type, NMOS}$

Reflect and remember: An important point to note is that the type MOS capactor (PMOS or NMOS) determined by the type of inversion charge present at the interface. Another point to remember here is that we have drawn the band diagrams considering an ideal scenario with no defect or trap states in the oxide region.

In the upcoming week, we will relax a few of these assumptions and show how they will impact the band diagrams and the basic parameters of a MOSCAP.

- 10. (2 marks) Identify the sign of applied gate voltage (V_q) and the bias condition for each of the band diagram.
 - A. $\textcircled{1} \rightarrow \text{-ve}$, inversion $\textcircled{2} \rightarrow \text{-ve}$, accumulation $\textcircled{3} \rightarrow \text{+ve}$, accumulation
- $B. \ \textcircled{1} \rightarrow \text{-ve, accumulation} \qquad \ \textcircled{2} \rightarrow +\text{ve, accumulation} \qquad \ \textcircled{3} \rightarrow +\text{ve, inversion}$

- D. $\textcircled{1} \rightarrow + \text{ve}$, accumulation $\textcircled{2} \rightarrow \text{ve}$, accumulation $\textcircled{3} \rightarrow + \text{ve}$, accumulation
- $\textbf{E.} \quad \textcircled{1} \rightarrow + \textbf{ve, depletion} \qquad \textcircled{2} \rightarrow \textbf{ve, inversion} \qquad \textcircled{3} \rightarrow \textbf{ve, depletion}$