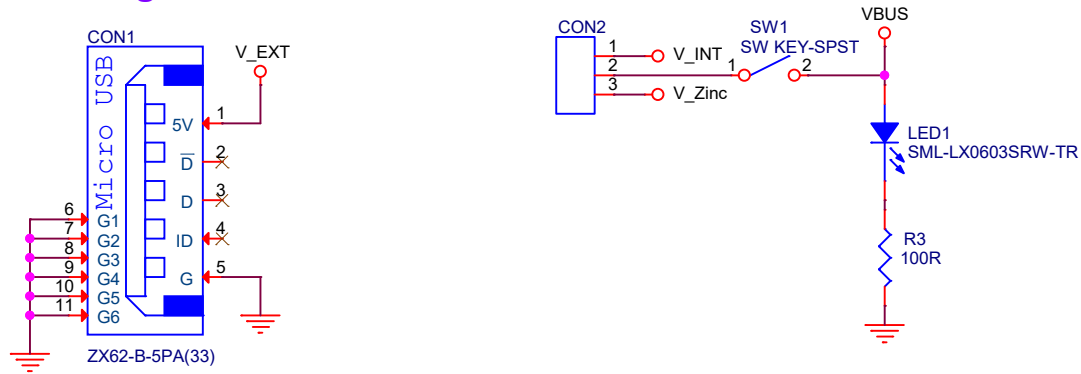
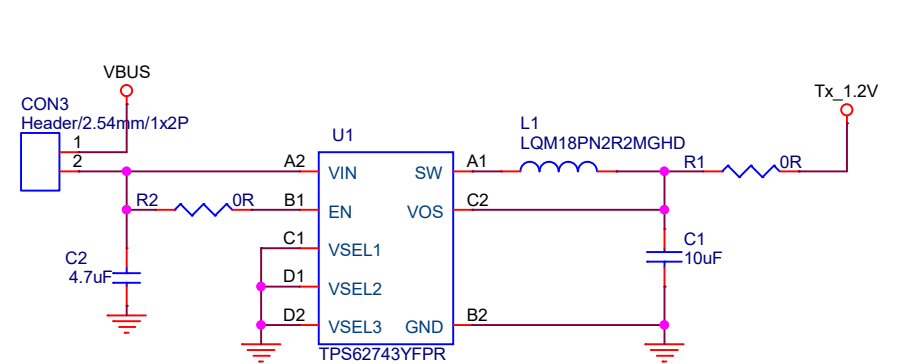


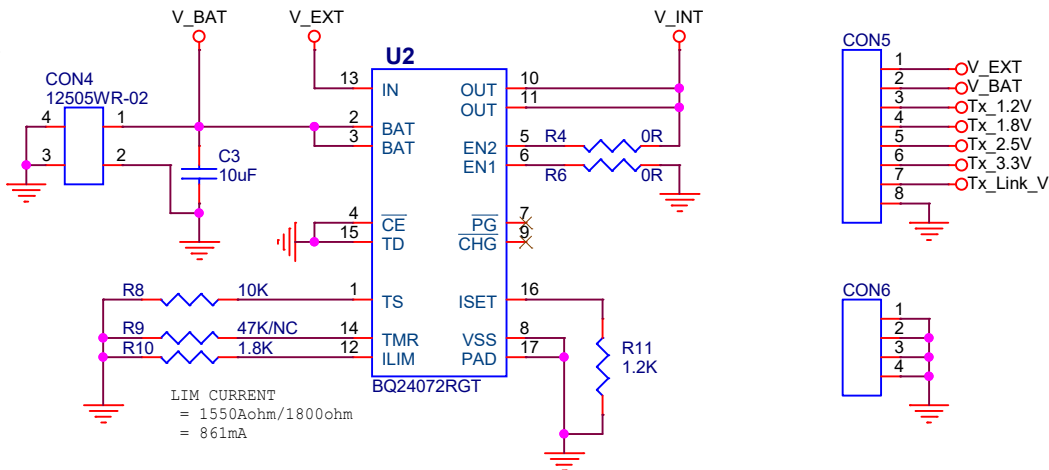
USB voltage source



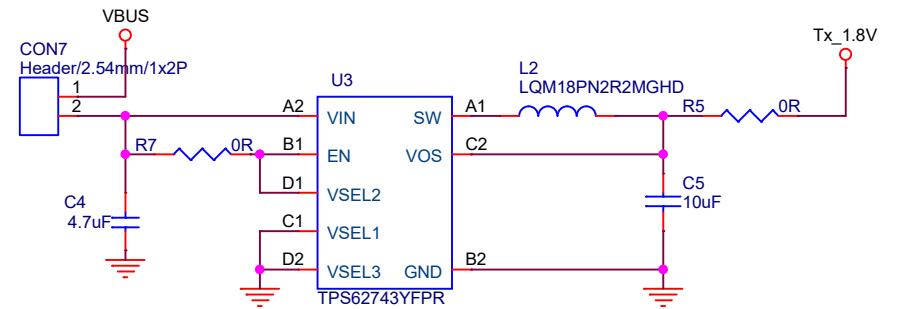
1.2V



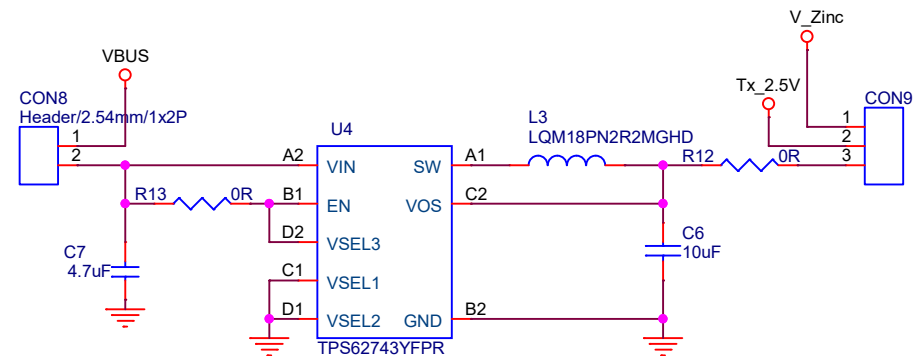
Charger



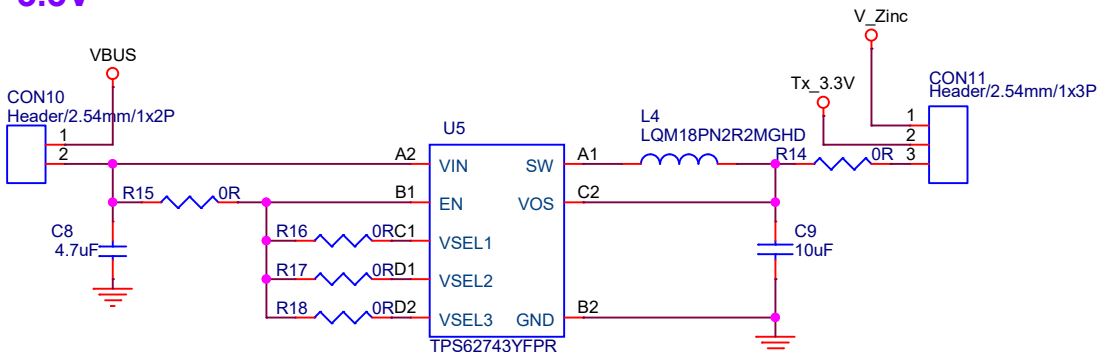
1.8V



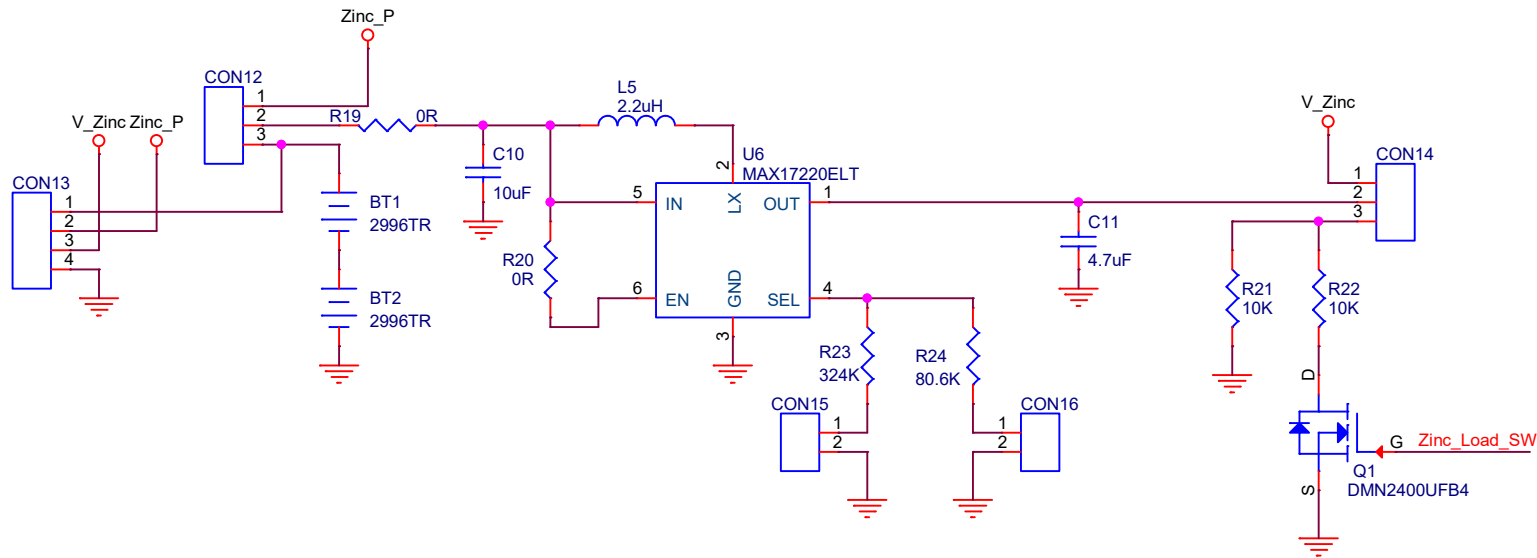
2.5V



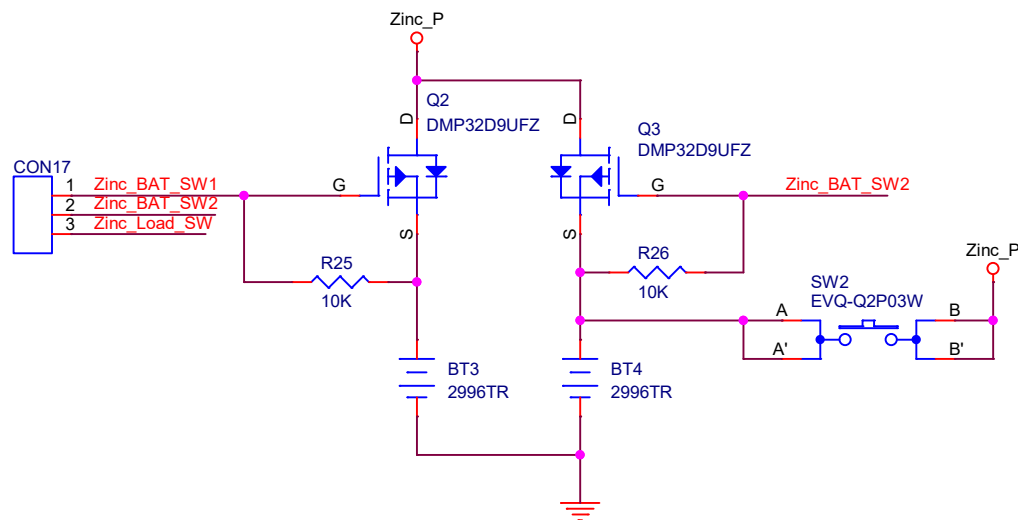
3.3V



Zinc-Air Series

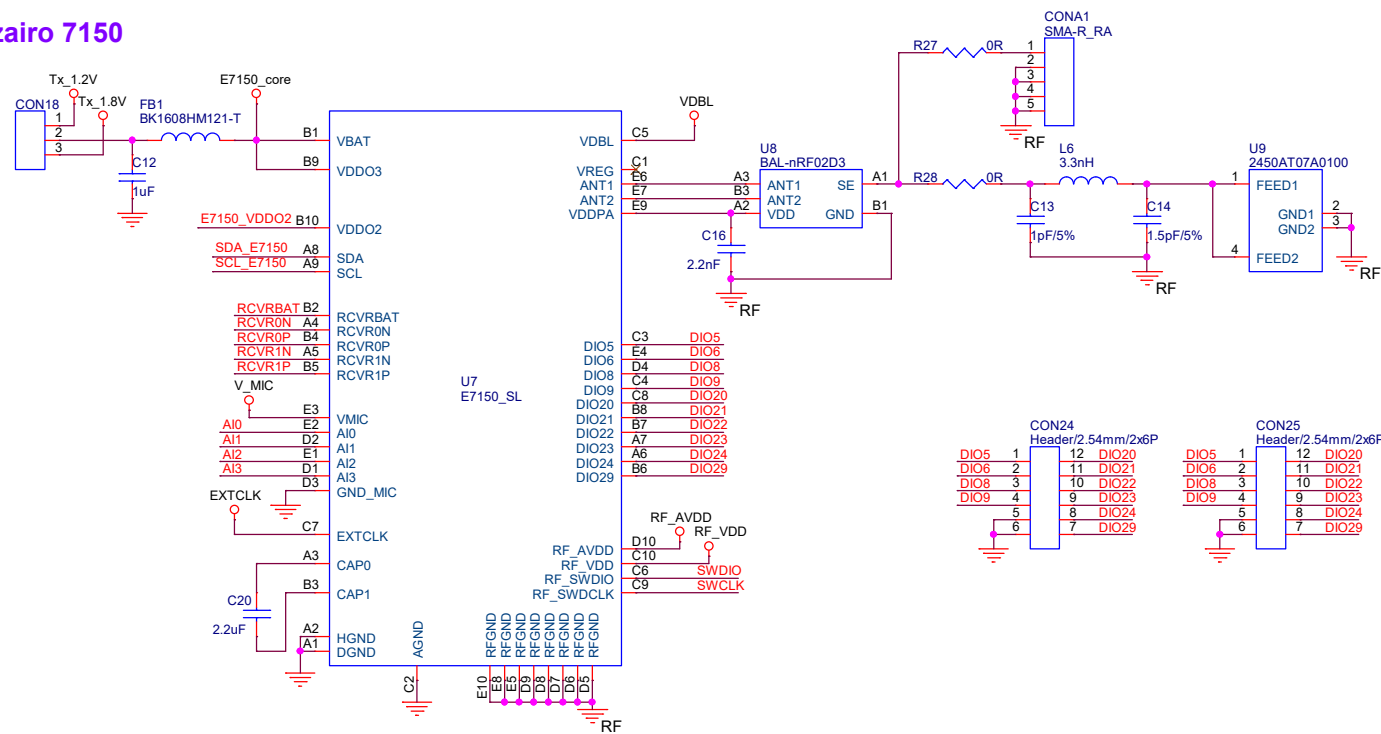


Zinc_Air Parallel

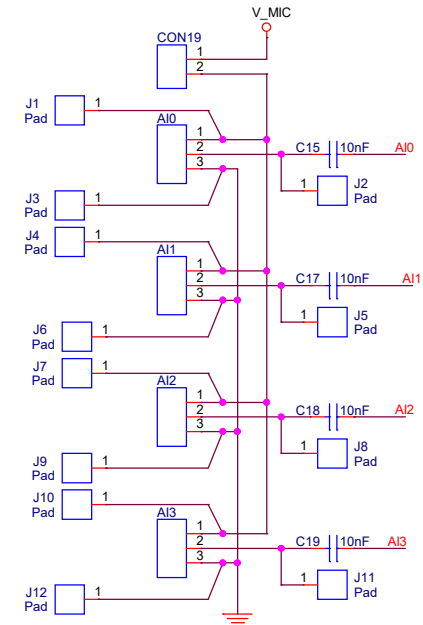


			Title			
			CI_General B'd			
T	O		Size	Document name	Rev	
D	O	C	A4	Tx_POWER_Zinc	0.0	
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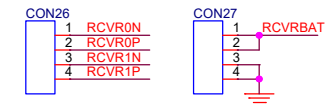
Ezairo 7150



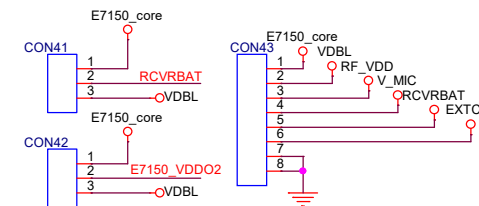
Analog input



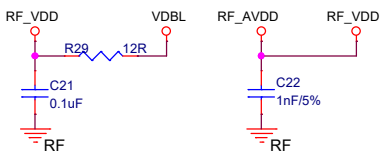
Analog output



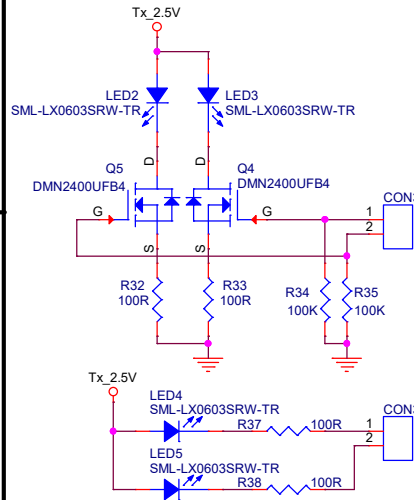
E7150 Voltage header



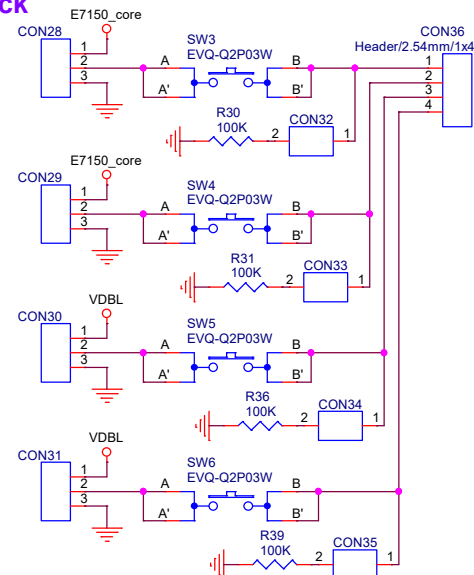
RF_Voltage



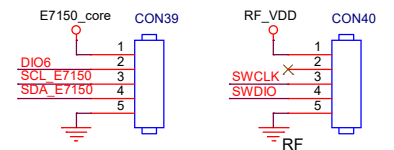
LED block



Switch block

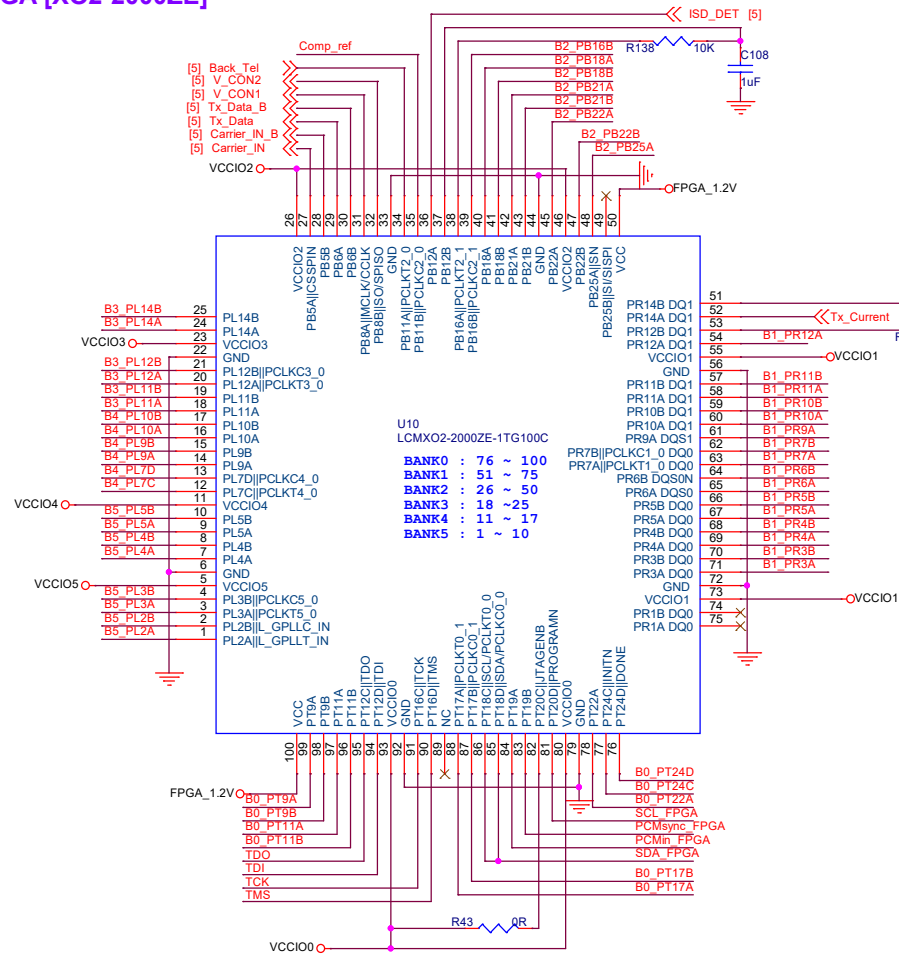


E7150 F/W

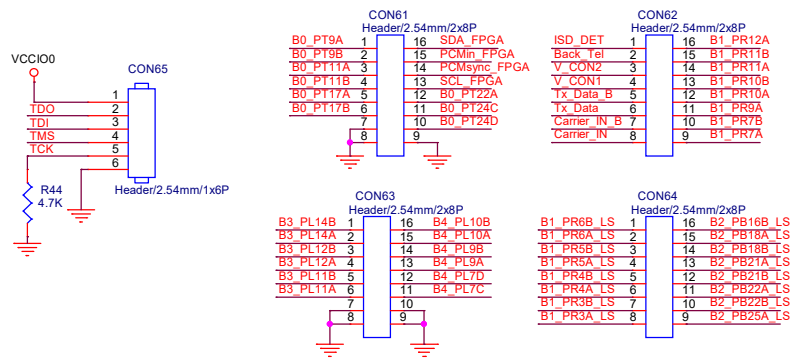


			Title		
			CI_General B'd		
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			B	Tx_E7150	0.0
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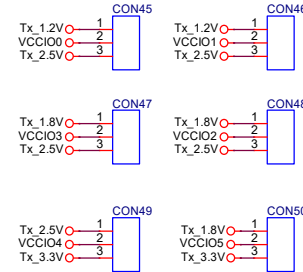
FPGA [XO2-2000ZE]



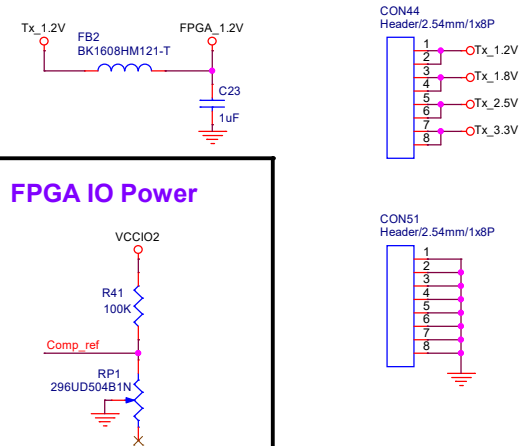
FPGA header



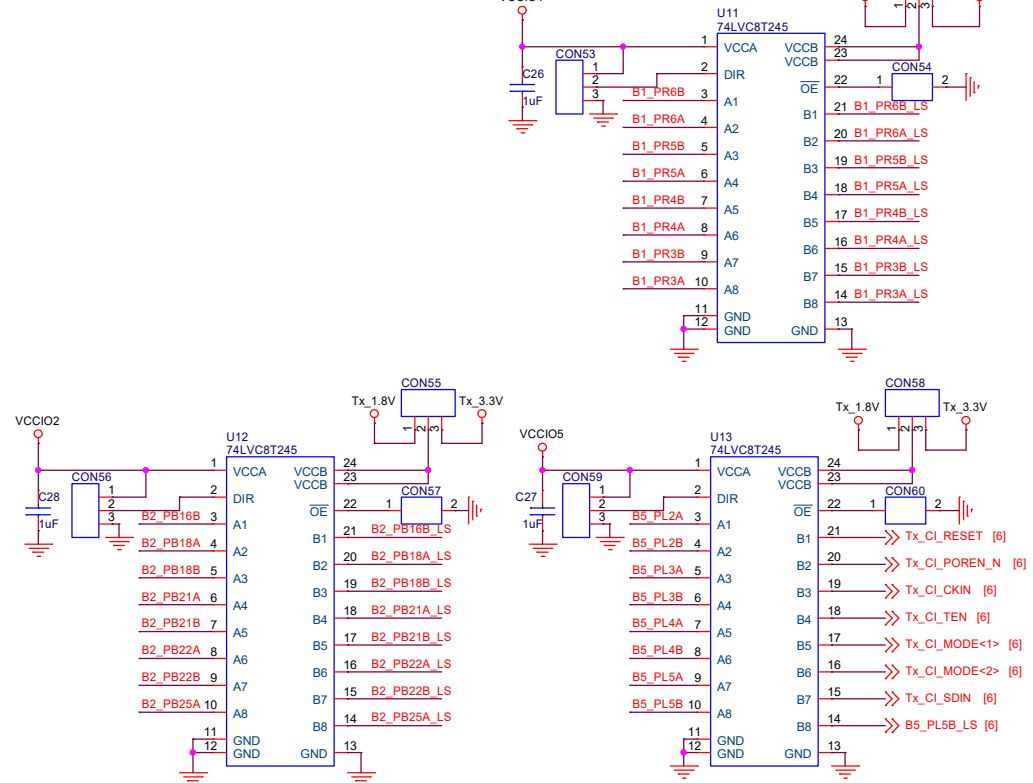
FPGA IO Power



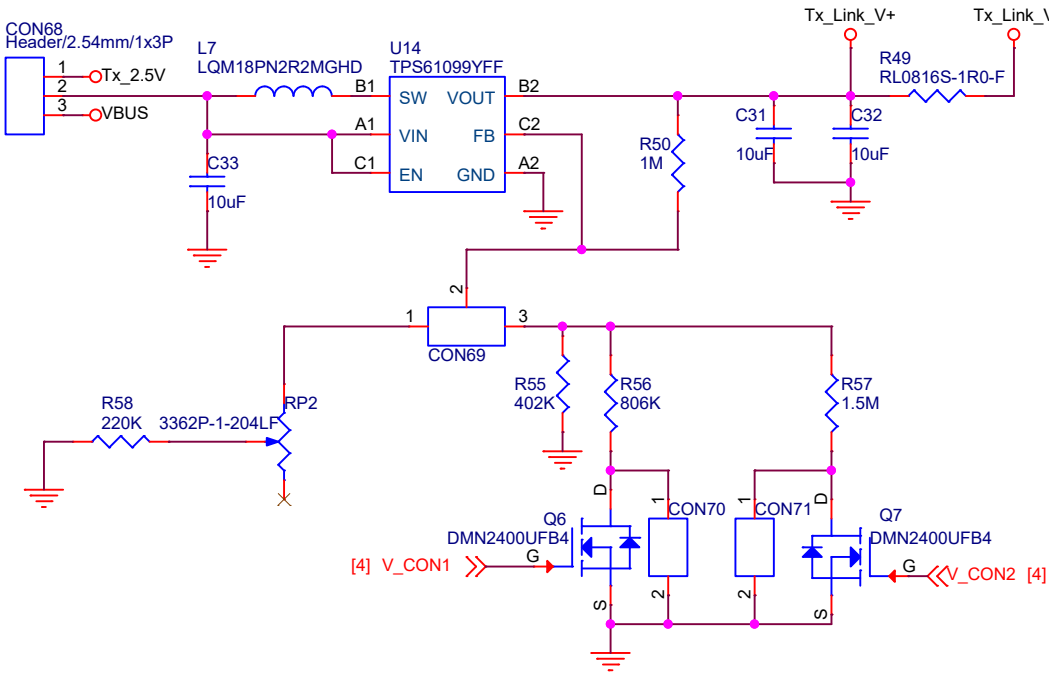
Voltage / GND



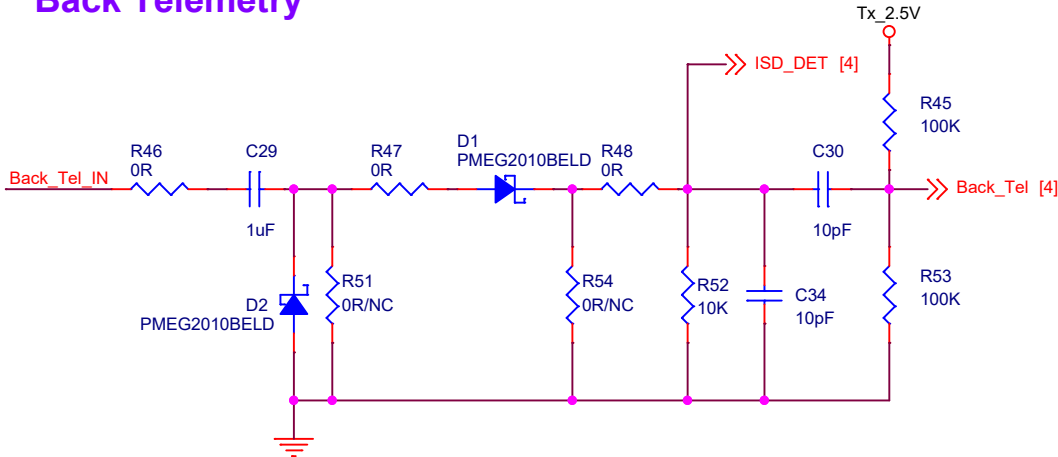
FPGA level shift



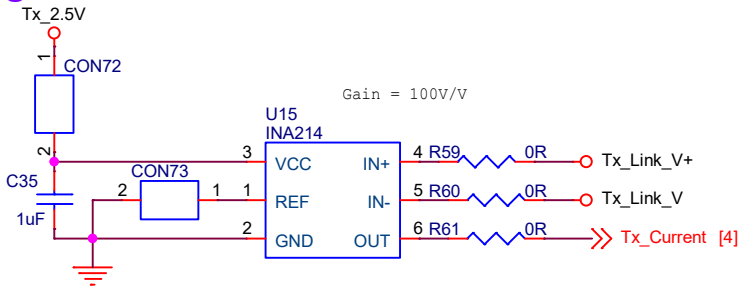
Boost converter (3.5V | 4.17V | 4.75V | 5.4V)



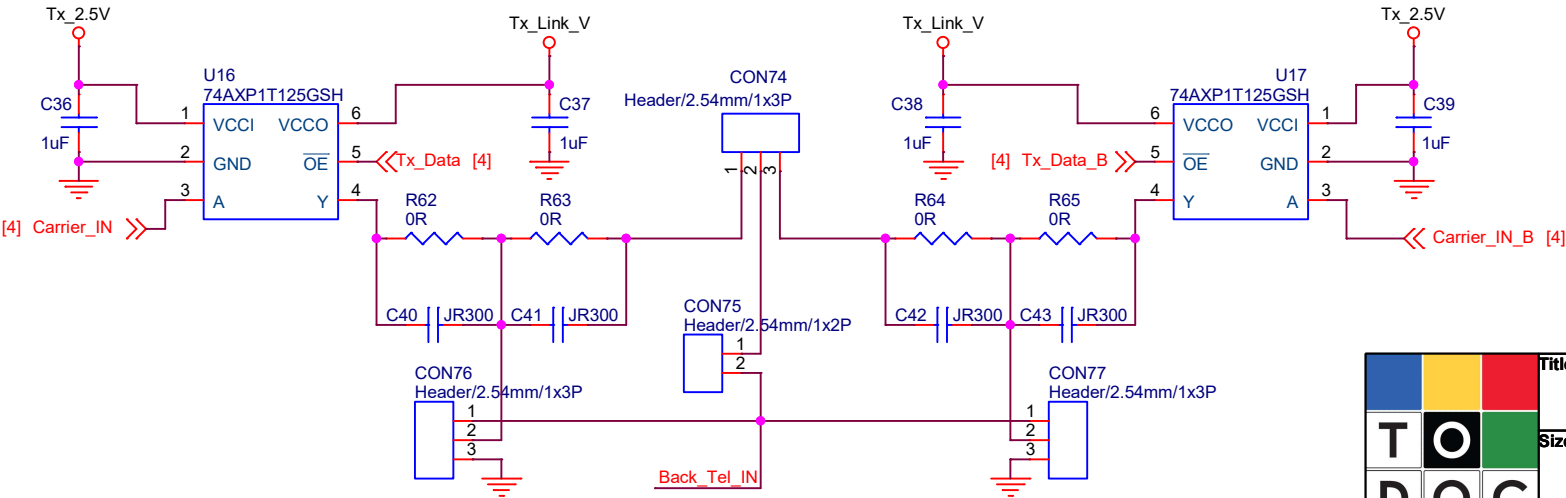
Back Telemetry



Current sensing

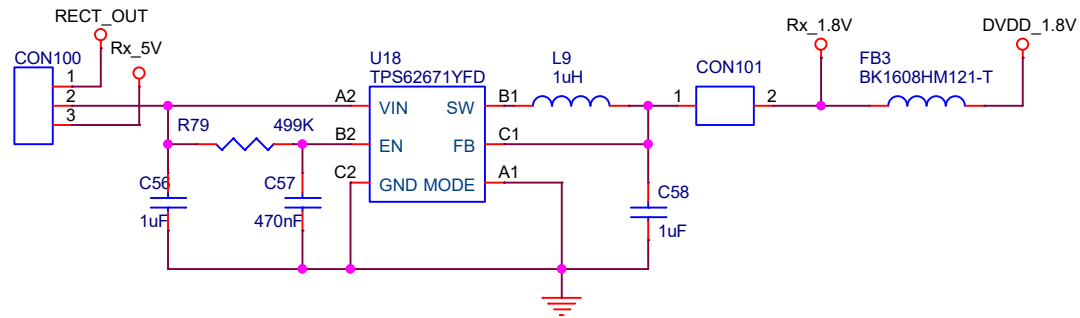


Class-D driver

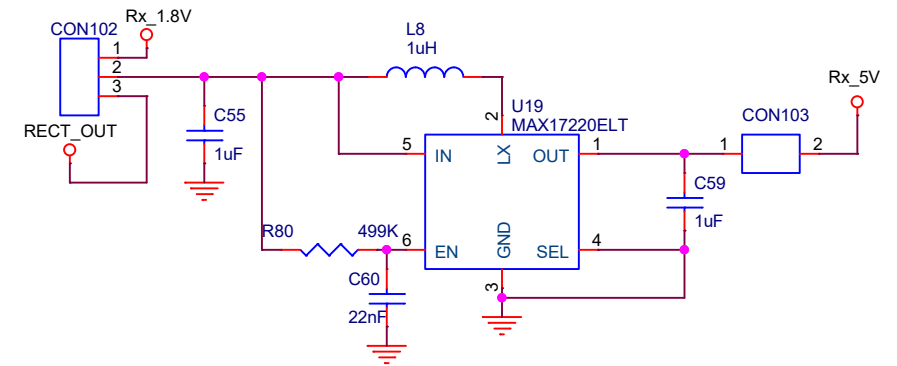


			Title		
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T	O		Size	Document name	Rev
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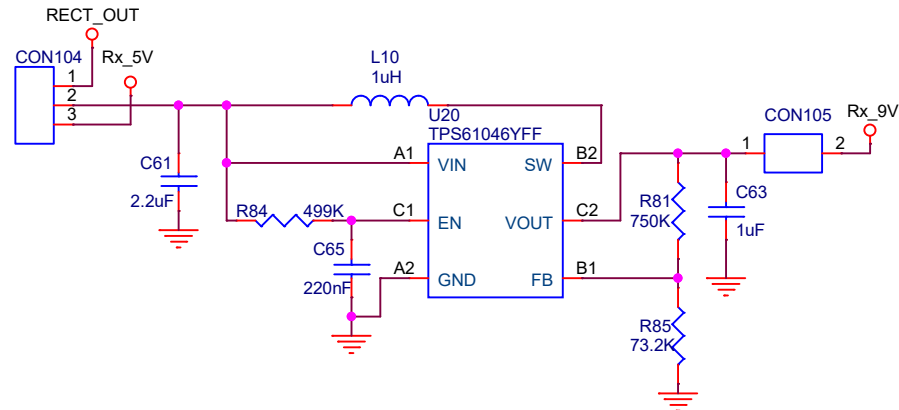
BUCK 1.8V



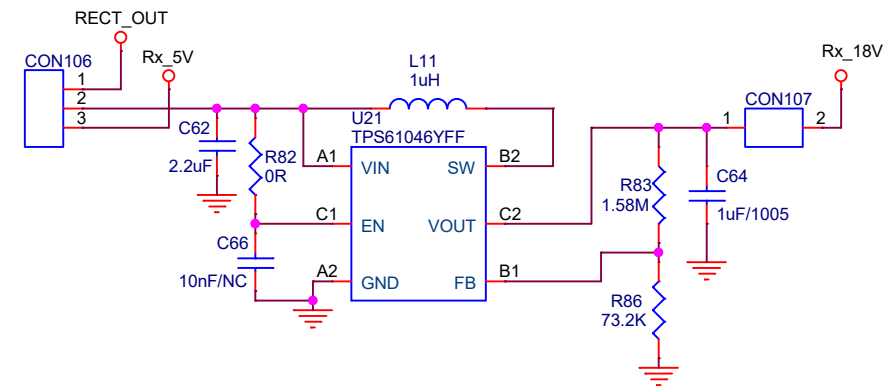
BOOST 5V



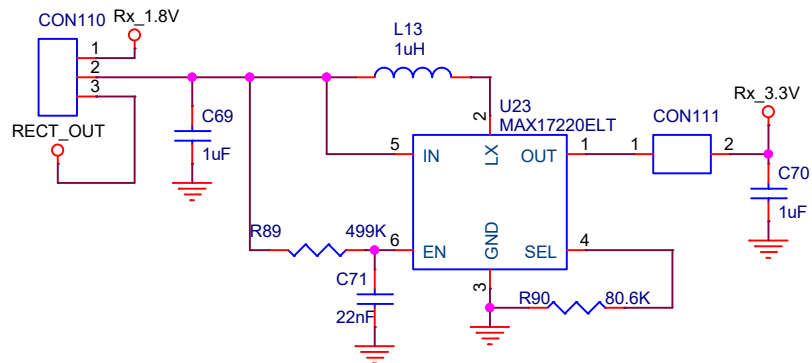
9V Block



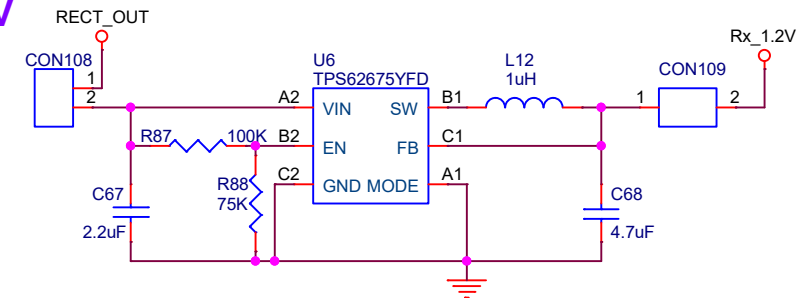
BOOST 18V



BOOST 3.3V

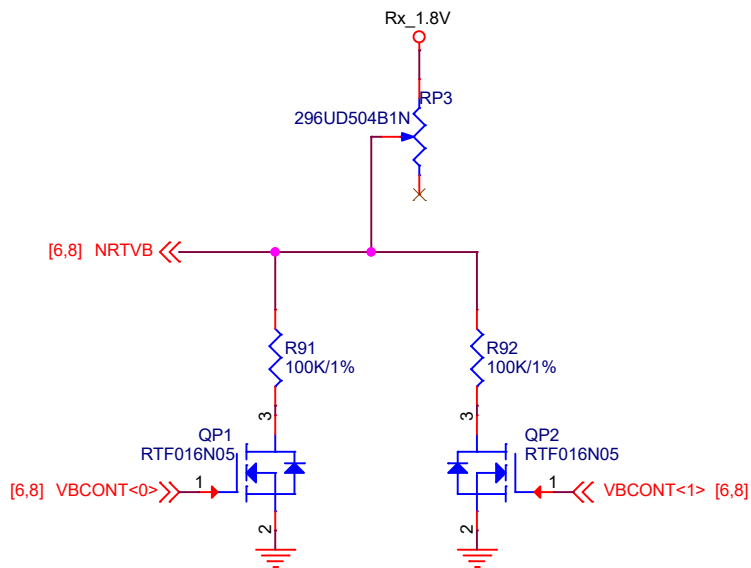


BUCK 1.2V

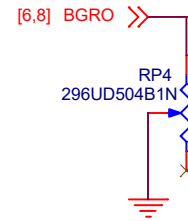


			Title			
			CI_General B'd			
T	O		Size	Document name	Rev	
			A4	Rx_Power	0.0	
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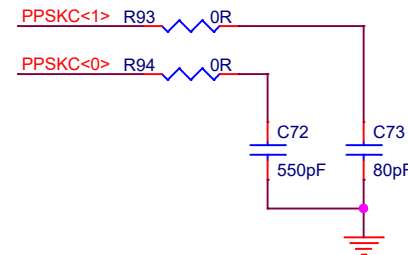
NRT Bias Control



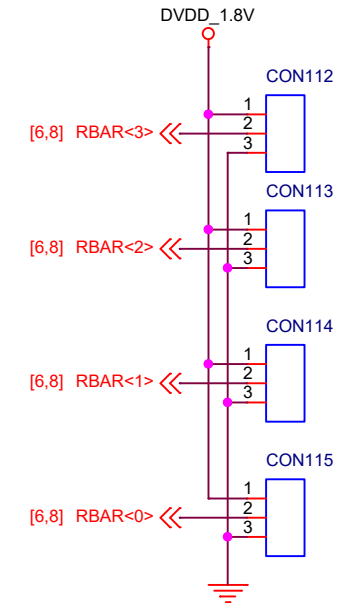
TEST_BGR



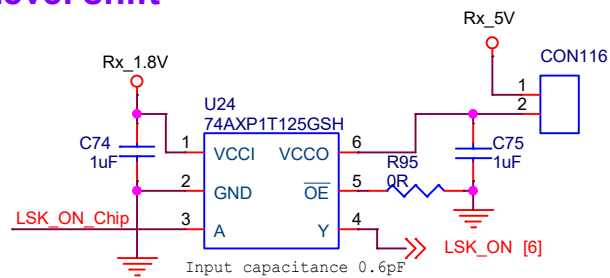
PPSK CAP



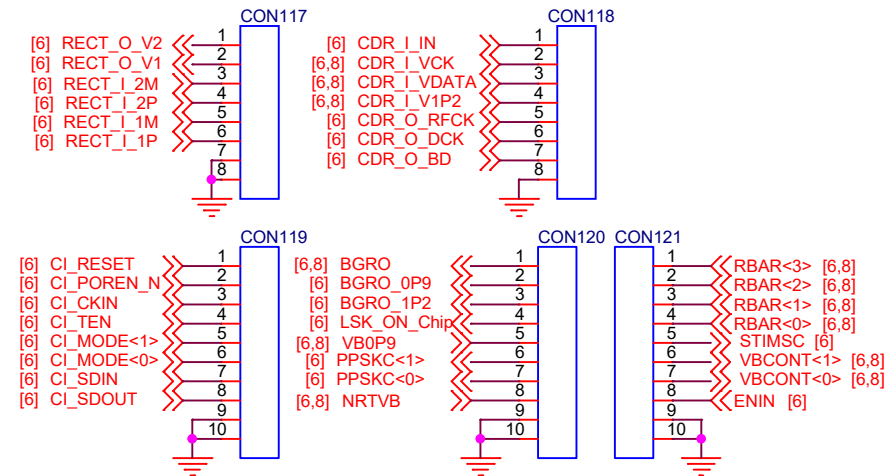
RBAR PIN



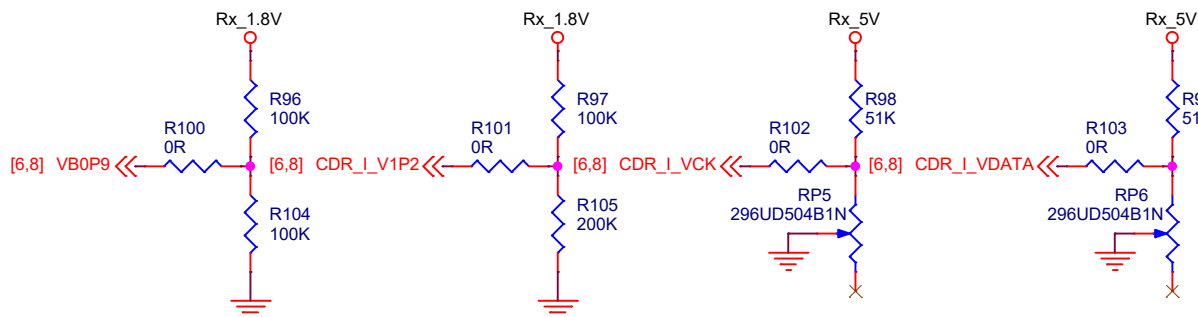
LSK Level shift



Function header

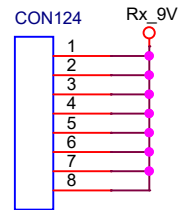
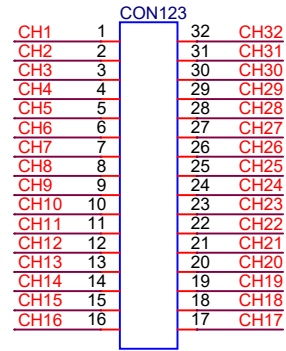


BIAS

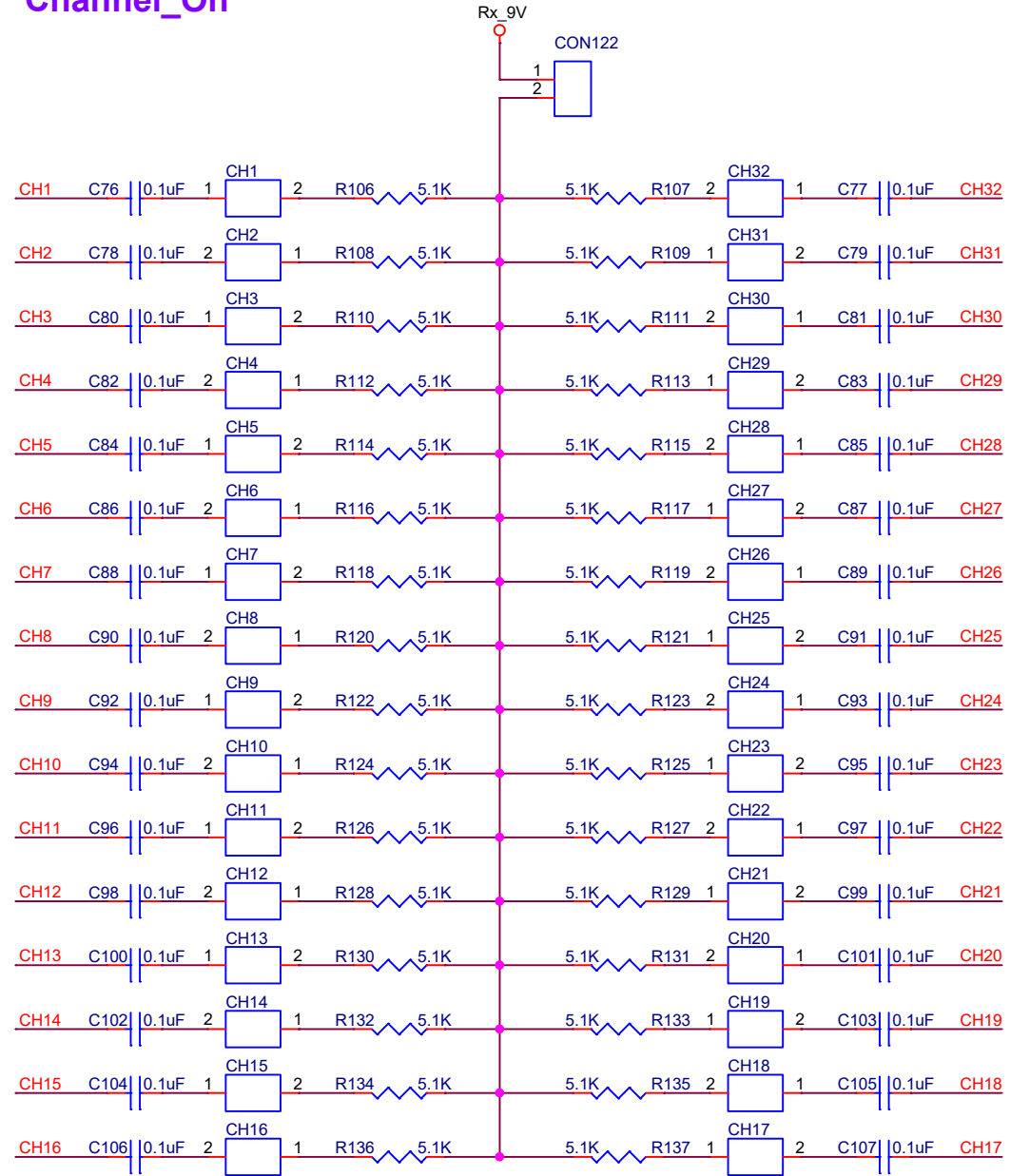


Title			
CI_General B'd			
Size	Document name		Rev
A4	Rx_Function		0.0
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Stimulation Out



Channel_On



			Title			
			CI_General B'd			
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D	O	C	A4	Rx_Stimulation	0.0	
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