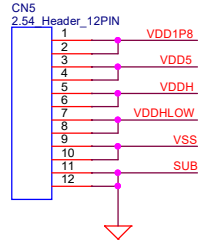
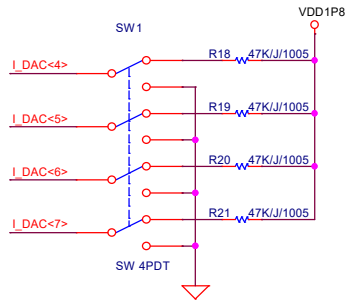


## VENUS\_POWER Left\_CONNECTOR



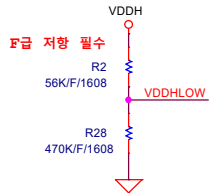
## I\_DAC\_SWITCH

상위 비트 선택 옵션 (I\_DAC4~7)



## VDDHLOW

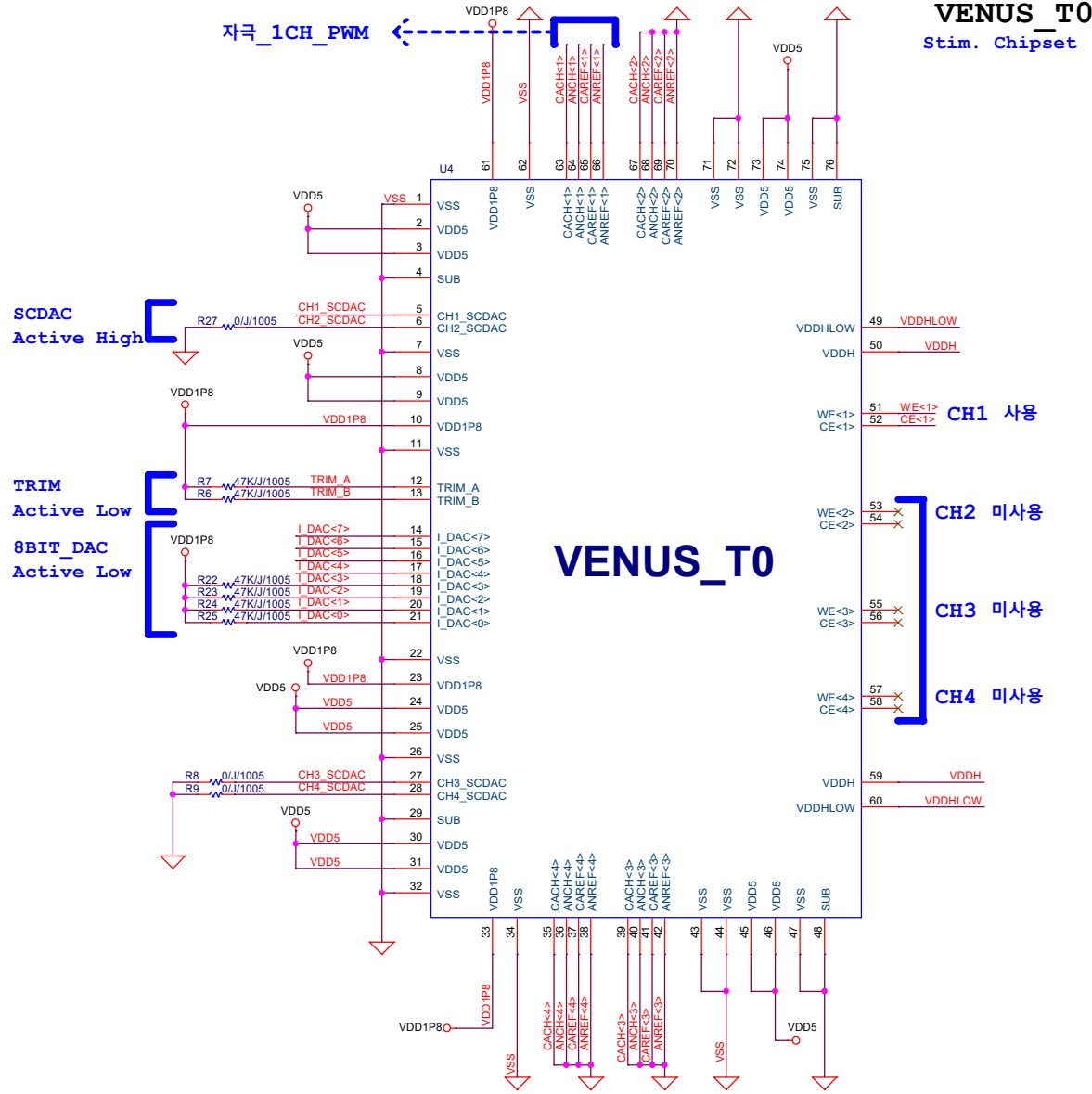
VDDH 대비 -5Vdc 유지 필요



## Note.

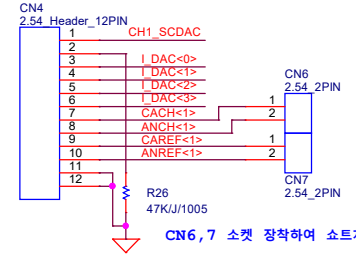
- 1) I\_DAC<7:0> : active low
- 2) TRIM\_A,B : High (disable) -> current 30% 감소
- 3) SCDAC : Low (disable) -> 미사용조건
- 4) AN/CA\_CH : GND (disable) -> 미사용조건
- 5) AN/CA\_REF : GND (disable) -> 미사용조건
- 6) VDDH : 40 Vdc fixed
- 7) VDDHLOW : VDDH - 5 Vdc = 35 Vdc

## 자극\_1CH\_PWM



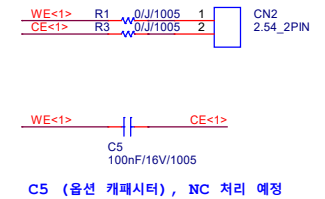
## VENUS\_T0 Stim. Chipset

## VENUS\_INPUT Right\_CONNECTOR

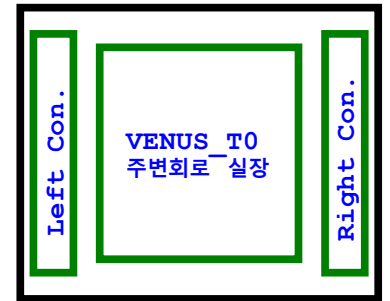


## STIMULATOR\_OUTPUT

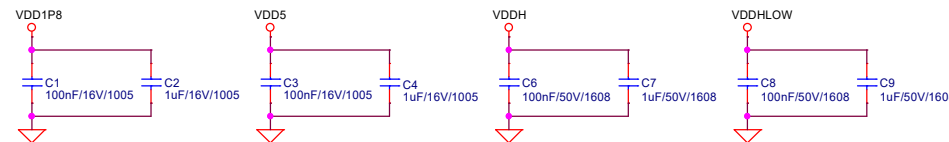
자극기 연결 커넥터



## VENUS\_T0 모듈 배치도 초안



## BYPASS\_CAP



todoc	Title <b>TB_SC_CC_STIM_IC_VENUS_V01</b>		
	Size	Document Number	Rev
	Custom	<b>VENUS_T0</b>	<b>V0.1</b>
	Date: Monday, November 07, 2022 Sheet 2 of 2		