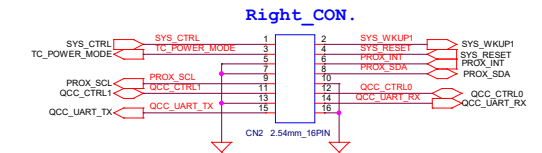
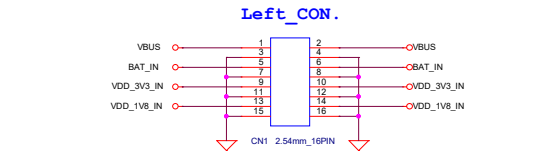
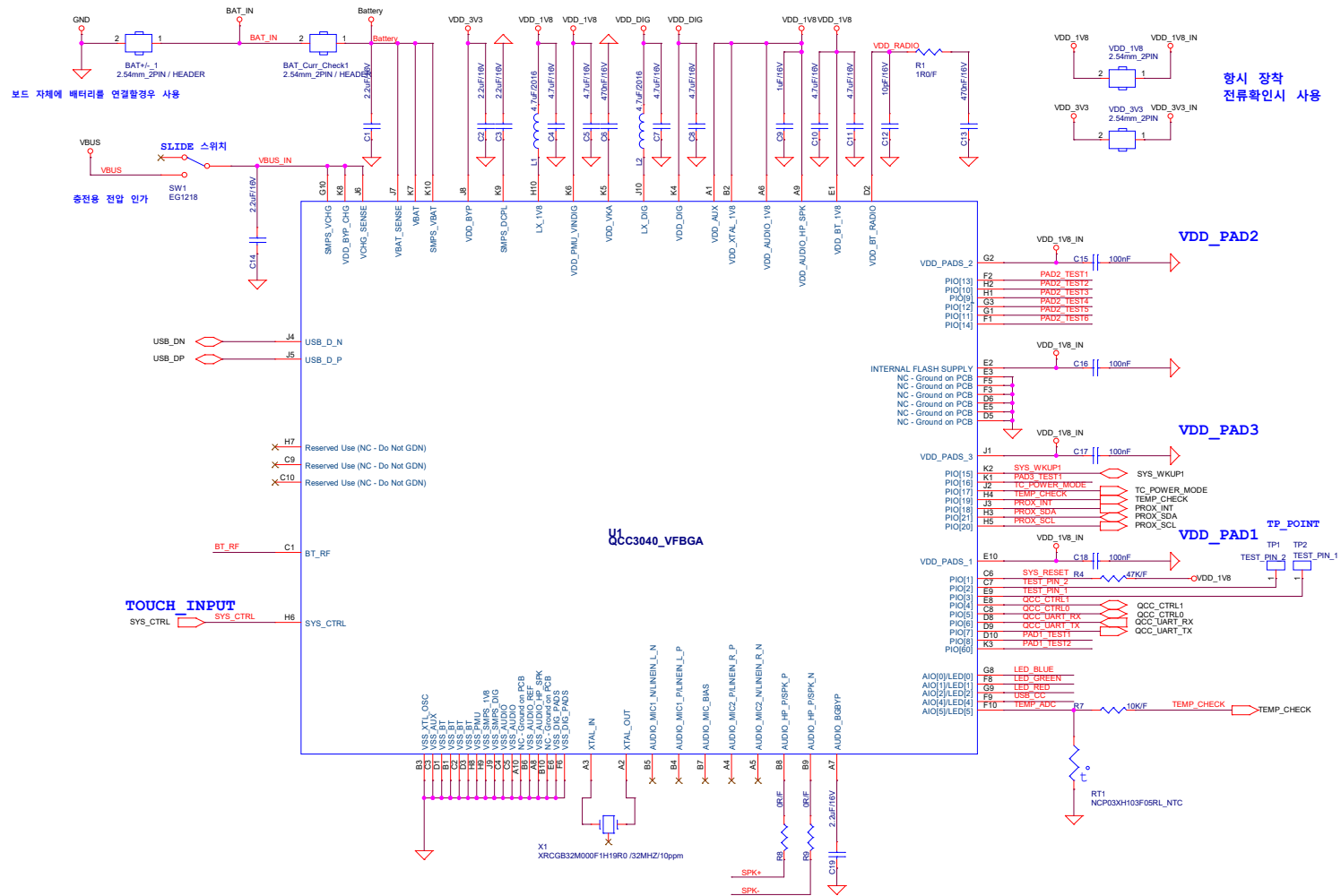
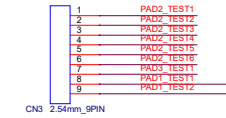


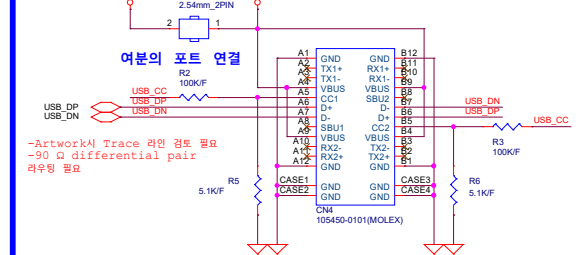
1. L1 , C4 , L2 , C7 와 U1 가깝게 배치해주세요



여분의 포트 연결



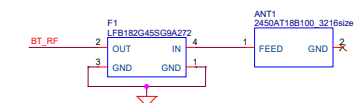
Debugging USB



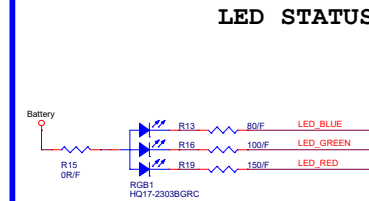
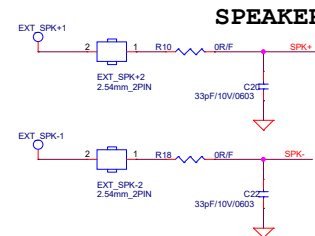
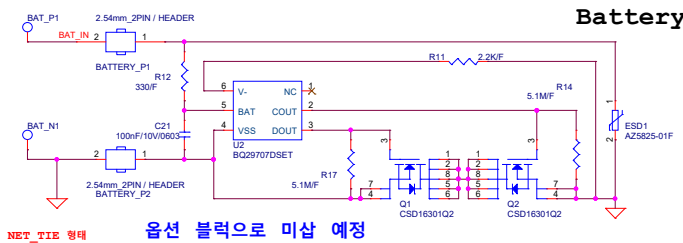
- Artwork시 Trace 라인 검토
- 90 Ω differential pair 라우팅 필요


RF Circuit

50ohm Matching Line!!



QCC_HW 디자인 가이드에 따른 LC 필터 소자 적용함



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