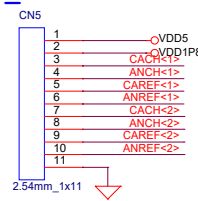


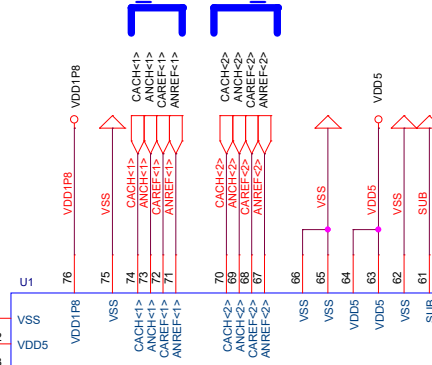
VENUS_T0

Stim. Chipset

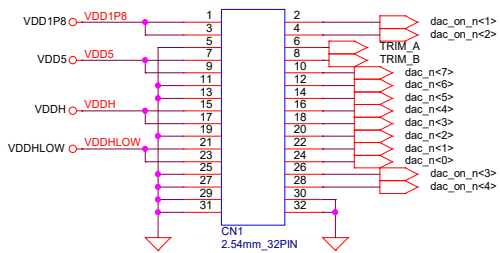
TOP_CONNECTOR



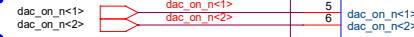
CH1 PWM CH2 PWM



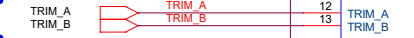
LEFT_CONNECTOR



dac_on



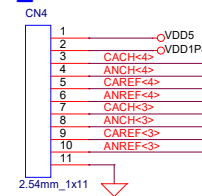
TRIM_IN



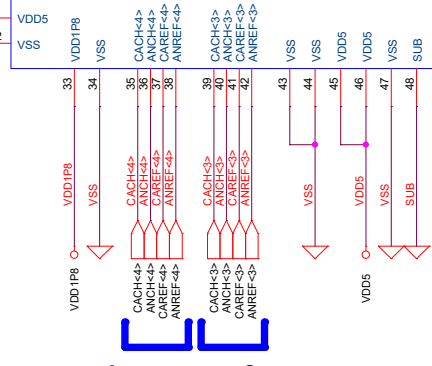
8BIT_DAC_IN



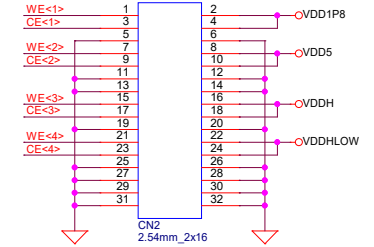
BOT_CONNECTOR



CH4_PWM CH3_PWM

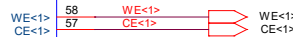


RIGHT_CONNECTOR

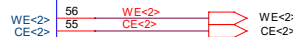


VENUS_T0

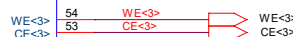
CH1 Stim.OUT



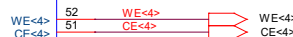
CH2 Stim.OUT



CH3 Stim.OUT

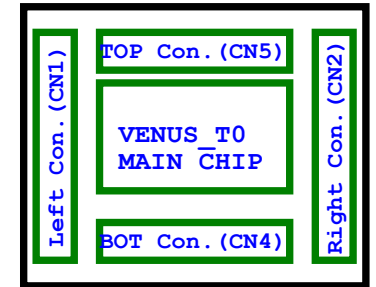


CH4 Stim.OUT



VENUS_T0 모듈 배치도 수정

PCB Size : 45mm x 45mm



아키텍처 네트 이름 동일화
액티브 low high 구분

표시

Note. 상세 설명 추가 필요->추적문서번호

- 1) dac_n<7:0> : Active Low
- 2) TRIM_A,B : Active Low
- 3) dac_on_n : Active High
- 4) AN/CA_CH : Active High
- 5) AN/CA_REF : Active High
- 6) VDDH : 40 Vdc fixed
- 7) VDDHLOW : VDDH - 5 Vdc = 35 Vdc

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