Dr.-Ing. Juan José Montero Jimenez

juan.montero@itcr.ac.cr | 2550 9338

Información Laboral

Cédula: 304270545

Tipo de nombramiento: Propiedad

Fecha de contratación: 06/02/2013

Sede: Cartago

Escuela: Ingeniería Electromecánica

Correo: juan.montero@itcr.ac.cr

ORCID: 0000-0002-3215-3736

Educación

Instituto Tecnológico de Costa Rica, Licenciatura in Ingeniería en Mantenimiento Industrial – Costa Rica — 2011

ISAE-SUPAERO, Maestría in Ingeniería Aeroespacial – Francia — 2018

ISAE-SUPAERO, Doctorado in Ingeniería Industrial e Informática – Francia — 2022

Carrera Profesional

Profesor Instructor — 06/02/2013

Profesor Adjunto — 23/10/2023

Profesor Asociado — 23/10/2O23

Publicaciones

An ontology model for maintenance strategy selection and assessment — 29/11/2021
Juan José Montero Jiménez, Rob Vingerhoeds, Bernard Grabot, Sébastien Schwartz
10.1007/s10845-021-01855-3 (Journal of Intelligent Manufacturing)

An unsupervised approach for health index building and for similarity-based remaining useful life estimation — 10/2022
Sébastien Schwartz, Juan José Montero Jiménez, Rob Vingerhoeds, Michel Salaün
10.1016/J.COMPIND.2022.103716 (Computers in Industry)

Enhancing predictive maintenance architecture process by using ontology-enabled Case-Based Reasoning — 13/9/2021
Juan Jose Montero-Jimenez, Rob Vingerhoeds, Bernard Grabot
10.1109/ISSE51541.2021.9582535 (2021 IEEE International Symposium on Systems Engineering (ISSE))

Towards multi-model approaches to predictive maintenance: A systematic literature survey on diagnostics and prognostics — 7/2020
Juan José Montero Jimenez, Sébastien Schwartz, Rob Vingerhoeds, Bernard Grabot, Michel Salaün
10.1016/j.jmsy.2020.07.008 (Journal of Manufacturing Systems)

A fault mode identification methodology based on self-organizing map — 1/1/2020
Sébastien Schwartz, Juan José Montero Jimenez, Michel Salaün, Rob Vingerhoeds
10.1007/S00521-019-04692-X (Neural Computing and Applications)

A System Engineering Approach to Predictive Maintenance Systems: from needs and desires to logical architecture 10/2019

Juan Jose Montero Jimenez, Rob Vingerhoeds

10.1109/ISSE46696.2019.8984559 (2019 International Symposium on Systems Engineering (ISSE))