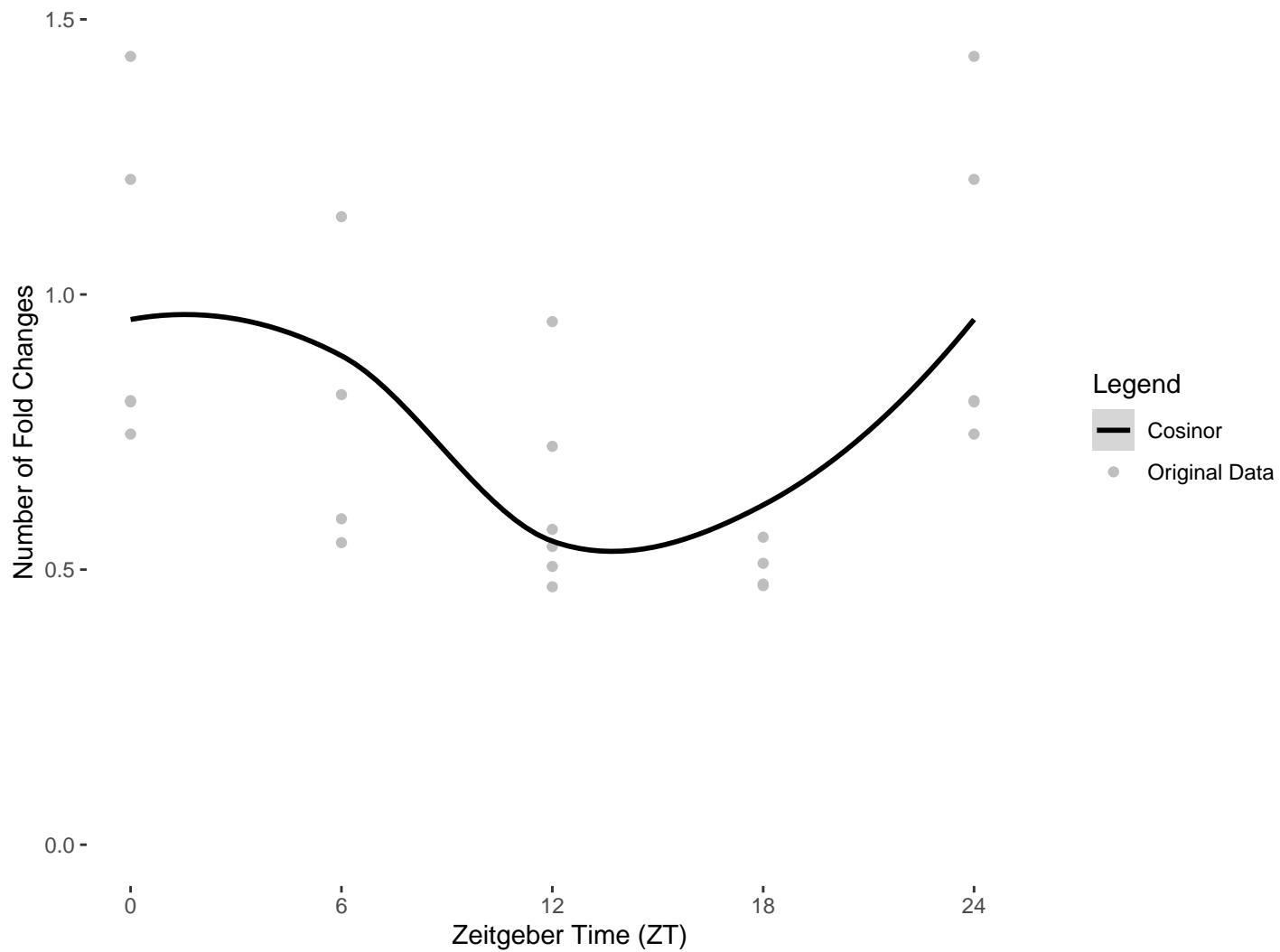


LD Clock Bmal1



rLEN Clock Bmal1

