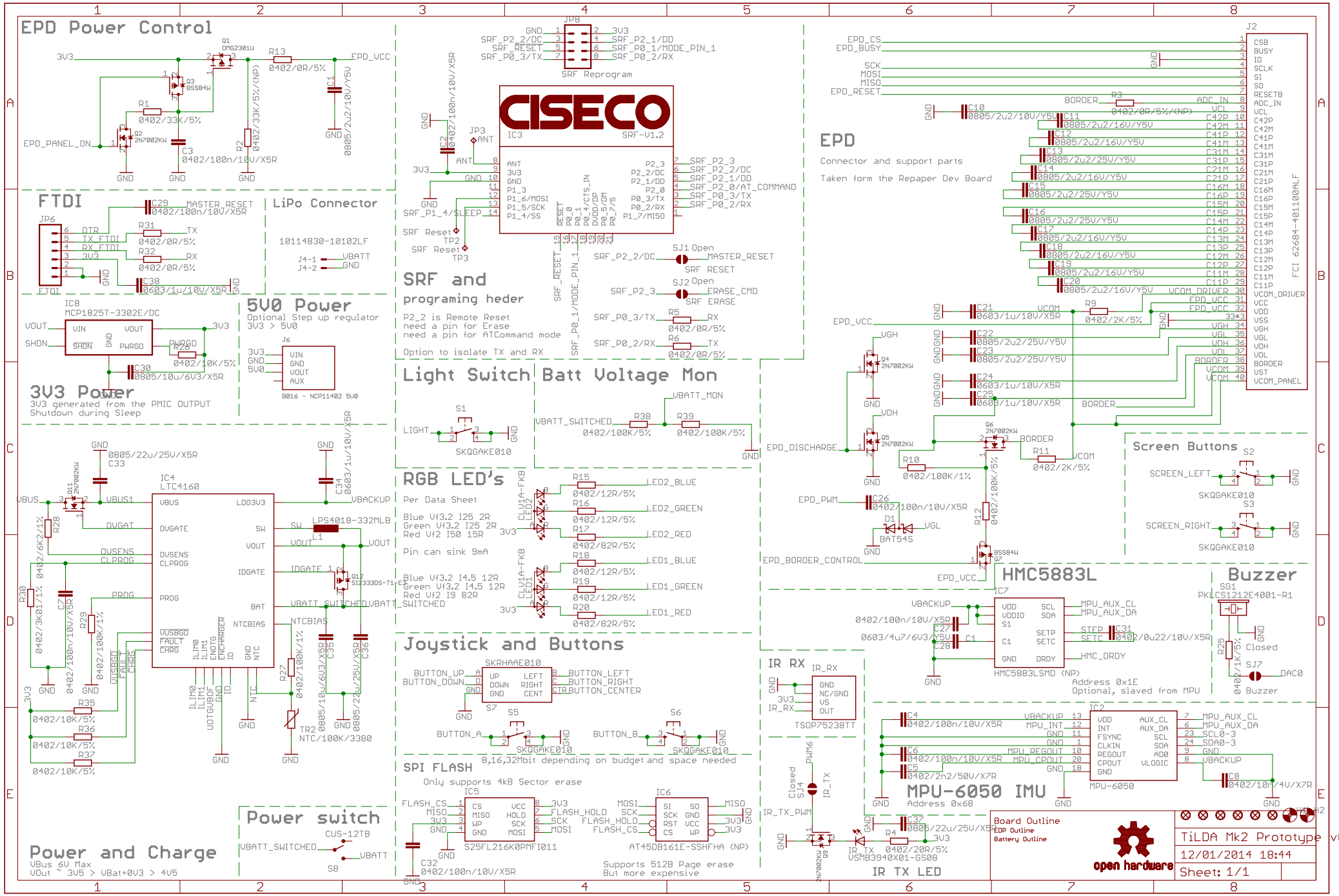
EPD Power Control
FTDI
LiPo Connector
10114830-10102LF
5V0 Power
Optional Step up regulator
3V3 > 5V0
3V3 Power
3V3 generated from the PMIC OUTPUT
Shutdown during Sleep
SRF and
programing heder
P2_2 is Remote Reset
need a pin for Erase
need a pin for ATCommand mode
Option to isolate TX and RX
SRF Reprogram
Light Switch
Batt Voltage Mon
RGB LED's
Per Data Sheet
Blue Vf3.2 I25 2R
Green Vf3.2 I25 2R
Red Vf2 I50 15R
Pin can sink 9mA
Blue Vf3.2 I4.5 12R
Green Vf3.2 I4.5 12R
Red Vf2 I9 82R
Joystick and Buttons
SPI FLASH
Only supports 4kB Sector erase
8,16,32Mbit depending on budget and space needed
Supports 512B Page erase
But more expensive
Power and Charge
VBus 6V Max
VOut ~ 3V5 > VBat+0V3 > 4V5
Power switch
EPD
Connector and support parts
Taken form the Repaper Dev Board
Screen Buttons
HMC5883L
Address 0x1E
Optional, slaved from MPU
Buzzer
IR RX
MPU-6050 IMU
Address 0x68
IR TX LED
Board Outline
EDP Outline
Battery Outline
open hardware
TiLDA Mk2 Prototype v0.1
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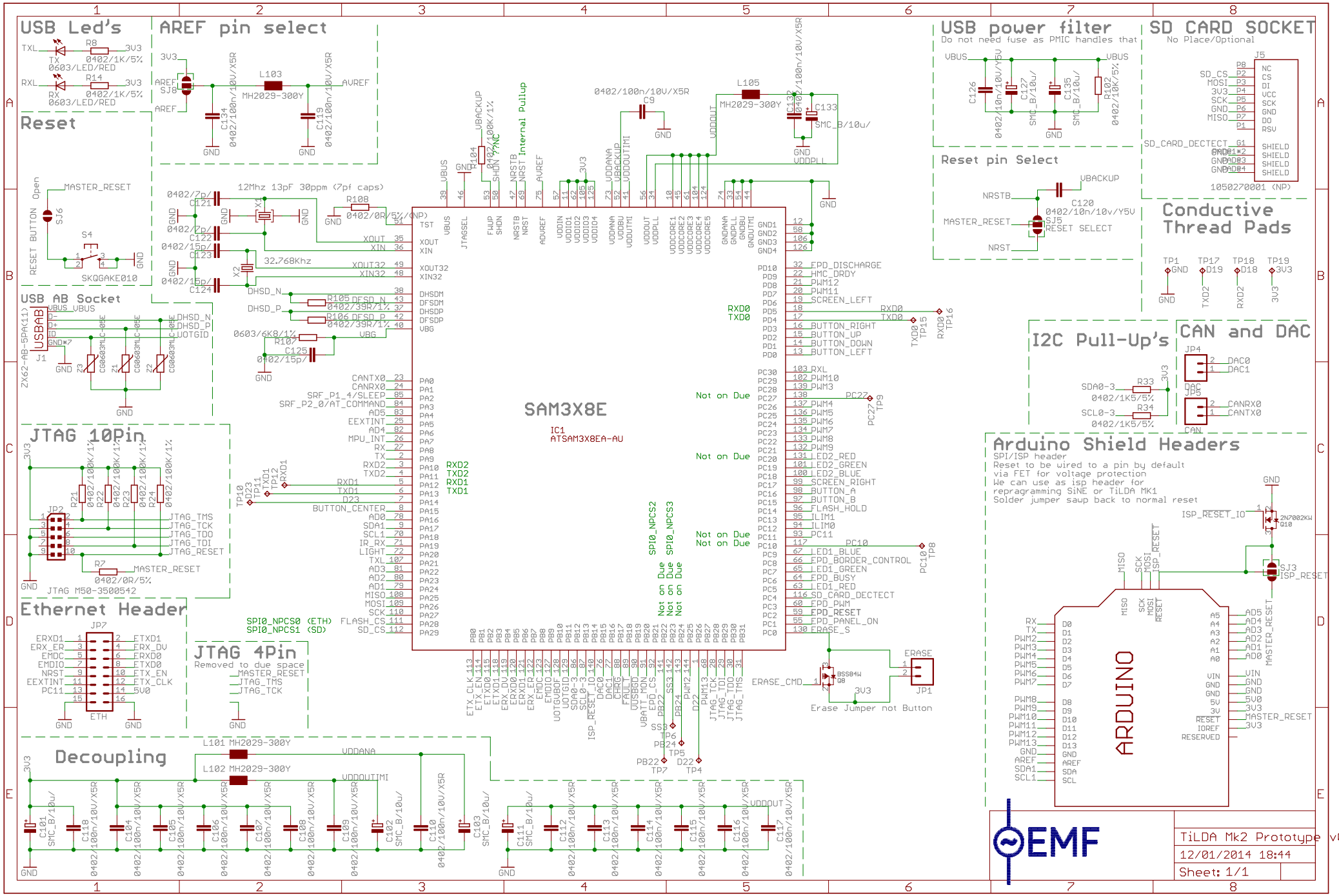
USB Led's
AREF pin select
Reset
USB power filter
Do not need fuse as PMIC handles that
SD CARD SOCKET
No Place/Optional
Reset pin Select
Conductive Thread Pads
USB AB Socket
I2C Pull-Up's
CAN and DAC
JTAG 10Pin
Arduino Shield Headers
SPI/ISP header
Reset to be wired to a pin by default
via FET for voltage protection
We can use as isp header for
reprogramming SiNE or TiLDA MK1
Solder jumper sawp back to normal reset
SAM3X8E
IC1
ATSAM3X8EA-AU
Ethernet Header
JTAG 4Pin
Removed to due space
Decoupling
Erase Jumper not Button
ARDUINO
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