FIFEX
Contex pipe (unisitive comm buff w/ 2 file descrips fil[0] read & fil[1] write)
finclude/canistd.l>
int pipe(int fil[2])
Data write & read FIFO base. No external/permanent name; only accessed v
Pipe can only be used by process that created it & its description;
close(file) closes a file description
depth read file files file.

depth read file description. Execution, Conditional Handling, must specify what to do fel's processes disc. Exp important when the 2 process of microschices them the contribution of microschices and the contribution of the process of the contribution of t Exchange Instructions:
const int n = /\* # processe
int bolt;
void P(int i) {
 int keyi = 1;
 while (true) {
 do exchange (keyi, b)
 while (keyi != 0);
 /\* crit section \*/;
 bolt = 0;
 /\* remainder \*/:
 } ProducerConsumer Problem: Insure that produced earl said data into full buff & consumer can't insure data from enepth buff in the consumer can't insure data from enepth buff in buff where a consumer taking into continuous continuou Write:
Atomic for at most PIPE\_BUF bytes
(512, 4k, 64k)
Blocking: if buffer full & read fd open
When all fd to read closed, causes
SIGPIPE sig for calling oid main() { bolt = 0; bolt = 0; parbegin (P(1), P(2), ..., P(n)); sees of, returns 0

When all fit read-dood, causes

From: unjud; facility, efficient commits

From: unjud; facilit bytes). TCP (Transmission Control Protocol): best known stream protocol, providing reliable stream serv. Heavyweight (needs 3 mags to ealsh virtual connection)
for the first (FECT) open (Section 1) and (Sec al Machine Instruct Adv: applicable to any # of pros on single/multiple processors ig main mem, simple & easy to verify, can be used to supp multi crit sects (each can be sharing main mem, simple & easy to vertly, can be used to 8upy must so 8upx defined by its own variable). Disade: busy-wait employed, while a proc is waiting for access to cit section it cout to consume processor time, starvation is possible when proc leaves crit sect & >-1 proc is waiting, deadlock is possible processings, Arenals);
send, npply(formult);
Arb: Block all details of mag passing, provides higher level of arbarcation, extends well-known model of programs, provides higher level of arbarcation, extends well-known model of programs (in the control of the con oid main() { parbegin (producer, co Inglementation of securiors: semi-wait & sens/sipal ops implemented as atomic primitives in landswarfurmware. Deliker' speternos' a sportime can be used. Use one of hardware comported schemes for matual exclusions or Manilares, construct that provides equiv functionality to semiphores that it easier to control that is implemented in a number of lange, & sep regions IIIs. Software module consisting of 2-1 procedures, in init sequence, & local data or program is software module consisting of 2-1 procedures, in init sequence, & local data only by most 'procedures (see Figure 1) procedures, on the procedure of the itincludes (cyclycia).

\*\*Itincludes (cyclic).

\*\*Itincludes (cy OS Structure: OS themselves implemented as sequence of ">-1 instructions appearing indivisible. Sequence is guaranteed or eas a group or not at all official Sections, which will be proved the requires access to shared resources & mut not be ease the section of Semapher: A variable w in at whether each 3 do not referred to the semapher where the semapher is the semapher with the semapher where the semapher is the semapher with the s Lindings. "—" proce continuously change states in response to changes in other procely wo Manual Exclusion: presignment when one process is not its cert that accesses shared resources no other price may be in crit seet that accesses any of those shared resources Principles. Interferency of contriputings of concurrencessing, Universities of other process. Of interrupt Principles and the processes relative speed of eace of proce can 'be prociscuit, dependent on activation of other process. Of interrupt Difficulties changes of plade resources, had not for OSs o optimize resource allocation management, had to locate programming errors. Beground the control of the processes of the p Message Passing: when proc interact with another these requirements must be satisfied and mag: synchronization (ensure mut exclu) and comm (to exchange info)

Blocking rend. Blocking rex: both sender & rec blocked til mag delivered, allowing for tight synchronization by procs int count; queueType queue; mailbox:

| Action Action Processes | Action Action | Act synchronization b/w procs

Nonblocking send, blocking rec; sender continues but rec blocked til req
Most useful combo. Sends >— I mag to to variety destinations asap

Nonblocking send, nonblocking rec, neither party required to wait

Readers/Writers Prob. data area shared among many procs, following o
satisfied: any il of readers may read file simultaneously, only 1 writer ma
writer is writing, no reader may read. id semWait(semaphore s) { s.count=;
if (s.count < 0) {

/\* place process in s.queue \*/;
/\* block this process \*/; sendiadis, mg, lengh);
recevipadis, lengh, lengh oid semSignal(semaphore s) {
s.count+\*;
if(s.count <= 0) {
/\* remove process P from s.queue \*/;
/\* place process P on ready list \*/; Sol to Reader/Writers Prob using semaphore (reader prio) to Reader/Writers Prob using s 'P program readersandwriters int readcout; semaphore x = 1, wsem = 1; void reader() { while (true) { semWait(x); readcout++; if (readcount -- 1) semWait(x); - 1) semWait (wsem); semSignal(x); READUNIT(); Numar Process Bodod gene Sougher Rock gene Stocked gener Senzaglates Street; sporce Procur Process Station of greater Semantican Broady sprease Bale Smapher Budy quar id writer() { while (true) { Process hile (true) { semWait(wsem); WRITEUNIT(); semSignal(wsem Beded gene Snaugherr Book years Societ queer Societation Reality person Numar oid main() { bolt = 0; parbegin (P(1), P(2), ..., P(n)); oid main() {
 parbegin (P(1), P(2), ..., P(n)); Adv: no pre-emp and rollback processes needed.

Disads-finture resource reep must be known by OS, procs can be blocked for long periods, precedires consideration must be independent & no sync rep, fixed # of a fixed process. The process of the pr PRACTICE EXAM PQ. Complete the following C++ program to guarantee that only one person at a time will be in the house, alternating between a Recon fin men & a Casto finnily ment (stating wifescon fin menuther). Yet program will recover from STON he # of open a finnily ment is 2.

\*\*Brachade\*\* of the Casto finnily ment is 2.

\*\*Brachade\*\* of t while (true) {
 smile (true) {
 smile (true) {
 smile (true) {
 information (true) {
 if (vritecount+
 if (vritecount+
 if (vritecount+
 informatic);
 smile (vsem) {
 informatic);
 smile (v Process fair Deniate ones...

Process fair Deniate or Security Francisco and Process of | Meta (Q): | September | Meta (Q): | Meta readcount = writecount = 0; parbegin (reader, writer); matrix to W. Petersen's Algorithm (Correct Solution taken from stides):
Need to observe state of both processes, which has right to insist on entering into CS both processes which has right to insist on entering into CS both process of the state of the .. Yes, the system is in a talk slope as are one able to determine of least one safe sopuring. stile (true)

if (count = 5) {
 if (imply (Taichbell) {
 if (imply (Taichbell) {
 count = 5)
 count = 5
 count = 5 Note: P5 is also supposed to be marked with a \* This is still bankers, just a different method (don't ask me I didn't do this one) county (september), and september (september (se wide \*access, one at a time(void \*family\_void\_pthread\_mutex\_lock(&bsem); char fam(20); thry(fam(che\*\*) family\_void\_ptr); while (buy= — true | stremp(fam,tum);-0) { if stremp(fam, \*fanCoN\*\*)-0} pthread\_cond\_wait(&nixon, &bsem); clse wise of clengty (peakingset), sector (seekingset), sept construt and (sep.55, \*06\*)) )
id writer(ist j)
nerape (meg)
while(tre) {
 rong = );
 send (writerequent, rong);
 roccire (shou(j), rong);
 Nottensit ();
 rong = );
 send ((shished, rong);
 send ((shished, rong);
} Nine of Chapty Insufreports Decimal Desireport, sept Country Send Despite, "Of"11 seed (map.)

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mod magni radio, and sharps algo a sharps algo a sharps algo a sharps pthread\_cond\_wait(&castro, &bsem); PTER 6: DEADLOCK AND STARVATION protection of the control of the con Deadlack\_permanent blocking of set of process that compete for sys resources or comm w/
each other. Set is deadlocked when each proc in the set is blocked waiting for event that can
only be triggered by another blocked process in the set
Resource categories: only be triggered by smooth to be a secondary means of the secondary secondary secondary secondary secondary secondary means that sim the special secondary means, levices & data structs).

Consumable: created/produced & destroyed/consumed. Interrs, sigs, msgs, & info in I/O buff V = R - A = [9 3 6] - [9 2 5] = [0 1 1] C-A\_row <= V, True, then V = V + A\_row Safe State -> V = R notinitable: elector protecte a customyea commine, ments, sags, mags, a ano in of our addiced Confidence.

Mutual Exclusion: one lone process may use resource at a time. If access to resource required it, then it must be supported by OS 

Hold.&.Wait: proc may hold allocated resources while awaiting assist of others. Requires 
proc to request all required resources at one it & blocking til all requests can be granted C-A.i: [2 2 2] > [0 1 1] so fail C-A.2: [0 0 1] < [0 1 1] so, new V = [0 1 1] + [0 1 2] = [6 2 3] C-A.3: [1 0 3] < [6 2 3] so, new V = [6 2 3] + [2 1 1] = [8 3 4] C-A.4: [4 2 0] < [8 3 4] so, new V = [8 3 4] + [0 0 2] = [8 3 6] The control of the control of the control of the blocking that it requests can be paramitted as the control of Since not all process were finished, go back to failed P1:  $C-A\_1$ : [2 2 2] < [8 3 6], so new V = [8 3 6] + [1 8 0] = [9 3 6] pthread\_mutex\_unlock(&bsem); return NULL;  $Q = \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 0 \\ 1 & 0 & 2 \\ 1 & 0 & 2 \\ 1 & 1 & 0 \end{bmatrix} \quad A = \begin{bmatrix} 0 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 1 \\ 1 & 0 & 0 \end{bmatrix}$   $R \text{ vector - resource vector. Obtains} \quad A = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 1 \\ 0 & 1 & 0 \end{bmatrix}$ PI Р3  $V = \begin{bmatrix} 1 & 1 & 0 \end{bmatrix}$ The following code comes from a prev exam. I was told "test to see if this myTurn to turn — myTurn) works b'e thats how the prof showed me" ainclude extreme include extreme in ainclude e else strcpy(family[i],"CASTRO");
if(pthread\_create(&tid[i], NULL, access\_one\_at\_a\_tifprintf[stderr, "Error creating thread in");
return 1; Ra Rb Re Rd Check A to find row of all 0's & mark. If no, Find a row in Q <= allocation vector V. Figure 6.6 Res n Graph for Figure 6.1b Applicable to P1: V = [1 1 0] + [0 1 0] = [1 2 0] [1 1 0] is original vector V, [1 2 0] is a row from A. P1 static pthread\_cond\_t mailTurn = PTHREAD\_COMD\_INITIALIZER static int turn; void "print\_in\_reverse\_order(void "void\_ptr\_argv) { [0 0 0] "[0 0 0] [0 1 0] [1 0 0] Q = [1 0 2] A = [0 0 1] [1 1 2] [1 1 1] [1 0 0] [0 1 0] Repeat. Applicable to P2: }
// Wait for the other threads to finish
for (int i = 0; i < nmembers; i++)
pthread\_join(tid[i], NULL.);
for(int i=0;i<nmembers;i++)
delete [] family[i]; in "print in "everte ercer(vota "voia ptr."
int myTurn = "(int ") void\_ptr\_argr;
// As long as its not my turn... WAIT!
pthread\_mutex\_lock(lbsem);
while (turn = myTurn) {
 pthread\_cond\_wait(lbwaitTurn, lbsem)
}
pthread\_mutex\_unlock(lbsem); // If it's my turn, print and and decreme
pthread\_mutex\_lock(absem);
cout < "1 am Thread " << myTurn << end1;
turn = turn = 1;
pthread\_cond\_broadcast(absem);</pre> Repeat. Applicable to PS: Rb (e) Greeder wait

Detection, Prevention & Avoidance

Prevention:

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threadNumber[i] = i;
if (pthread\_create(&tid[i], NULL, print\_in\_reverse\_order, &threadNumber of the state of the stat

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static pthread_stand_1 castes = PTHREAD_COND_INTELEXIS;
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False
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static pthread_cond_t wellTurn = PTHREAD_COND_INITIALIZER;
static int order;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             Salect the matrix of the lashour's appointment of the service pattern for the 
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                           d *print_in_increasing_order_even_odd(void *void_ptr_argv) {
nt *index = (int *)void_ptr_argv;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             void "pint_in_reverse_order(void "void_ptr_argv) {

pthread_mutex_lock(&bsem);

in "index - (ist ") void_ptr_argv,

whilf("index - (ist ") void_ptr_argv,

pthread_cond_wait(&waitum, &bsem);
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                            Action dispetition 4.

A feet or dispetition
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        std::cout<<"1 am Thread" << ("index) << std::endl;
order--;
pthread_cond_broadcast(&waitTurn);
pthread_mutex_unlock(&bsem);
return NULL;
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                  int manual
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A process that halts must do so without interfering with other processes.

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for (int i = 0; i < nthreads; i++)
pthread_join[tid[i], NULL];</pre>
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        ament is upon the authorized at once b. Banker's Algorithm of Contich Algorithm of Costch Algorithm of Cos
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a. Abort all deadlocked processes to 5 Successively abort deadlocked processes until deadlock no longer exists c. Successively preempt resources until deadlock no longer exists d. None of the above d. None of the above

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