

PIPEs
Create pipe (unidirect comm buff w/ 2 file descripts fd[0] read & fd[1] write)
#include <unistd.h>
int pipe(int fd[2]);
Data write & read FIFO base. No external/permanent name; only accessed via 2 fds.
Pipe can only be used by process that created it & its descendants.
close(fd) closes a file descriptor
dup(newfd) duplicates fd, dup2(newfd, oldfd) copies (more like alias)

Read:	Write:
Not nec atomic; may read less bytes Blocking: if no data, write fd still opens. If empty & all fd for write closes; read sees eof, returns 0	Atomic for at most PIPE_BUF bytes (512, 4k, 64k) Blocking: if buffer full & read fd open. When all fd to read closed, causes SIGPIPE sig for calling

Pros: simple, flexible, efficient comms
Cons: no way to open already existing pipe: impossible for 2 arbitrary processes to share same pipe (unless created by common ancestor)

IPC: UNIX Shared Mem

Parent & child processes run in separate addy spaces.
Shared mem segment: piece of mem that can be allocated & attached to addy space; process w/ this mem seg attached will have access to it but race conditions can occur
Procedure: find a key (unix uses key to identify shared mem segments) -> shmget() allocates a shared mem -> shmat() attach shared mem to addy space -> shmdt() detach mem from addy space -> shmctl() deallocate shared mem
Keys: global; If other processes know key, can access shared mem
Can ask sys to provide private key using IPC_PRIVATE
DIY:

```
key_t SomeKey;  
SomeKey = 1234;  
Autogenerate:  
key_t = ftk(char *path, int ID);  
Asking for Shared Memory:  
Use shmget() to request a shared mem (returns shared mem ID):  
shm_id = shmget(key_t key, int size, int flag)  
Flag for our purpose either 0666 (rw) or IPC_CREAT|0666  
Include following:  
#include <sys/types.h>  
#include <sys/ipc.h>  
#include <sys/shm.h>
```

Attaching Shared Memory:

Use shmat() to attach existing shared mem to addy space (ret void pointer to mem):
shm_ptr = shmat(int shm_id, char *ptr, int flag);
shm_id ID from shmget(), use NULL for ptr, flag = 0

Detaching & Removing Shared Memory:

To detach shared memory, use shmdt(shm_ptr);
shm_ptr is pointer returned by shmat().

After shared mem *detached*, it's still there (can be reattached to use again. *Removing* a shared mem will make it stop existing)

To remove shared memory, use shmctl(shm_ID, IPC_RMID, NULL);
shm_ID is shared mem ID returned by shmget().

INTERPROCESS COMMUNICATION

Types: Message passing (Blocking/non-blocking, Datagrams, virtual circuits, streams, Remote Procedure Calls (RPC)), Shared Memory
Shared Memory: >=2 processes share part of their addy space
Adv: fast & easy to use (but occur access to shared data can cause issues, and must sync access to shared data)
Disadv: senders & receivers must be on same machine, less secure (processes can directly access part of addy space of other processes)

Message Passing: processes want to exchange data send & receive msgs
One send and one receive:
send(addr, msg, length);
receive(addr, msg, length);
Adv: senders & receivers can be on diff machines; receiver can inspect msgs received before processing them
Disadv: hard to use (every data transfer requires send() & receive(), and receiving process must expect send())

Defining Issues of Msg Passing: Direct/Indirect communication, Blocking/Non-Blocking primitives, exception handling, quality of service
Direct Comm: send & rec calls specify processes as destination/source:
send(process, msg, length);
receive(process, msg, &length);

No intermediary b/w sender & receiver. Ex. each phone hardwired to another
Proc exe rec call must know identity of all procs likely to send msgs (bad for servers)
Indirect Comm: send & rec primitives specify intermediary as destination or source (mailbox: sys obj created by kernel @ request of user process):
send(mailbox, msg, size);
receive(mailbox, msg, &size);

Diff procs can send msgs to same mailbox. A proc can rec msgs from procs it knows nothing about, can wait for msgs from diff senders (answ 1st msg rec)
Private Mbox aka ports: proc requesting creation & children are only ones that can rec msgs through that mbox. Ceases to exist when proc requesting its creation (and all children) terminates.
Public Mboxes: owned by sys, & shared by all procs having right to rec msgs through it (works best when all procs on same machine). Survives termination of proc that requested creation. Ex. msg queues

Blocking Primitives: blocking send doesn't return til receiving process has received msg; no buffering needed. Blocking receive doesn't return til msgs have been received (std choice, but not good choice for direct comm)
Non-Blocking Primitives: Non-blocking send returns as soon as msg has been accepted for delivery by OS (assuming OS can store msg in a buffer, std choice). Non-blocking receive returns as soon as it has either retrieved msg or learned mbox is empty (retrieve) (acts as receive) (for receiver).

Simulating blocking receives: can sim blocking receive w/ non-blocking receiving within a loop (aka busy wait)
do {
Code = receive(mbox, msg, size);
sleep(1); // delay
} while (code == EMPTY_MBOX);

Simulating blocking sends: can sim blocking send w/ 2 non-blocking sends & a blocking receive
Sender sends msg & requests ACK -> sender wait for ACK from receiver using blocking receive -> receivers sends ACK

Non-blocking primitives require buffering to let OS store msgs that have been sent but not received. Buffers have a bounded capacity, but theoretically unlimited capacity.

Exception Condition Handling: specify what to do if processes die, exp. important when the 2 procs on diff machines (must handle host failures & network partitions)
Quality of Service: when sender & receiver on diff machines, msgs can be lost, corrupted or duped, and can arrive out of sequence. Can still decide to provide reliable msg delivery
Datagrams: msgs sent individually (can be lost, duped, out of seq).
Reliable: msgs resent until ACK. Unreliable: msgs not ACK (works well when msg requests reply which acts as implicit ACK)
UDP (User Datagram Protocol) best known datagram protocol. Provides unreliable datagram services which is best for short interactions
Virtual Circuits: establishes logical connection b/w sender & receiver. Msgs guaranteed to arrive in seq w/o lost/duped msgs.
Requires virtual connection before sending any data. Best for transmitting large data amounts requiring sending several msgs (FTP, HTTP)
Streams: like virtual circuits, but does NOT preserve msg boundaries (seamless stream of bytes).
TCP (Transmission Control Protocol): best known stream protocol, providing reliable stream serv. Heavyweight (needs 3 msgs to establish virtual connection)
Remote Procedure Calls (RPC): applies to client-server model
send_req(args); rcv_req(&args);
process(args, &results); send_reply(results);
rcv_reply(&results);

Adv: hides all details of msg passing, provides higher level of abstraction, extends well-known model of programming
Disadv: illusion not perfect (RPCs don't behave exactly like regular procedure calls, client & server don't share same addy space), programmer must be aware of differences
User program contains user code & calls user stub that appears to call server procedure
rpc(xyz, args, &results);
User stub procedure generated by RPC package; parks args into request msg & performs required conversions (arg marshaling) -> sends request msg -> waits for server reply -> unpacks results & performs required conversions (arg unmarshaling)
Server stub: generic server generated by RPC pack waits for client requests, unpack request args & performs required data conversions, calls appropriate server procedure (which is written by user & does actual processing), packs results into reply msg (performs required conversions), sends reply msg
Client & server processes don't share same addy space: no global variables, can't pass by ref, can't pass dynam data structs via pointers; RPC can pass args by value & result (passes curr val to RP & copies returned val in user program).

CHAPTER 5: CONCURRENCY

Multi App: invented to allow processing time to be shared among active apps
Structures Apps: extension of modular design & struct programming
OS Structure: OS themselves implemented as set of processes/threads
Key Terms:

Atomic Operation: function/action implemented as sequence of >=1 instructions appearing indivisible. Sequence is guaranteed of exe as a group or not at all
Critical Section: section within proc that requires access to shared resources & must not be exe while another process is in a corresponding section of code
Deadlock: situation where >= 2 processes unable to proceed bc each is waiting for one of the others to do something

Livelock: >=2 proc continuously change states in response to changes in other proc(s) w/o doing useful work
Mutual Exclusion: requirement when one process is in crit sect that accesses shared resources, no other proc may be in crit sect that accesses any of those shared resources
Principles: Interleaving & overlapping (ex of concur processing), Uniprocessor - relative speed of exe of procs can't be predicted; dependent on activities of other procs, OS interrupt handling, OS scheduling policies

Difficulties: sharing of global resources, hard for OS to optimize resource allocation management, hard to locate programming errors
Rare condition: occurs when multi procs/threads read & write data items. Final result depends on order of exe ("loser" updates last, determines final val)
OS Concerns (Design & mgmt. issues): OS keeping track of various procs, allocation & deallocate resources for each active proc, protect data & phys resources of each proc against interference, ensure procs & outputs are independent of processing speed

Interaction Process

Degree of Awareness	Relationship	Influence of 1 Process on the other	Potential Ctl Probs
Processes unaware of each other	Competition	- Results of 1 process independent of others - Timing may be affected	- Mut Exclusion - Deadlock (renewable) - Starvation
Processes indirectly aware of each other	Coop by sharing	- Results of 1 proc may depend on info fr others - Timing may be affected	- Mut Exclusion - Deadlock (renewable) - Starvation - Data coherence
Processes directly aware of each other (have comm prim available to them)	Coop by comm	- Results of 1 proc may depend on info from others - Timing may be affected	- Deadlock (consumable) - starvation

Resource Competition: concurr procs competing for use of same resource (I/O devices, mem, proc time, clock). Control Probs: need mutual exclusion, deadlock, starvation
Mutual Exclusion Requirements: Must be enforced, proc that halts has to w/o interfering w/ other procs, no deadlock/starvation, proc must not be denied access to crit sect when no other proc using it, no assump made about relative proc speeds/# of procs, proc remains inside crit sect for finite time only
Mutual Exclusion hardware supp: Special machine instructions - compare & swap (compare & exchange instruction). Compare made b/w mem val & test val. If same, swap.
Compare & Swap Instructions:
const int n = /* # processes */;
int bolt;
void P(int i) {
while (true) {
while (compare_and_swap(bolt, 0, 1) == 1)
/* do nothing*/;
/* crit section */;
bolt = 0;
/* remainder */;
}
}
void main() {
bolt = 0;
parbegin (P(1), P(2), ..., P(n));

Exception Condition Handling: specify what to do if processes die, exp. important when the 2 procs on diff machines (must handle host failures & network partitions)
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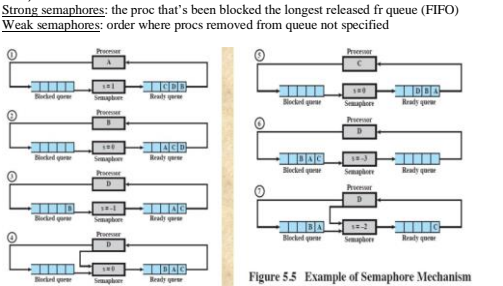
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while (compare_and_swap(bolt, 0, 1) == 1)
/* do nothing*/;
/* crit section */;
bolt = 0;
/* remainder */;
}
}
void main() {
bolt = 0;
parbegin (P(1), P(2), ..., P(n));

Semaphore: A variable w/ int val where only 3 ops are defined -> no way to inspect/manip semaphores other than those ops: 1) may be init to nonneg int val 2) semWaita op decrements val 3) semSignal op increments val
Cons: no way to know before proc decrments semaphore if block, no way to know which proc will cont immediately on unproc sys when 2 running concurr, don't know if another proc is waiting so # of unblocked proc is 0 or 1
Semaphore Primitives
struct semaphore {
int count;
queueType queue;
};
void semWait(semaphore s) {
s.count--;
if (s.count < 0) {
/* place process in s.queue */;
/* block this process */;
}
}
void semSignal(semaphore s) {
s.count++;
if (s.count <= 0) {
/* remove process P from s.queue */;
/* place process P on ready list */;
}
}
Strong semaphores: the proc that's been blocked the longest released fr queue (FIFO)
Weak semaphores: order where procs removed from queue not specified



Sol to Reader/Writer Prob using semaphore (writer prio)

```
#include<iostream>
using namespace std;
semaphore s=1, r=1, w=1, l=1, sum=1, sum+=1;
void reader() {
    while (true) {
        wait(s);
        cout<<"Reader " <<id<<" is reading\n";
        wait(r);
        sum++;
        if (sum==1) wait(l);
        wait(w);
        cout<<"Reader " <<id<<" finished reading\n";
        signal(w);
        signal(r);
        if (sum==0) signal(l);
        signal(s);
    }
}
void writer() {
    while (true) {
        wait(w);
        cout<<"Writer " <<id<<" is writing\n";
        wait(l);
        sum--;
        if (sum==0) signal(l);
        signal(w);
        cout<<"Writer " <<id<<" finished writing\n";
        signal(w);
    }
}
int main() {
    reader();
    writer();
}
```

Sol to Reader/Writer Prob using Msp Passing

```
#include<iostream>
using namespace std;
message msg;
void reader(int i) {
    while (true) {
        msg = 1;
        cout<<"Reader " <<i<<" is reading\n";
        cout<<"Reader " <<i<<" finished reading\n";
    }
}
void writer(int i) {
    while (true) {
        msg = 2;
        cout<<"Writer " <<i<<" is writing\n";
        cout<<"Writer " <<i<<" finished writing\n";
    }
}
```

CHAPTER 6: DEADLOCK AND STARVATION

Deadlock: permanent blocking of set of procs that compete for sys resources or comm w/ each other. Set is deadlocked when each proc in the set is blocked waiting for event that can only be triggered by another blocked process in the set

Resource categories:

- Reusable: can be safely used by only proc at a time & isn't depleted after use (processors, I/O channels, main & secondary mem, devices & data structs)
- Consumable: created/produced & destroyed/consumed. Inters, sigs, msgs, & info in I/O buff

Deadlock Conditions:

- **Mutual Exclusion:** one lone process may use resource at a time. If access to resource required it, then it must be supported by OS
- **Hold-&-Wait:** proc may hold allocated resources while awaiting asstn of others. Requires proc to request all required resources at one tie & blocking til all requests can be granted simultaneously
- **No pre-emption:** no resource can be forcibly removed from proc holding it; if proc holding certain resources is denied further request, it must first release original resources & request them again
- **Circular wait:** closed chain of procs exists, s.t. each proc hold ≥ 1 resource needed by next proc in chain. Define linear ordering of resource types.

Resource Allocation Graphs:

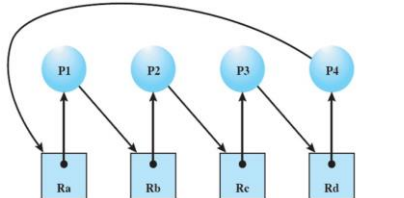
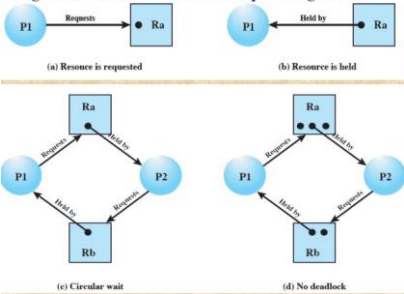


Figure 6.6 Resource Allocation Graph for Figure 6.1b



Detection, Prevention & Avoidance

- Prevention:**
- conservative: undercommits resources & imposes restricts. Policies to elim condition
 - Requesting all resources at once
 - Adv: works well for procs performing single activity burst. No preempt needed
 - Disadv: inefficient, delays proc init, future resource reqs must by known
 - Prevention Strats:
 - o Indirect: prevents occurrence of 1 of 3 necessary conditions
 - o Direct: prevent occurrence of circular wait
 - Pre-emption
 - Adv: convenient when applied to resources whose state saved & stored easily
 - Disadv: preempts more often than necessary
 - Resource ordering
 - Adv: feasible to enforce via compile-time checks, needs no run-time comp since prod solved in sys design
 - Disadv: disallows incremental resource requests

Avoidance: Midway b/w detection & prevention. Make dynamic choices based on state of resource allocation

- Manip to find at least 1 safe path

- Adv: no pre-empt and rollback processes needed
 - Disadv: future resource reqs must be known by OS, procs can be blocked for long periods, proc under consideration must be independent & no sync reqs, fixed # of resources to allocate, no proc may exit while holding resources
 - Resource Allocation Denial (Bankers Algo): doesn't grant an incremental resource request to proc if might lead to deadlock
 - Process Init Denial: doesn't start proc if its demands might lead to deadlock
- Detection:** very liberal; requesting resources are granted where possible. Detect & take action to recover
- Invokes periodically tested for deadlock
 - Adv: never delays proc init, facilitates online handling. Disadv: preempt loss & can consume considerable processor time

Deadlock Detection Algorithm:

1. Mark each process that has a row in the allocation matrix of all zero.
2. Initialize a temporary vector W equal to the available vector.
3. Find index i s.t. i is unmarked & i th row of $Q \leq W$. If not found, terminate
4. If such a row is found, mark process i & add the corresponding row of the allocation matrix to W.

Peterson's Algorithm (Correct Solution taken from slides):

Need to observe state of both processes, which has right to insist on entering into CS

```
boolean flag[2];
int turn;

Process 0 {
    flag[0] = TRUE;
    turn = 1;
    while (flag[1] && turn == 1)
        /* do nothing */;
    /* CS */
    flag[0] = FALSE;
}

Process 1 {
    flag[1] = TRUE;
    turn = 0;
    while (flag[0] && turn == 0)
        /* do nothing */;
    /* CS */
    flag[1] = FALSE;
}
```

Bankers Algo

C = Claim matrix, A = Allo matrix,
R = Resource vector, A = available vector

$\begin{bmatrix} 3 & 2 & 2 \\ 6 & 1 & 3 \\ 2 & 1 & 4 \\ 4 & 2 & 2 \end{bmatrix}$ $\begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 1 \\ 0 & 0 & 1 \\ 0 & 0 & 2 \end{bmatrix}$ $\begin{bmatrix} 2 & 2 & 2 \\ 0 & 0 & 1 \\ 0 & 0 & 1 \\ 4 & 2 & 0 \end{bmatrix}$ $R = \begin{bmatrix} 9 & 3 & 6 \end{bmatrix}$

$V = R - A = \begin{bmatrix} 9 & 3 & 6 \end{bmatrix} - \begin{bmatrix} 9 & 2 & 5 \end{bmatrix} = \begin{bmatrix} 0 & 1 & 1 \end{bmatrix}$

C-A_row $\leq V$, True, then $V = V + A_row$
Safe State $\rightarrow V = R$

C-A-1: $\begin{bmatrix} 2 & 2 & 2 \end{bmatrix} > \begin{bmatrix} 0 & 1 & 1 \end{bmatrix}$ so fail
C-A-2: $\begin{bmatrix} 0 & 0 & 1 \end{bmatrix} < \begin{bmatrix} 0 & 1 & 1 \end{bmatrix}$ so, new $V = \begin{bmatrix} 0 & 1 & 1 \end{bmatrix} + \begin{bmatrix} 6 & 1 & 2 \end{bmatrix} = \begin{bmatrix} 6 & 2 & 3 \end{bmatrix}$
C-A-3: $\begin{bmatrix} 1 & 0 & 3 \end{bmatrix} < \begin{bmatrix} 6 & 2 & 3 \end{bmatrix}$ so, new $V = \begin{bmatrix} 6 & 2 & 3 \end{bmatrix} + \begin{bmatrix} 2 & 1 & 1 \end{bmatrix} = \begin{bmatrix} 8 & 3 & 4 \end{bmatrix}$
C-A-4: $\begin{bmatrix} 4 & 2 & 0 \end{bmatrix} < \begin{bmatrix} 8 & 3 & 4 \end{bmatrix}$ so, new $V = \begin{bmatrix} 8 & 3 & 4 \end{bmatrix} + \begin{bmatrix} 0 & 0 & 2 \end{bmatrix} = \begin{bmatrix} 8 & 3 & 6 \end{bmatrix}$

Since not all process were finished, go back to failed P1:
C-A-1: $\begin{bmatrix} 2 & 2 & 2 \end{bmatrix} < \begin{bmatrix} 8 & 3 & 6 \end{bmatrix}$, so new $V = \begin{bmatrix} 8 & 3 & 6 \end{bmatrix} + \begin{bmatrix} 1 & 0 & 0 \end{bmatrix} = \begin{bmatrix} 9 & 3 & 6 \end{bmatrix}$

$R = V \geq \text{TRUE} !$

Given the following matrices Q and A, and the available vector V, calculate the R vector and run the deadlock detection algorithm to determine the processes that are deadlocked.

$Q = \begin{bmatrix} 1 & 1 & 0 \\ 0 & 1 & 0 \\ 1 & 0 & 2 \\ 1 & 1 & 2 \\ 1 & 0 & 0 \end{bmatrix}$ $A = \begin{bmatrix} 0 & 1 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 1 \\ 0 & 1 & 0 \end{bmatrix}$ $V = \begin{bmatrix} 1 & 1 & 0 \end{bmatrix}$

R vector = resource vector. Obtain by adding vectors A+V
 $R = \begin{bmatrix} 2 & 2 & 2 \end{bmatrix} + \begin{bmatrix} 1 & 1 & 0 \end{bmatrix} = \begin{bmatrix} 3 & 4 & 2 \end{bmatrix}$

Check A to find row of all 0's & mark. If no, continue.
Find a row in Q \leq allocation vector V.

Applicable to P1:
 $V = \begin{bmatrix} 1 & 1 & 0 \end{bmatrix} + \begin{bmatrix} 0 & 1 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 2 & 0 \end{bmatrix}$
 $\begin{bmatrix} 1 & 1 & 0 \end{bmatrix}$ is original vector V, $\begin{bmatrix} 1 & 2 & 0 \end{bmatrix}$ is a row from A.

$\begin{bmatrix} 0 & 0 & 0 \\ 0 & 1 & 0 \\ 1 & 0 & 2 \\ 1 & 1 & 2 \\ 1 & 0 & 0 \end{bmatrix}$ $\begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 1 \\ 0 & 1 & 0 \end{bmatrix}$

Repeat.
Applicable to P2:
 $\begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 1 & 0 & 2 \\ 1 & 1 & 2 \\ 1 & 0 & 0 \end{bmatrix}$ $\begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 1 \\ 0 & 1 & 0 \end{bmatrix}$

Repeat.
Applicable to P5:
 $\begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 1 & 0 & 2 \\ 1 & 1 & 2 \\ 1 & 0 & 0 \end{bmatrix}$ $\begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 1 \\ 0 & 1 & 0 \end{bmatrix}$

If cannot repeat, the remaining process \geq vector V, deadlocked

3. A restaurant has a single employee taking orders and has three seats for its customers. The employee can only serve one customer at a time and each seat can only accommodate one customer at a time. Complete the following function template in a way that guarantees that customers will never have to wait for a seat while holding the food they have just purchased.

```
semaphore seats = 3;
semaphore employee = 1;
void customer (i) {
    semWait(&seats);
    semWait(&employee);
    order_food(i);
    semSignal(&employee);
    eat(i);
    semSignal(&seats);
} // customer
```

a) Set the initial values of the semaphores (5 points).
b) Select the missing instructions after `order_food()` and `eat()` from the following list (15 points):

1. semSignal(&seats);
2. semWait(&employee);
3. semWait(&seats);
4. semSignal(&employee);

Banker's Algorithm for Deadlock Avoidance Date: 03/31/2022

Process	Allocation (A _i)	Max (C _i)	Available (V)
P ₁	0 0 0	1 2 *	1 5 2 0
P ₂	1 0 0	4 *	1 5 2 0
P ₃	2 3 5	4 *	1 5 2 0
P ₄	0 0 3	3 *	1 5 2 0
P ₅	0 1 4	4 *	1 5 2 0

Need (Q):
 $Q = C - A$

Process	Need (Q _i)
P ₁	1 2 0
P ₂	3 4 0
P ₃	2 1 0
P ₄	3 3 0
P ₅	3 3 0

Available vector V: $V = \begin{bmatrix} 1 & 5 & 2 & 0 \end{bmatrix}$

Step-by-step calculations:

- $V = P_1 + V = \begin{bmatrix} 0 & 0 & 2 & 0 \end{bmatrix} + \begin{bmatrix} 1 & 5 & 2 & 0 \end{bmatrix} = \begin{bmatrix} 1 & 5 & 2 & 0 \end{bmatrix}$
- $V = P_2 + V = \begin{bmatrix} 1 & 0 & 0 & 0 \end{bmatrix} + \begin{bmatrix} 1 & 5 & 2 & 0 \end{bmatrix} = \begin{bmatrix} 2 & 5 & 2 & 0 \end{bmatrix}$
- $V = P_3 + V = \begin{bmatrix} 2 & 3 & 5 & 0 \end{bmatrix} + \begin{bmatrix} 2 & 5 & 2 & 0 \end{bmatrix} = \begin{bmatrix} 4 & 8 & 7 & 0 \end{bmatrix}$
- $V = P_4 + V = \begin{bmatrix} 0 & 0 & 3 & 0 \end{bmatrix} + \begin{bmatrix} 4 & 8 & 7 & 0 \end{bmatrix} = \begin{bmatrix} 4 & 8 & 10 & 0 \end{bmatrix}$
- $V = P_5 + V = \begin{bmatrix} 0 & 1 & 4 & 0 \end{bmatrix} + \begin{bmatrix} 4 & 8 & 10 & 0 \end{bmatrix} = \begin{bmatrix} 4 & 9 & 14 & 0 \end{bmatrix}$

Safe Sequence: P_1, P_2, P_3, P_4, P_5

Yes, the system is in a safe state as we are able to determine at least one safe sequence.

Note: P5 is also supposed to be marked with a * This is still bankers, just a different method (don't ask me I didn't do this one)

The following code comes from a previous practice exam, prints in reverse order

```
main.cpp
1 #include <pthread.h>
2 #include <iostream>
3 #include <string.h>
4 #include <stdlib.h>
5 #include <fcntl.h>
6
7 static pthread_mutex_t bsem;
8 static pthread_cond_t waitTurn = PTHREAD_COND_INITIALIZER;
9 static int turn;
10
11 void *print_in_reverse_order(void *void_ptr_argv)
12 {
13     int threadID = *((int*) void_ptr_argv);
14     pthread_mutex_lock(&bsem);
15     while(threadID != turn)
16         pthread_cond_wait(&waitTurn, &bsem);
17     pthread_mutex_unlock(&bsem);
18     std::cout << "I am Thread " << threadID << std::endl;
19     pthread_mutex_lock(&bsem);
20     turn = turn - 1;
21     pthread_cond_broadcast(&waitTurn);
22     pthread_mutex_unlock(&bsem);
23     return NULL;
24 }
25
26 int main()
27 {
28     int nthreads;
29     std::cin >> nthreads;
30     pthread_mutex_init(&bsem, NULL); // Initialize access to 1
31     pthread_t *tid = new pthread_t[nthreads];
32     int *threadNumber = new int[nthreads];
33     turn = nthreads - 1;
34     for(int i=0; i<nthreads; i++)
35     {
36         threadNumber[i] = i;
37         pthread_create(&tid[i], NULL, print_in_reverse_order, &threadNumber[i]);
38     }
39     // Wait for the other threads to finish.
40     for (int i = 0; i < nthreads; i++)
41         pthread_join(tid[i], NULL);
42     delete [] threadNumber;
43     delete [] tid;
44     return 0;
45 }
```

The following code comes from a prev exam. I was told "test to see if this switch from (turn > myTurn to turn == myTurn) works b/c thats how the prof showed me"

```
#include <pthread.h>
#include <iostream>
#include <string.h>
#include <stdlib.h>
#include <fcntl.h>
using namespace std;

static pthread_mutex_t bsem;
static pthread_cond_t waitTurn = PTHREAD_COND_INITIALIZER;
static int turn;

void *print_in_reverse_order(void *void_ptr_argv)
{
    int myTurn = *((int*) void_ptr_argv);
    // As long as its not my turn... WAIT!
    pthread_mutex_lock(&bsem);
    while (turn > myTurn)
        pthread_cond_wait(&waitTurn, &bsem);
    pthread_mutex_unlock(&bsem);

    // If it's my turn, print and decrement turn.
    pthread_mutex_lock(&bsem);
    cout << "I am Thread " << myTurn << endl;
    turn = turn - 1;
    pthread_cond_broadcast(&waitTurn);
    pthread_mutex_unlock(&bsem);

    return NULL;
}

int main()
{
    int nthreads;
    std::cin >> nthreads;
    pthread_mutex_init(&bsem, NULL); // Initialize access to 1
    pthread_t *tid = new pthread_t[nthreads];
    int *threadNumber = new int[nthreads];
    turn = nthreads - 1;
    for(int i=0; i<nthreads; i++)
    {
        threadNumber[i] = i;
        if (pthread_create(&tid[i], NULL, print_in_reverse_order, &threadNumber[i])) {
            cerr << "Failed to create thread";
            return 1;
        }
    }
    // Wait for the other threads to finish.
    for (int i = 0; i < nthreads; i++)
        pthread_join(tid[i], NULL);
    delete [] threadNumber;
    delete [] tid;
    return 0;
}
```