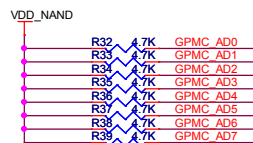
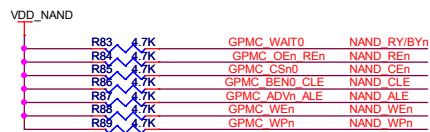
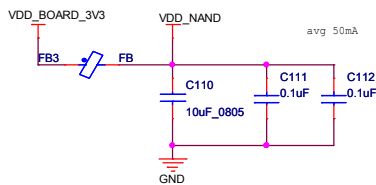


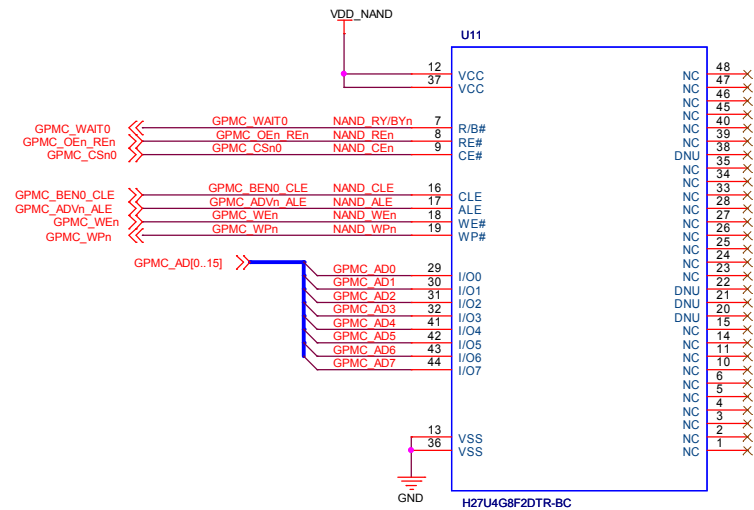
# MINI8600B 120900

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- 05 AM335XZCZ-DDR-NAND
- 06 AM335XZCZ-LCD-UART-I2C-MCASP
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if space careless, serial resistors recommended(22R) on data[0..7]



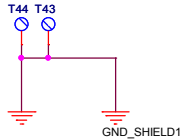
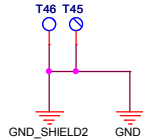
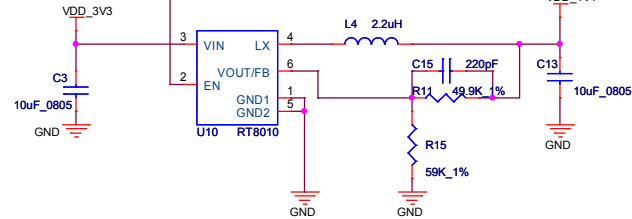
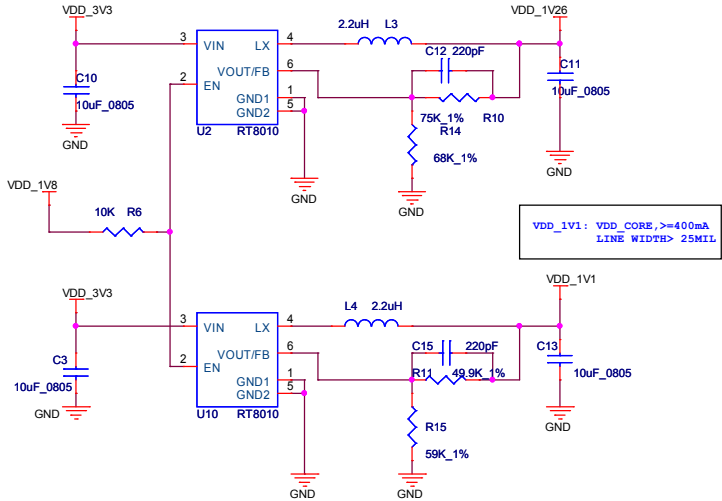
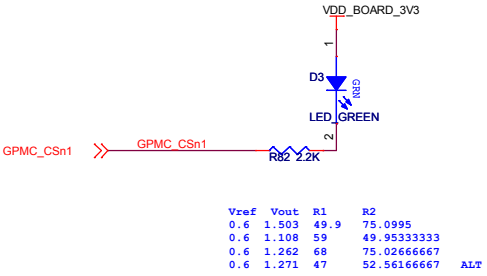
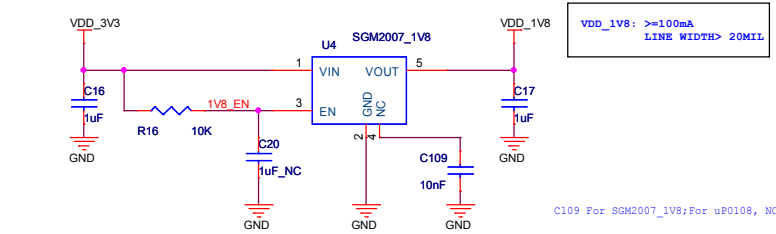
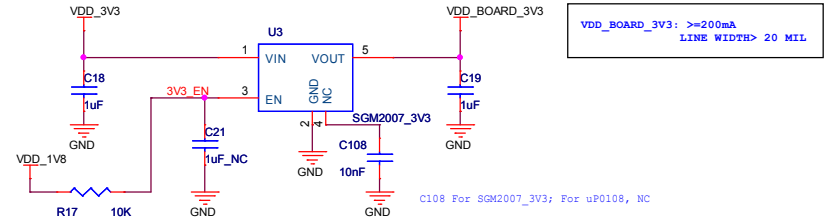
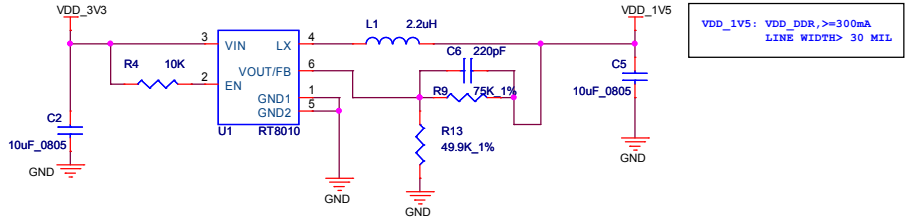
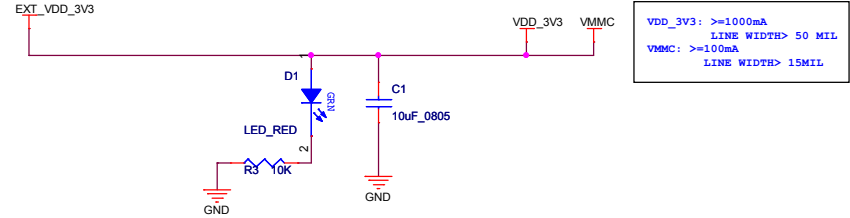
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HARDWARE INSTRUCTION

PCB LAYOUT GUIDLE

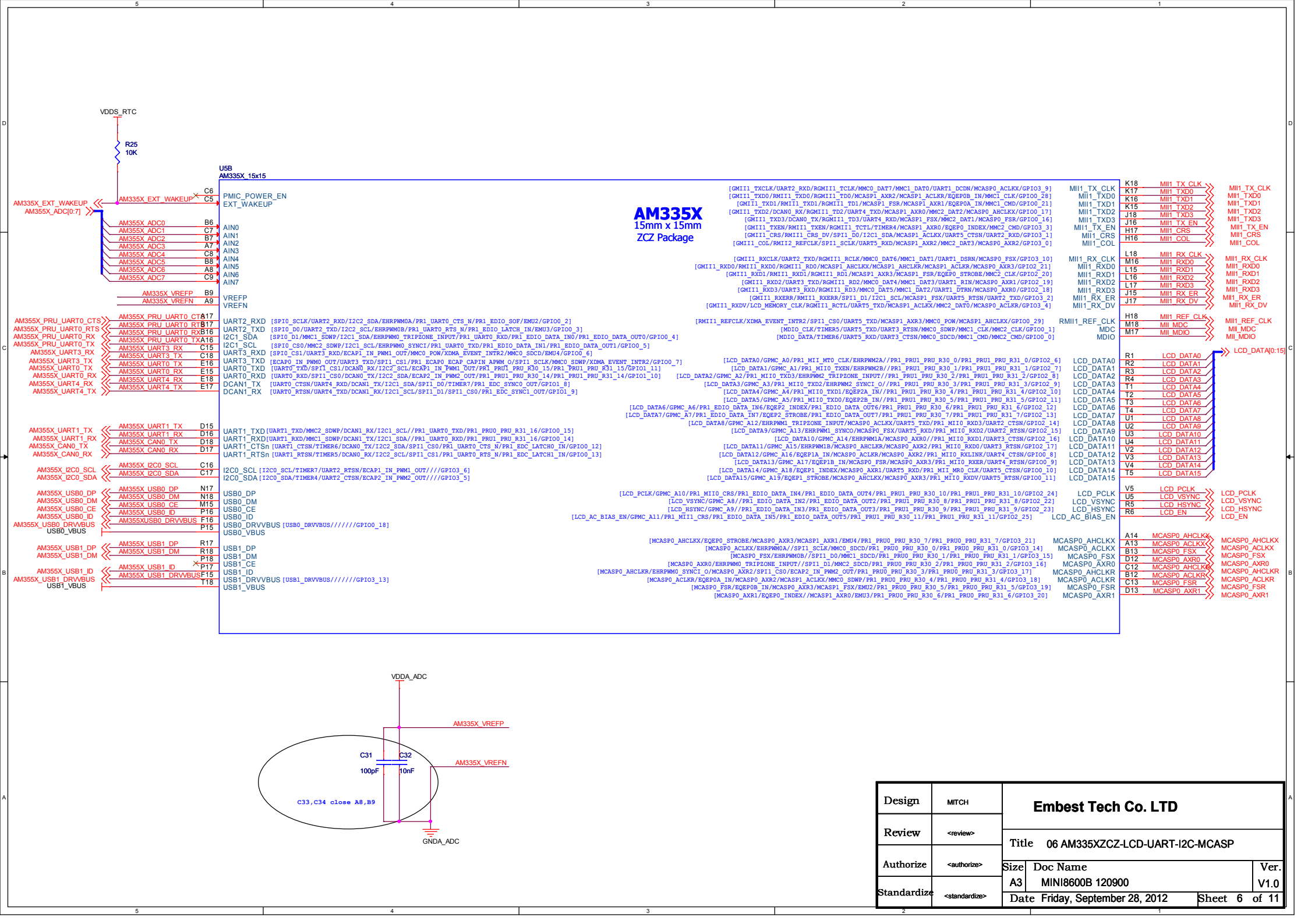
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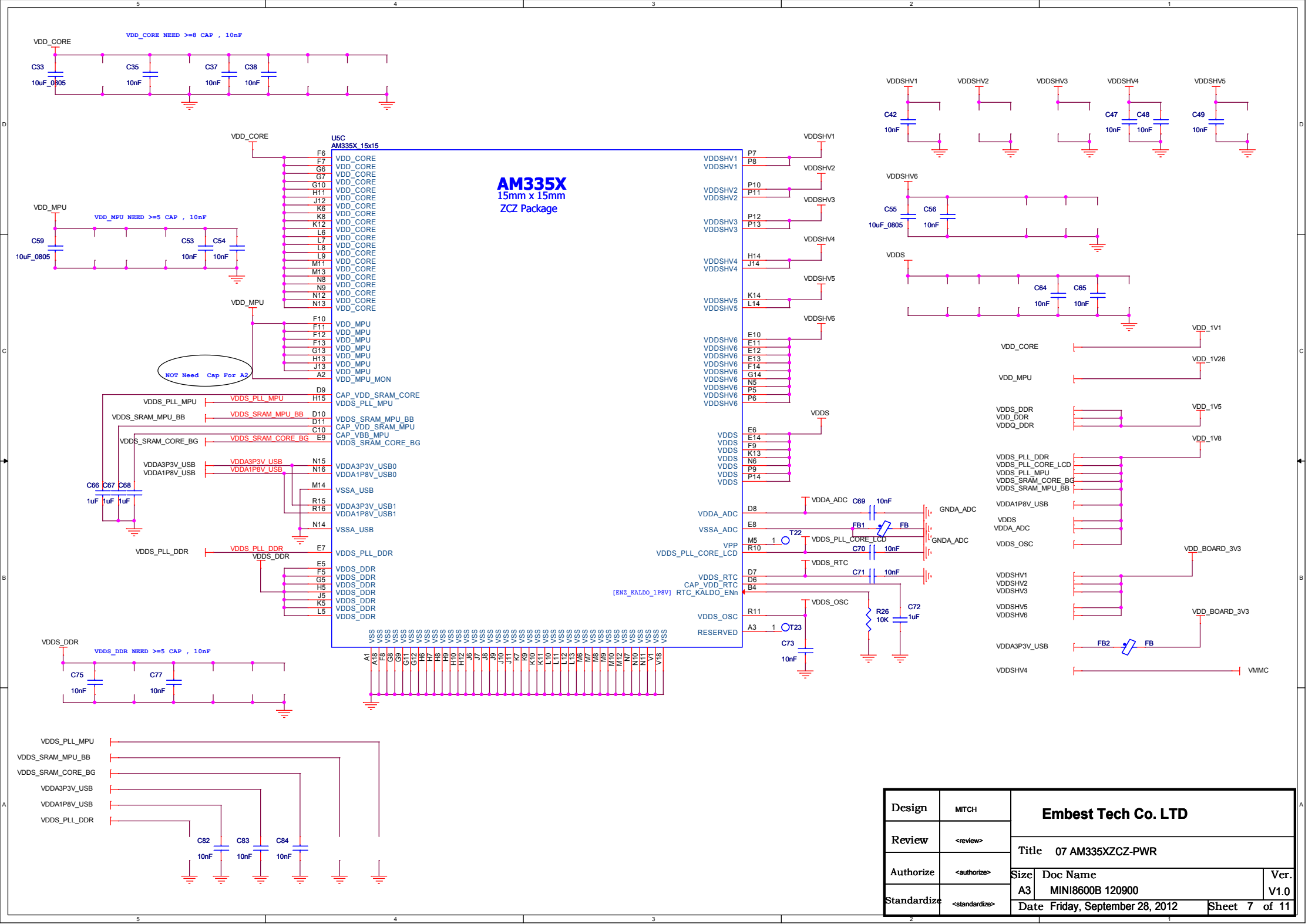
VDD\_3V3: 3.3V, 5%,650mA MIN



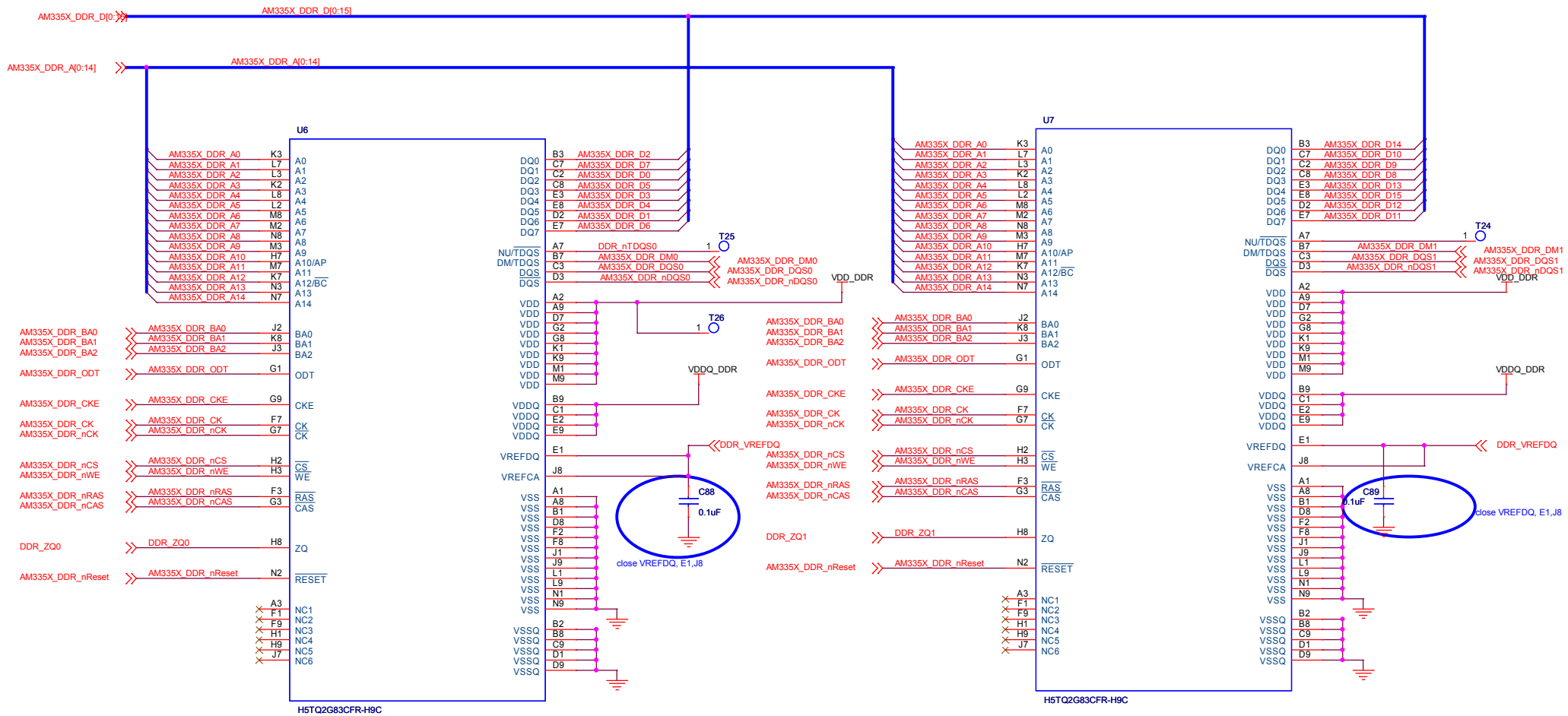
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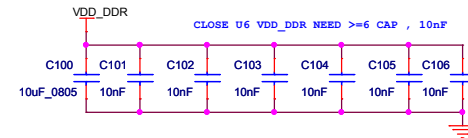
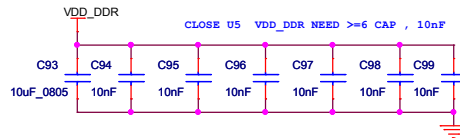
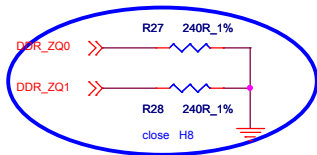
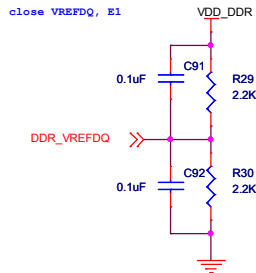




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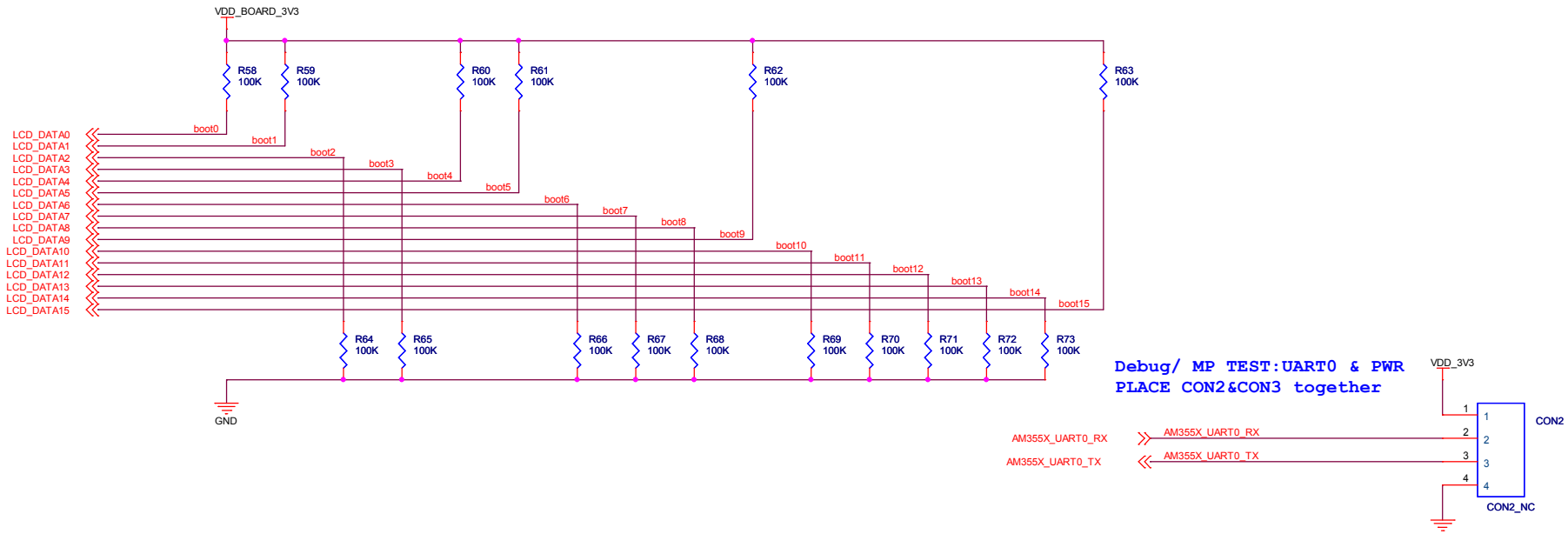
DDR\_VREFDQ LINE WIDTH>20MIL



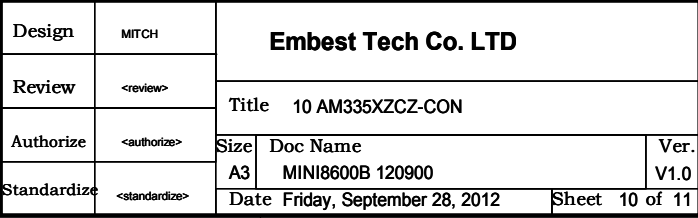
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Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900	Ver. V1.0
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BOOT[15...0]: 1000 0010 0011 0011b  
BOOT[4...0]: 10011 NAND-NANDI2C-MMC0-UART0 || 10111 MMC0->SPI0->UART0->USB0  
BOOT[5]: 1b CLKOUT1=enable  
BOOT[7,6]:00b MII  
BOOT[8]: 0b 8BIT NAND  
BOOT[9]: 1b ECC by NAND  
BOOT[11,10]:00b for NAND  
BOOT[13,12]: 00b by default  
BOOT[15,14]: 10b 25MHz



Design	MITCH	Embest Tech Co. LTD		
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Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 9 of 11	



# MINI8600B Revision History

## MINI8600B 110900

1 Modify From MINI8600 0601, Add Nand;RM TF card .

Design	MITCH	Embest Tech Co. LTD		
Review	<review>	Title 11 REVISION HISTORY		
Authorize	<authorize>	Size A3	Doc Name MINI8600B 120900	Ver. V1.0
Standardize	<standardize>	Date Friday, September 28, 2012	Sheet 11 of 11	