Registers	HEX Code	Key Note
R40	89	Power-up sequence : SWD --> 2ms--> SWAB,SWC --> 2ms--> CAMP pin power good
R41	D9	
R42	0	
R43	0	
R45	78	SWA output setting (1.1V);
R46	61	SWA soft stop: 1mS
R47	78	SWB output setting (1.1V);
R48	60	SWB soft stop: not used in dual-phase operation
R49	78	SWC output setting (1.1V);
R4A	61	SWC soft stop: 1mS
R4B	78	SWD output setting (1.8V);
R4C	60	SWD soft stop: 1mS
R4D	99	DCM, 750KHz of SWA,B
R4E	99	DCM, 750KHz of SWC and SWD
R4F	1	SWA&SWB : Dual-phase operation; SWAB,SWC and SWD: Single ended sensing
R50	FF	Inductor valley over current limit: SWA=SWB=SWC=SWD=3.5A for small PMIC or 5.5A for big PMIC
R51	42	LDOs output setting (1.8V/1.0V); Default SWA/B/C/D output range
R58	D2	Power-down sequence : SWAB,SWC --> 2ms --> SWD
R59	D8	
R5A	0	
R5B	0	
R5D	22	SWAB soft start: 2ms
R5E	22	SWC/SWD soft start: 2ms

Note: Values in other DIMM vendor MTP registers are 0.