



W S P
B J J H Q K T F V
E P X U W Y M C E N X X W
C P Y C D A M E C I C T U D P G G
M C S R U N A J G F G F H W O J R P A
W E R C O S P E T Y B O L I K Y F A E G V
S R E O M U G C I A P L Q L N X U X R D Z
R H U N O E K B D E C J R E S U L T S J C I H
D N T H L M D I I V F S T U P T U O J P B H C
R H W U R I V S Y B A F R W F H A R D W A R E E T
W O N F J N C O K O F N K R O W T E N C Y V D I R
O Y I Z G G Q P R E S E A R C H G E K E A Q U I C
K F C P S I M U L A T I O N T H K C I P A R K Z Y E U
W P D A P S J L J E I L F R T Z N N C O S C U F I I O
P B E R O O J P D D B Z S B Q A G T I A S R Y K N Y K
L D A W K H O O A L E R S M S H D F S B Q C M P M
D O L E W N P C L L C Z R T V W R V A T I W Y U Z
U C L R Z Q R D B Y H O O H T R W N P S A N O T T
S E E H H O A U K F R P F S J K F R D E E E L
S L U G M C B A R A C Q L A P E A O P P O H T
O L P L E K E G L E L I F G Y O G D C H Q
O M F J S P E C A L C U L A T E R O C W G
T Y M S B A C K U P T A P E P A O W O
J L O E T Y B P I H C N E V M O Y
R U B B G I Q I D L H V S
G P S E G E C Z J
E V Z



ARCHER
BACKINGSTORAGE
BACKUPTAPE
BIT
BLADE
BYTE
CABINET
CABLES
CALCULATE
CHIP
CODE

COOLING
CPU
CRAY
DATA
DISC
EPCC
EPSRC
FAST
FILE
FUTURE
GHZ

HARDWARE
HEATSINK
HPC
INPUT
KILOBYTE
LOGIC
MEMORY
NERC
NETWORK
NODE
OUTPUT

PARALLEL
PERFORMANCE
POWER
PREDICT
PROCESSOR
PROGRAM
RESEARCH
RESULTS
SCIENCE
SIMULATION

