



S S A H U C B C  
P K R B P G E R U T U F  
E R C W B Y D K N J Y A M W A B  
E X H V S L O N Q H O S C H I P E L I F  
D L E H E C N A M R O F R E P L F V U A Y L  
G K R Q U R R E S E A R C H L O X P R O G R A M  
H A R D W A R E M D A T A N G R Z A S S C R Z E  
B C A L C U L A T E S C H I M C Z I U T L H H E  
C L Q P R O C E S S O R C T S F M P H C P H O R  
I S X P J E C R G D N I Z A P U E O U T P U T A  
Y O Q O D O A A X A O N H X L R W X S E L B A C  
C V W D Y T W K K E Z K A C B F T B J T E V  
R A E X Y V T A T F C T O Q Z U Y P Y S L T  
Q R Y Y G F W C E I M V Q K T K J F A G  
E G A R O T S O P S C I E N C E L F  
A W R S G N U O Z O Z Y A R C T  
K V X U T L L L Z L Y S T B  
X C E A W Y Z I G Z Y P  
R G U X R P I N G E  
T B W O X X C G  
V X M O Z L  
H E I S  
M P



ARCHER

BYTE

CABLES

CALCULATE

CHIP

CODE

COOLING

CRAY

DATA

FAST

FILE

FUTURE

HARDWARE

LOGIC

MEMORY

NETWORK

OUTPUT

PERFORMANCE

POWER

PROCESSOR

PROGRAM

RESEARCH

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