



U S T  
W X P R O G R A M  
E E R F F D R S R S T U R  
Q K D O E J V C O O N U H A E M F  
T L O K R W R D M S O P Q Q J S E G N  
R S I P U I O E O P I T X G R T E C E E S  
U V T T Q H P T R S U I M E T C A T E W H  
W W N U I M N O S W E R W T L A H  
D T F X Y Z P T N E E C M Y K R O  
D W C Y O D E E P S O N D N C P K D H L B H D D C  
T F L R R J J D K X E O C S T O R A G E T A W K R  
L I G S I M U L A T I O N A T D R E I N V N A P V  
S T L U S E R I D W Y L F T M P W I P F N H Z R I M N  
M N E D A T A D O D Y K C K K E S O F T W A R E H S V  
S D A K Y V Z R M A D L A B Q F M F J K T S N A C T J  
U K Q E K P R T Q S L E L L A R A P B Y F E Y  
J P Y N C K C O O L I N G Y S U B C K Y S  
T E E Q A V H V B R F C F T Y T O R F X N  
P V R Z Z D R E B O  
L I D C W F E F I H J B  
L K P O W Q A L R S E L B A C F C J F C Q  
P O P Q M C Z E C N A M R O F R E P X R C  
V G V D P S S M B J Y R O M E M R V A  
U I Z Y U C A L C U L A T E M O A  
C C Y T I E C N E I C S I  
V J E T G Q Q H O  
R X J



ARCHER  
BYTE  
CABLES  
CALCULATE  
CHIP  
CODE  
COOLING  
CRAY  
DATA  
FAST

FILE  
FUTURE  
HARDWARE  
LOGIC  
MEMORY  
NETWORK  
OUTPUT  
PARALLEL  
PERFORMANCE  
POWER

PROCESSOR  
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RESEARCH  
RESULTS  
SCIENCE  
SIMULATION  
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SPEED  
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