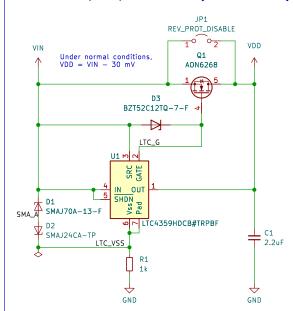
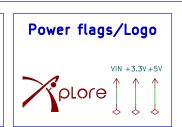
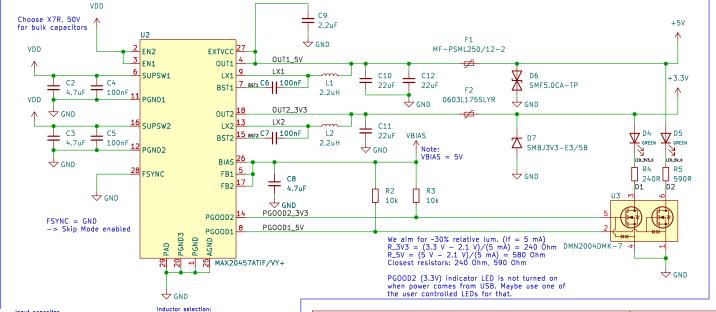


Reverse input protection (ideal diode)





3.3V / 5V Dual-channel switching regulator





Input capacitor IRMS = ILOAD/2 = 6 A / 2 = 3 An = 90% (efficiency) fsw = 2.1 MHz VIN = 29 V Output capacitor: Max overshoot (VSOAR) L = 2.2 uH BUCK 1 IOUT = 3.5 A VOUT = 5 V D = VOUT/(VIN.max * n) = 0.154 (duty cycle) LMIN = (VIN - VOUT)*D / (fsw * IOUT * LIR) BUCK 1: VOUT = 5 V COUT = 22 uF + 22 uF = 44 uF UOAD,max = 3.5 A VSOAR = ILOAD,max^2 * L / (2*COUT*VOUT) = 61.25 mV = 1.68 uH BUCK 2 BUCK 2 IOUT = 2 A VOUT = 3.3 V D = VOUT/(VIN,max * n) = 0.102 (duty cycle) LMIN = (VIN - VOUT)*D / (fsw * IOUT * LIR) = 1.94 uH BUCK 2: YOUT = 3.3V COUT = 22 uF ILOAD,max = 2 A VSOAR = ILOAD,max^2 * L / (2*COUT*VOUT) = 60.61 mV We choose L = 2.2 uH

Nominal input: 4-30 V (5.1 V min for 5V output) Absolute maximum ratings: -40 V to 36 V Output (fused): 5V @ 2.5 A and 3.3 V @ 1.75 A Author: Vincent Nguyen Core EPFL Xplore File: orion power.kicad sch

Title: Meissa v2: 3.3V/5V Switching Regulator

Date: 2023-07-09 KiCad E.D.A. kicad 7.0.1

Project Kerby

Rev: 2

ld: 1/1