«typedef» BasicCostModel Note AccessPattern: # hardware: Hardware Vector<Tuple<AccessType, unsigned int» BasicCostModel defines trivial responses to these # known_data_layouts: Map<String, DataLayout> queries (i.e. return 1;). + «constructor» BasicCostModel() + «constructor» BasicCostModel(hw info: Hardware&) It can be inherited from and query functions «typedef» DevID: unsigned int overridden as we see fit. + getHardware(): Hardware& + addDataLayout(name: String, extent: unsigned int, pattern: AccessPattern&): void + rmDataLayout(name: String): void «typedef» + accessCost(device_id: DevID, data_layout: DataLayout&, access_pattern: AccessPattern&, access_count: unsigned int): Cost Cost: unsigned int + accessCost(device_id: DevID, data_layout: DataLayout&, access_pattern: AccessPattern&, access_count: unsigned int, hardware_info: Hardware&): Cost + movementCost(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&): Cost + movementCost(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&, hardware_info: Hardware&): Cost «enumeration» «enumeration» + movementDecision(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&, access_pattern: AccessPattern&): bool AccessType NetworkType + movementDecision(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&, access_pattern: AccessPattern&, hardware_info: Hardware&): bool **FREE** PART_CONN_GRAPH + recommendDevice(data_layout: DataLayout, access_pattern: AccessPattern&, access_count: unsigned int): DevID BASIC FULL CONN GRAPH + recommendDevice(data_layout: DataLayout, access_pattern: AccessPattern&, access_count: unsigned int, hardware_info: Hardware&): DevID EXPENSIVE STAR BUS RING 0..1 CART l «uses» 0..1 Hardware Access devices: Vector<Device> - PATTERN: const AccessPattern topo: Topology COUNT: const unsigned int DATA_LAYOUT: const DataLayout num_devices: unsigned int + «constructor» Hardware() + «constructor» Access(patt: AccessPattern&, type = CM_BYTE: DataLayout, count = 1: unsigned int) + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double» &, topo_info: unsigned int) + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double»&, topo_info: Tuple<unsigned int, Topology>&) + getReps(): unsigned int + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double» &, topo_info: Graph &) + begin(): AccessPattern::const_iterator + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double»&, old_hw: Hardware&) + end(): AccessPattern::const_iterator + getDeviceName(device_id: unsigned int): String + getNumDevices(): unsigned int + getDevice(id: unsigned int): const Device& + getTopology(): const Topology& 0..* Device Topology DataLayout - next id: DevID topology: Graph<Link> NAME: const String - EXTENT: const unsigned int id: DevID network_type: const NetworkType - NAME: const String - PATTERN: const AccessPattern + «constructor» Topology(num_devices: unsigned int, type = PART_CONN_GRAPH: NetworkType) **BAC:** const Cost + «constructor» DataLayout(name: String, extent: unsigned int, layout: AccessPattern&) + «constructor» Topology(num_devices: unsigned int, old_topo: Topology&) **EAC:** const Cost + «constructor» Topology(Graph<Link>&, type = PART_CONN_GRAPH: NetworkType) CAPACITY: const double + getName(): const String VECTOR_LENGTH: const unsigned int + getNetworkType(): NetworkType + getExtent(): const unsigned int + setLink(IDA: const DevID, IDB: const DevID, link: Link): void + «constructor» Device(name: String, rac: Cost, cac: Cost, cap: double, + getPattern(): const AccessPattern& + unsetLink(IDA: const DevID, IDB: const DevID): void veclen: unsigned int) + linkExists(IDA: const DevID, IDB: const DevID): bool + routeExists(IDA: const DevID, IDB: const DevID): bool + getID(): DevID + getRoute(IDA: const DevID, IDB: const DevID): Vector<Link> + getName(): String + getBasicAccessCost(N: const unsigned int): Cost + getExpensiveAccessCost(N: const unsigned int): Cost + getCapacity(): double + getVectorLength(): unsigned int Link LATENCY: const unsigned int

- INV_BW: const unsigned int

+ getLatency(): unsigned int+ getInverseBW(): unsigned int

+ «constructor» Link(lat: unsigned int, inverse bw: unsigned int)