«typedef» BasicCostModel Note AccessPattern: # hardware: Hardware Vector<Tuple<AccessType, unsigned int» BasicCostModel defines trivial responses to these queries (i.e. return 1;). # known_data_layouts: Map<String, DataLayout> + «constructor» BasicCostModel() It can be inherited from and query functions overridden as we see fit. + «constructor» BasicCostModel(hw info: Hardware&) «typedef» DevID: unsigned int getHardware(): Hardware& + addDataLayout(name: String, extent: unsigned int, pattern: AccessPattern&): void + rmDataLayout(name: String): void «typedef» + accessCost(device_id: DevID, data_layout: DataLayout&, access_pattern: AccessPattern&): Cost Cost: unsigned int + accessCost(device_id: DevID, data_layout: DataLayout&, access_pattern: AccessPattern&, hardware_info: Hardware&): Cost + movementCost(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&): Cost + movementCost(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&, hardware_info: Hardware&): Cost «enumeration» «enumeration» + movementDecision(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&, access_pattern: AccessPattern&): bool NetworkType AccessType + movementDecision(device_A: DevID, data_layout_A: DataLayout&, device_B: DevID, data_layout_B: DataLayout&, access_pattern: AccessPattern&, hardware_info: Hardware&): bool RANDOM PART_CONN_GRAPH + recommendDevice(data_layout: DataLayout, access_pattern: AccessPattern&): DevID CONTIGUOUS FULL_CONN_GRAPH + recommendDevice(data_layout: DataLayout, access_pattern: AccessPattern&, hardware_info: Hardware&): DevID STAR BUS RING 0..1 CART l «uses» 0..1 Hardware Access - PATTERN: const Vector<Tuple<AccessType, unsigned int» devices: Vector<Device> topo: Topology REPS: const unsigned int DATA_LAYOUT: const DataLayout num_devices: unsigned int + «constructor» Access(patt: AccessPattern&, type = CM_BYTE: DataLayout, reps = 1: unsigned int, name = "custom": String) + «constructor» Hardware() «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double»&, topo_info: unsigned int)</p> + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double»&, topo_info: Tuple<unsigned int, Topology>&) + getReps(): unsigned int + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double»&, topo_info: Graph&) + begin(): AccessPattern::const_iterator + «constructor» Hardware(device_info: Vector<Tuple<String, Cost, Cost, double»&, old_hw: Hardware&)</p> + end(): AccessPattern::const_iterator getDeviceName(device_id: unsigned int): String - getNumDevices(): unsigned int - getDevice(id: unsigned int): const Device& + getTopology(): const Topology& Device Topology DataLayout - next_id: DevID - NAME: const String topology: Graph<Link> id: DevID network_type: const NetworkTvpe **EXTENT**: const unsigned int NAME: const String PATTERN: const AccessPattern + «constructor» Topology(num_devices: unsigned int, type = PART_CONN_GRAPH: NetworkType) 0..1 **RAC:** const Cost + «constructor» Topology(num_devices: unsigned int, old_topo: Topology&) + «constructor» DataLayout(name: String, extent: unsigned int, layout: AccessPattern&) CAC: const Cost - «constructor» Topology(Graph<Link>&, type = PART_CONN_GRAPH: NetworkType) - CAPACITY: const double + getName(): const String * «constructor» Device(name: String, rac: Cost, cac: Cost, cap: double) + getExtent(): const unsigned int + getNetworkType(): NetworkType + setLink(IDA: const DevID, IDB: const DevID, link: Link): void + getPattern(): const AccessPattern& + getID(): DevID + unsetLink(IDA: const DevID, IDB: const DevID): void + linkExists(IDA: const DevID, IDB: const DevID): bool + getName(): String + routeExists(IDA: const DevID, IDB: const DevID): bool + getRandomAccessCost(N: const unsigned int): Cost + getContiguousAccessCost(N: const unsigned int): Cost + getRoute(IDA: const DevID, IDB: const DevID): Vector<Link> + getCapacity(): double Link

- LATENCY: const unsigned int- INV_BW: const unsigned int

+ getLatency(): unsigned int + getInverseBW(): unsigned int

+ «constructor» Link(lat: unsigned int, inverse_bw: unsigned int)