SPEC CPU®2017 Integer Speed Result Copyright 2017-2021 Standard Performance Evaluation Corporation My Corporation SPECspeed®2017_int_base `46 SPECspeed®2017 int **Mot Run** Test Date: Oct-2021 CPU2017 License: nnn (Your SPEC license number) Hardware Availability: **Test Sponsor:** My Corporation Tested by: Software Availability: My Corporation 0.100 0.200 0.300 0.400 1.10 1.40 600.perlbench_s 602.gcc_s 605.mcf s 620.omnetpp_s 623.xalancbmk_s 1.46 625.x264 s 32 631.deepsjeng_s 641.leela s 648.exchange2_s 657.xz_s SPECspeed®2017_int_base (1.46) Hardware Software CPU Name: AMD Ryzen Threadripper PRO 3955WX 16-Core OS: Ubuntu 20.04.2 LTS Max MHz: 5.10.0-1044-oem Nominal: Compiler: C/C++/Fortran: Version 9.3.0 (Ubuntu Enabled: cores, 1 chip, threads/core 9.3.0-17ubuntu1~20.04) of GCC, the Orderable: **GNU** Compiler Collection Cache L1: Parallel: No L2: Firmware: L3: File System: nfs System State: Run level 5 (add definition here) Other: 31.188 GB fixme: If using DDR4, the format is: Base Pointers: 64-bit Memory: 'N GB (Nx N GB nRxn PC4-nnnnX-X)' Peak Pointers: Not Applicable 9 TB add more disk info here Storage: Other: **Ø**ther: Power Management: --**Errors** 'reportable' flag not set during run

600.perlbench s (base) did not have enough runs! 641.leela_s (base) did not have enough runs! 657.xz_s (base) did not have enough runs! 648.exchange2_s (base) did not have enough runs! 623.xalancbmk_s (base) did not have enough runs! 602.gcc s (base) did not have enough runs! 605.mcf_s (base) did not have enough runs! 631.deepsjeng_s (base) did not have enough runs! 620.omnetpp_s (base) did not have enough runs! Unknown flags were used! See

(Continued on next page)

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SPECspeed®2017 int peak 7 Not Run

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Errors (Continued)

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl for information about how to get rid of this error.

Results Table

D. I.															
	Base							Peak							
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Sec	ønds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s															
602.gcc_s									7						
605.mcf_s			_					$\Big)$							
620.omnetpp_s								$\left(\right)$							
623.xalancbmk_s								\							
625.x264_s	32	1206	1.46	1211	<u>1.46</u>										
631.deepsjeng_s)/								
641.leela_s				/	//		?								
648.exchange2_s															
657.xz_s					N	1									

SPECspeed*2017_int_base = 146 SPECspeed*2017_int_peak = Not Run

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

"/u/home/het/wer/toolchains/rv64iamc/lib64/:/u/home/hettwer/toolchains/rv64iamc/lib/:/lib64"

Platform Notes

Sysinfo program /u/home/hettwer/cpu2017/bin/sysinfo Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011 running on time-x Thu Oct 14 16:56:10 2021

SUT (System Under Test) info as seen by some common utilities. For more information on this section, see https://www.spec.org/cpu2017/Docs/config.html#sysinfo

(Continued on next page)

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Platform Notes (Continued)

```
From /proc/cpuinfo
   model name: AMD Ryzen Threadripper PRO 3955WX 16-Cores
        "physical id"s (chips)
      32 "processors"
   cores, siblings (Caution: counting these is hw and system dependent. The following
   excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 16
      siblings : 32
                                            9 10 11
      physical 0: cores 0 1 2 (3
                                                     2 13 14 15
From lscpu:
                                       x86_64
     Architecture:
                                        32-bit, 64-bit
     CPU op-mode(s):
                                       Little Endian
     Byte Order:
                                       43 bits physical, 48 bits virtual
     Address sizes:
     CPU(s):
                                       32
     On-line CPU(s) list
                                       0 = 31
     Thread(s) per core
                                       16
     Core(s) per socket
                                       1
     Socket(s):
     NUMA node(s):
                                       1
     Vendor ID:
                                       AuthenticAMD
     CPU family;
                                       23
     Model:
                                       AMD Ryzen Threadripper PRO 3955WX 16-Cores
     Model name:
     Stepping:
                                       enabled
     Frequency boost:
     CPU MHz:
                                       2196.286
     CPU max MHz
                                       4402.7339
     PD min MHx:
                                       2200.0000
     BogoMIPS:
                                       7785.19
     Virtualization:
                                       AMD-V
     Lld cache:
                                       512 KiB
     Mli cache
                                       512 KiB
                                       8 MiB
     L2 cache
     N3 cache:
                                       64 MiB
     NUMA hode0 CPU(s):
                                       0 - 31
     Vulnerability Itlb multihit:
                                       Not affected
     Vulnerability L1tf:
                                       Not affected
     Vulnerability Mds:
                                       Not affected
     Vulnerability Meltdown:
                                       Not affected
     Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
     prctl and seccomp
     Vulnerability Spectre v1:
                                       Mitigation; usercopy/swapgs barriers and __user
     pointer sanitization
     Vulnerability Spectre v2:
                                       Mitigation; Full AMD retpoline, IBPB conditional,
```

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Software Availability:

Platform Notes (Continued)

STIBP conditional, RSB filling

Vulnerability Srbds: Not affected Vulnerability Tsx async abort: Not affected

vgif umip rdpid overflow recov succor smca

Flags:

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Tested by:

fpu vme de pse tsc mar pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mm fxsk sse sse2 ht systall nx mmxext fxsr_opt pdpelgb rdtscp lm constant_tsc rep_good hopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy sym extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tee topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba sev ibpb stibp vmmcall sev_es fsgsbase bmil avx2 smep bmi2 com rdt_a paseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbyl xsaves cqm_11c cqm_occup_11c cqm_mbm_total cqm_mbm_local clzero irperf xsaveer tr rdpry who invd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload

/proc/cpuinfo cache data cache size : 512 KB

WARMING: a numactl 'node' might or might not correspond to a From numactl --hardware physical chip.

From /proc/meminfo

MemTotal: 32703484 KB HugePages_Total: Hugepagesize: 048 kB

/usr/bin/lsb_release Ubunta 20.04.2 LTS

etc/*release*//etc/*version* debian_version bullseye/sid

os-release:

NAME="Ubuncu"

VERSION=/20.04.2 LTS (Focal Fossa)"

ID=uburtu

ID_LIKE=debian

PRETTY_NAME="Ubuntu 20.04.2 LTS"

VERSION_ID="20.04"

HOME_URL="https://www.ubuntu.com/"

SUPPORT_URL="https://help.ubuntu.com/"

uname -a:

Linux time-x 5.10.0-1044-oem #46-Ubuntu SMP Wed Aug 11 09:50:57 UTC 2021 x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

(Continued on next page)

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Platform Notes (Continued)

```
itlb_multihit:
                                           Not affected
CVE-2018-3620 (L1 Terminal Fault):
                                           Not affected
Microarchitectural Data Sampling:
                                           Not affected
CVE-2017-5754 (Meltdown):
                                           Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled
                                           Tia protl and seccomp
CVE-2017-5753 (Spectre variant 1):
                                          tigation usercopy/swapgs barriers and __user
                                          pointer sanitization
                                          Mitigation: Full AMD retpoline, IBPB:
CVE-2017-5715 (Spectre variant (2):
                                           conditional, STIBP: conditional, RSB filling
                                          Not affected
srbds:
                                          Not affected
tsx_async_abort:
run-level 5 Aug 26 10:31
SPEC is set to: /u/home/hettwer/cpu2017
   Filesystem
                                                    Type Size Used Avail Use% Mounted
   nasil10.informatik.tu muenchen.den/erv/il10/home nfs
                                                          6.9T 5.4T 1.3T 82% /u/home
From /sys/devices/virtual/dmi/id
                    $07KT1FA 05/19/2021
    BTOS:
             LENOVO
    Vendor: LENOVO
    Product Family: ThinkStation P620
Cannot run dmidecode: consider saying (as root)
   chmod +s /usr/sbin/dmidecode
        data from sysinfo program)
(End of
```

Compiler Version Notes

```
625.x264_s/base)
Using built in specs.
COLLECT_QC=/u/home/hettwer/toolchains/rv64iamc/bin/riscv64-unknown-linux-gnu-gcc
COLLECT_LTC WRAPPER=/u/home/hettwer/toolchains/rv64iamc/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper
Target: riscv64-unknown-linux-gnu
Configured with:
  /u/home/hettwer/build-rv64iamc/../riscv-gnu-toolchain/riscv-gcc/configure
  --target=riscv64-unknown-linux-qnu
  --prefix=/u/home/hettwer/toolchains/rv64iamc
  --with-sysroot=/u/home/hettwer/toolchains/rv64iamc/sysroot
  --with-system-zlib --enable-shared --enable-tls
```

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Compiler Version Notes (Continued)

- --enable-languages=c,c++,fortran --disable-libmudflap -disable-libssp
- --disable-libquadmath --disable-libsanitizer --disable-nls
- --disable-bootstrap --src=../../riscv-qnu-toolchain/riscv-gcs
- --disable-multilib --with-abi=lp64 --with-arch=rv64imac --with-tune=rocket
- 'CFLAGS_FOR_TARGET=-02 -mcmodel=medlew' CXXFLAGS_FOR_TARGET=-02
- -mcmodel=medlow'

Thread model: posix

Supported LTO compression algorithms: zli

gcc version 11.1.0 (GCC)

Base Unknown Flags

625.x264_s: "/u/home/hettwer/toolchains/rv64ianc/bin/riscv64-unknown-linux-gnu-" (in CC)

- "/u/home/hettwer/toolchains/rv64iamc/bin/risov64-unknown-linux-gnu-" (in LD)
- "-fcommon -static" (in OPTIMIZE)
- "-fno-openmp" (in EXTRA_OPTIMIZE)

Base Runtime Environment

C benchmarks:

625.x264_s: No flags used

Base Compiler Invocation

Cbenchmarks

625.x264_s: gcc

Base Portability Flags

625.x264_s: -DSPEC_LP64

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Base Optimization Flags

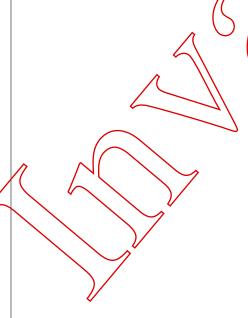
C benchmarks:

625.x264_s: -std=c99 -O3 -fno-unsafe-math-optimizations -DSPEC_SUPPRESS_OPENMP -fno-strict-aliasing fgnu89-inline

Base Other Flags

C benchmarks:

625.x264_s: No flags used



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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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