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## My Corporation

**Test Sponsor:** 

**Tested by:** 

SPECspeed®2017\_int\_base

SPECspeed®2017 int peak 7 **Not Run** 

CPU2017 License: nnn (Your SPEC license number)

My Corporation My Corporation Test Date:

Oct-2021

Hardware Availability: Software Availability:

### Results Table

	Base							Peak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	<u>1135</u>	<u>1.56</u>	1126	1.58					<i>^</i>				
602.gcc_s	32	<u>1069</u>	<u>3.72</u>	1060	3.76									
605.mcf_s	32	<u>1107</u>	<u>4.26</u>	1072	4.40	$\langle \cdot \rangle$			)/					
620.omnetpp_s	32	803	2.03	<u>828</u>	<u>1.97</u>				V					
623.xalancbmk_s	32	<u>546</u>	<u>2.59</u>	537	2.64		//							
625.x264_s	32	1000	1.76	<u>1000</u>	<u>1.76</u>	<i>  (</i> (		$\searrow$						
631.deepsjeng_s	32	<u>853</u>	<u>1.68</u>	(846)	1.69									
641.leela_s	32	1015	1.68	1035	<u>1.65</u>			ノバ						
648.exchange2_s	32	535	5.50	<u>536</u>	5.49									
657.xz_s	32	<u>3560</u>	1,74	3488	17.77									

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Results appear in the order in which they were run. Fold underlined text indicates a median measurement.

### **Submit Notes**

The config file option 'submi

#### Environment Variables Notes

Environment variables set by runcpu before the start of the run: LD\_LIBRARY\_PATH\_=

"/u/home/hettwer/toolchains/rv64q/lib64/:/u/home/hettwer/toolchains/rv64 q/lib/:/lib64"

### **Platform Notes**

Sysinfo program /u/home/hettwer/cpu2017/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011

running on time-x Thu Oct 14 18:30:11 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : AMD Ryzen Threadripper PRO 3955WX 16-Cores

- 1 "physical id"s (chips)
- 32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following

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#### Platform Notes (Continued)

```
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 16
      siblings : 32
      physical 0: cores 0 1 2 3 4 5 6 7/8 9 10 11 12 13 14
From lscpu:
     Architecture:
                                        32/bit, 64-bit
     CPU op-mode(s):
                                       Little Endian
     Byte Order:
                                        43 bits physical, 48 bits virtual
     Address sizes:
                                        32
     CPU(s):
     On-line CPU(s) list:
                                        0-31
                                        2
     Thread(s) per core:
     Core(s) per socket:
                                        16
     Socket(s):
                                        1
     NUMA node(s):
                                        1
     Vendor ID:
                                        AuthenticAMD
     CPU family:
                                        23
     Model:
     Model name:
                                       AMD Ryzen Threadripper PRO 3955WX 16-Cores
     Stepping:
                                        enabled
     Frequency boos
     CPU MHz:
                                        2395.170
     CPU max MHz:
                                        4402.7339
     CPU min MMz
                                        2200.0000
     BogoMIPS:
                                        7785.19
     Virtualization:
                                        AMD-V
                                        512 KiB
     Lld cache:
     Lli/cache
                                        512 KiB
```

8 MiB

cache: 64 MiB MUMA RodeO CPU(s): 0 - 31Vulnerability Itlb multihit: Not affected Vulnerability L1tf: Not affected **V**ulnerabi*l*ity Mds: Not affected Vulnerability Meltdown: Not affected

Wulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via

prot1 and seccomp

L2 cache

Vu**l**merability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user

pointer sanitization

Vulnerability Spectre v2: Mitigation; Full AMD retpoline, IBPB conditional,

STIBP conditional, RSB filling

Not affected Vulnerability Srbds: Vulnerability Tsx async abort: Not affected

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr Flags:

pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr\_opt pdpelgb rdtscp lm constant\_tsc rep\_good nopl nonstop\_tsc cpuid extd\_apicid

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Test Date: Oct-2021 Hardware Availability:

Software Availability:

#### Platform Notes (Continued)

aperfmperf pni pclmulqdq monitor ssse3 fma cx16 sse4\_1 sse4\_2 movbe popcht aes xsave avx f16c rdrand lahf\_lm cmp\_legacy svm extapic cr8\_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr\_core perfctr\_nb bpext perfctr\_llc mwaitx cpb cat\_13 cdp\_13 kw\_pstate sme ssbd mba sev ibpb stibp vmmcall sev\_es fsgsbase bmi1 avx2 smep bmi2 cqm rdt\_a rdseed adx smap clflushopt clwb sha\_ni xsaveopt xsavec xgetbv1 xsaves cqm\_llc cqm\_occup\_llc cqm\_mbm\_total cqm\_mbm\_local clzero irperf xsaveerptr rdpru wbnoinvo arat npt lbrv svm\_lock nrip\_save tsc\_scale vmcb\_clean flushbyasid decodeassists pausefilter pfthreshold avic v\_vmsave\_vmload vgif umip rdpid overflow\_recov succpr smca

```
/proc/cpuinfo cache data
  cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
From /proc/meminfo

MemTotal: 32703484 RB

HugePages_Total: 0

Hugepagesize: 2048 RB
```

/usr/bin/lsb\_release(-d Ubuntu 20.04.2 TIS

x86\_8/4 GNU/Linux

```
From /etc/*release* /etc/*version*
debian_version: bullseye/sid
os-release:
    NAME="Ubuntu"
    VERSION="20.04.2 LTS (Focal Fossa)"
    ID=ubuntu
    ID_LIKE=debian
    PRETTY_NAME="Obuntu 20.04.2 LTS"
    VERSION_ID=*20.04"
    HOME_URL=*https://www.ubuntu.com/"
    SUPPORT_URL="https://help.ubuntu.com/"
uname -a:
```

Kernel self-reported vulnerability status:

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled

Linux t/me-x 5.10.0-1044-oem #46-Ubuntu SMP Wed Aug 11 09:50:57 UTC 2021 x86\_64 x86\_64

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Hardware Availability: Software Availability:

#### Platform Notes (Continued)

```
via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

CVE-2017-5715 (Spectre variant 2):

Mitigation: usercopy/swapgs barriers and __user
pointer sanitization

Mitigation: Full AMD retpoline, IBPB:
conditional, STIBP: conditional, RSB filling
Not affected
tsx_async_abort:

Via prctl and seccomp

Mitigation: usercopy/swapgs barriers and __user
pointer sanitization

Mitigation: Full AMD retpoline, IBPB:
conditional, STIBP: conditional, RSB filling
Not affected

Mot affected
```

run-level 5 Aug 26 10:31

SPEC is set to: /u/home/hettwer/cpu2017

Filesystem
on
nasil10 informatik tu-manchen de:/ery

Type Size Used Avail Use% Mounted

nasil10.informatik.tu-wuenchen.de:/srv/il10/home nfs 6.9T 5.4T 1.3T 82% /u/home from /sys/devices/virtual/dmi/id

BIOS: LENOVO S07KT1FA 05/19/2021 Vendor: LENOVO Product Family: ThinkStation P620

Cannot run dmidecode; consider saying (as root) chmod +s /usr/sbin/dmidecode

(End of data from systinfo program)

### **Compiler Version Notes**

```
C | 600 perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)

Deing built-in spece.

COLLECT_GCC_/u\home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-gcc

COLLECT_LTO_WRAPPER=/u/home/hettwer/toolchains/rv64g/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper

Target: riscv64-unknown-linux-gnu

Configured with:

/u/home/hettwer/build-rv64g/../riscv-gnu-toolchain/riscv-gcc/configure

--target=riscv64-unknown-linux-gnu

--prefix=/u/home/hettwer/toolchains/rv64g

--with-sysroot=/u/home/hettwer/toolchains/rv64g/sysroot --with-system-zlib

--enable-shared --enable-tls --enable-languages=c,c++,fortran

--disable-libmudflap --disable-libssp --disable-libquadmath

--disable-libsanitizer --disable-nls --disable-bootstrap
```

#### (Continued on next page)

--src=../../riscv-gnu-toolchain/riscv-gcc --disable-multilib --with-abi=lp64d --with-arch=rv64imafdc --with-tune=rocket

'CFLAGS FOR TARGET=-02 -mcmodel=medlow' 'CXXFLAGS FOR TARGET=-02

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Hardware Availability: Software Availability:

### Compiler Version Notes (Continued)

```
-mcmodel=medlow'
Thread model: posix
Supported LTO compression algorithms: zlib
gcc version 11.1.0 (GCC)
_____
                                               620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
        641.leela_s(base)
Using built-in specs.
COLLECT_GCC=/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-g++
COLLECT_LTO_WRAPPER=/u/home/hettwer/toolchains/rv64s/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper
Target: riscv64-unknown-limux-gru
Configured with:
 /u/home/hettwer/build-rv64g/...\riscv-gnu-toolchain/riscv-gcc/configure
  --target=riscv64-unknown-linux-gru
 --prefix=/u/home/hettwer/toolchains/rv64g
  --with-sysroot=/u/home/nettwer/toolchains/rv64g/sysroot --with-system-zlib
 --enable-shared --enable-tlanguages=c,c++,fortran
 --disable-libmudflap --disable-libsp --disable-libquadmath
  --disable-libsanitizer --disable-nls --disable-bootstrap
  --src=../../riscv-gnu-toolchain/riscv-gcc --disable-multilib
  --with-abi=lp64d --with-arch=rv64imafdc --with-tune=rocket
  'CFLAGS_FOR_TARGET=-02
                        -mcmodel=medlow' 'CXXFLAGS_FOR_TARGET=-02
  -mcmodel=medlow'
Thread model: posix
Supported LTO compression algorithms: zlib
gcc version 11.1.0 (GCC)
 ----
Fortran |
         648.exchange2_s(base)
Using built-in specs.
COLLECT_SCC=/14/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-gfortran
COLLECT_LTO_WRAPPER=/u/home/hettwer/toolchains/rv64g/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper
Target: niscv64-unknown-linux-gnu
Configured with:
  /u/home/hettwer/build-rv64g/../riscv-gnu-toolchain/riscv-gcc/configure
  --target=riscv64-unknown-linux-gnu
  --prefix=/u/home/hettwer/toolchains/rv64g
  --with-sysroot=/u/home/hettwer/toolchains/rv64g/sysroot --with-system-zlib
 --enable-shared --enable-tls --enable-languages=c,c++,fortran
  --disable-libmudflap --disable-libssp --disable-libquadmath
  --disable-libsanitizer --disable-nls --disable-bootstrap
  --src=../../riscv-gnu-toolchain/riscv-gcc --disable-multilib
```

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SPECspeed®2017\_int\_base

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### Compiler Version Notes (Continued)

--with-abi=lp64d --with-arch=rv64imafdc --with-tune-rocket

-mcmodel=medlow' 'CXXFLAGS\_FOR\_TARGET=-02 'CFLAGS FOR TARGET=-02

-mcmodel=medlow'

Thread model: posix

Supported LTO compression algorithms: z

gcc version 11.1.0 (GCC)

### Base Unknown Flags

600.perlbench\_s: "/u/home/hettwer/toolchains/ry64g/bin/risev64-unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA OPTIMIZE)

602.gcc\_s: "/u/home/hettwer/toolchains/xv64g/bjh/riscv64-unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolchains/rvo4g/bin/risov64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA\_OPTIMIZE)

605.mcf\_s: "/u/home/het/wer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolohains/ry64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static"(in OPTIMIZE)
"-fno-openmp"(in EXTRA\_OPTIMIZE)

620.omnetpp\_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in CXX)

"/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon static" (in OPTIMIZE)

-fno-openmp" (in EXTRA\_OPTIMIZE)

623.xalancbrok\_s."/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in CXX)

"/w/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

-fcommon -static (in OPTIMIZE)

"-fno-openmp" (in EXTRA\_OPTIMIZE)

625.x264\_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in CC)

"/u/home/hgttwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA OPTIMIZE)

631.deepsjeng\_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-" (in CXX)

"/u/home/hettwer/toolchains/rv64q/bin/riscv64-unknown-linux-qnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA OPTIMIZE)

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### My Corporation

SPECspeed®2017\_int\_base >

SPECspeed®2017\_int\_peak = Not Run

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### Base Unknown Flags (Continued)

641.leela\_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-tnknown-linux-gnu-" (in CXX)

"/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA\_OPTIMIZE)

648.exchange2\_s: "/u/home/hettwer/toolchains/rv64g/bin/xiscv64-unknown-linux-gnu-" (in FC)

"/u/home/hettwer/toolchains/rv64g/bin/riscy64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA\_OPTIMIZE)

657.xz\_s: "/u/home/hettwer/toolchains/ry64g/bin/risev64/unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolchains/rx64g/bih/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA\_OPTIMIZE)

# Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

Fortran benchmarks:

gfortran

### **Base Portability Flags**

600.perlbench\_s: -DSPEC\_LINUX\_X64 -DSPEC\_LP64

602.gcc\_s: -DSPEC\_LP64 605.mcf\_s: DSPEC\_LP64

620.omnetpp\_s: \_DSPEC\_LP64

623.xalancomk\_s: -DSPEC\_LINUX -DSPEC\_LP64

625.x264\_s/DSPEC LP64

631.deepsjeng\_s: -DSPEC\_LP64

641.leela\_s: -DSPEC\_LP64

648.exchange2\_s: -DSPEC LP64

657.xz\_s: -DSPEC\_LP64

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SPECspeed®2017\_int\_base

SPECspeed®2017\_int\_peak Not Run

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### Base Optimization Flags

#### C benchmarks:

-std=c99 -03 -fno-unsafe-math-optimizations -DSPEC\_SUPPRESS\_OPENME

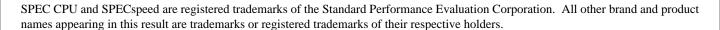
-fno-strict-aliasing -fgnu89-inline

#### C++ benchmarks:

-std=c++03 -03 -fno-unsafe-math-optimization -DSPEC\_SUPPRESS\_OPENMP

#### Fortran benchmarks:

-03 -fno-unsafe-math-optimizations -DSPEC SUPPRESS OPENME



For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU $^{\circ}$ 2017 v1.1.0 on 2021-10-14 18:30:10+0200. Report generated on 2021-10-15 01:09:14 by CPU2017 PDF formatter v6255.

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