

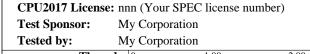
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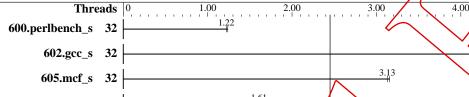
SPECspeed®2017_int_base =

4.72

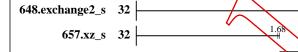
SPECspeed®2017_int_peak > Not Run



Test Date: Oct-2021 Hardware Availability: Software Availability:







Software

Ubuntu 20.04.2 LTS 5.10.0-1044-oem

Compiler: C/C++/Fortran: Version 9.3.0 (Ubuntu

9.3.0-17ubuntu1~20.04) of GCC, the GNU Compiler Collection

Parallel: No

Firmware:

File System: nfs

System State: Run level 5 (add definition here)

Base Pointers: 64-bit

Peak Pointers: Not Applicable

Other:

SPECspeed®2017_int_base (2.45)

OS:

Power Management: --

Hardware

CPU Name: AMD Ryzen Threadripper PRO 3955WX 16-Cores

Max MHz: Nominal:

Enabled: cores, 1 chip, threads/core

Orderable:

Cache L1: L2:

L2. L3: Other:

Memory: 31.188 GB fixme: If using DDR4, the format is:

'N GB (N x N GB nRxn PC4-nnnnX-X)'

Storage: 6.9 TB add more disk info here other:

Errors

'reportable' flag not set during run 625.x264 s (base) bad invalid runs!

Run of 625 x 264 s (base) was not valid; status is VE

Unknown flags were used! See

https://www.spec.org/cpu2017/Docs/runcpu.html#flagsurl

for information about how to get rid of this error.

6.22

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Test Sponsor:

Tested by:

SPECspeed®2017_int_base

SPECspeed®2017 int peak 7 **Not Run**

CPU2017 License: nnn (Your SPEC license number)

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Results Table

	Base							Peak						
Benchmark	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	<u>1455</u>	<u>1.22</u>	1433	1.24					1				
602.gcc_s	32	844	4.72	<u>844</u>	<u>4.72</u>	\wedge								
605.mcf_s	32	<u>1509</u>	<u>3.13</u>	1496	3.16				 					
620.omnetpp_s	32	974	1.67	<u>1014</u>	<u>1.61</u>				V					
623.xalancbmk_s	32	<u>616</u>	<u>2.30</u>	607	2.34									
625.x264_s	32	1116	0.00	1114	0.00	<i> (</i> (\searrow						
631.deepsjeng_s	32	644	2.23	645	<u>2.22</u>									
641.leela_s	32	<u>828</u>	<u>2.06</u>	739	2,31			ノバ						
648.exchange2_s	32	469	6.27	<u>473</u>	6.22									
657.xz_s	32	3641	1.70	<u> 3685</u>	<u>1.68</u>)							

SPECspeed®2017_int_base =

Not Run

SPECspeed®2017_int_peak =

Results appear in the order in which they were run. Fold underlined text indicates a median measurement.

Submit Notes

The config file option 'submi

Environment Variables Notes

Environment variables set by runcpu before the start of the run: LD_LIBRARY_PATH_=

"/u/home/hettwer/toolchains/rv64q/lib64/:/u/home/hettwer/toolchains/rv64 q/lib/:/lib64"

Platform Notes

Sysinfo program /u/home/hettwer/cpu2017/bin/sysinfo

Rev: r6365 of 2019-08-21 295195f888a3d7edb1e6e46a485a0011

running on time-x Tue Oct 12 01:37:42 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

https://www.spec.org/cpu2017/Docs/config.html#sysinfo

From /proc/cpuinfo

model name : AMD Ryzen Threadripper PRO 3955WX 16-Cores

- 1 "physical id"s (chips)
- 32 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following

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Hardware Availability: Software Availability:

```
Platform Notes (Continued)
```

```
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
      cpu cores : 16
      siblings : 32
      physical 0: cores 0 1 2 3 4 5 6 7/8 9 10 11 12 13 14
From lscpu:
     Architecture:
                                       32/bit, 64-bit
     CPU op-mode(s):
                                       Little Endian
     Byte Order:
                                       43 bits physical, 48 bits virtual
     Address sizes:
                                       32
     CPU(s):
     On-line CPU(s) list:
                                       0-31
                                       2
     Thread(s) per core:
     Core(s) per socket:
                                       16
     Socket(s):
                                       1
     NUMA node(s):
                                       1
     Vendor ID:
                                       AuthenticAMD
     CPU family:
                                       23
     Model:
     Model name:
                                       AMD Ryzen Threadripper PRO 3955WX 16-Cores
     Stepping:
                                       enabled
     Frequency boos
     CPU MHz:
                                       1916.143
     CPU max MHz:
                                       4402.7339
     CPU min MMz
                                       2200.0000
     BogoMIPS:
                                       7785.19
     Virtualization:
                                       AMD-V
                                       512 KiB
     Lld cache:
     Lli/cache
                                       512 KiB
     L2 cache
                                       8 MiB
       cache:
                                       64 MiB
    MUMA RodeO CPU(s):
                                       0 - 31
     Vulnerability Itlb multihit:
                                       Not affected
     Vulnerability L1tf:
                                       Not affected
     Vulnerability Mds:
                                       Not affected
     Vulnerability Meltdown:
                                       Not affected
     Wulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via
     prot1 and seccomp
     Vulmerability Spectre v1:
                                       Mitigation; usercopy/swapgs barriers and __user
     pointer sanitization
     Vulnerability Spectre v2:
                                      Mitigation; Full AMD retpoline, IBPB conditional,
     STIBP conditional, RSB filling
                                       Not affected
     Vulnerability Srbds:
     Vulnerability Tsx async abort:
                                       Not affected
                                       fpu vme de pse tsc msr pae mce cx8 apic sep mtrr
     Flags:
     pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt
     pdpelgb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid
```

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Hardware Availability: Software Availability:

Platform Notes (Continued)

aperfmperf pni pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core_perfctr_nb bpext perfctr_llc mwaitx cpb cat_13 cdp_13 kw_pstate sme ssbd mba sev ibpb stibp vmmcall sev_es fsgsbase bmi1 avx2 smep bmi2 cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr rdpru wbnoinvo arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif umip rdpid overflow_recov succpr smca

```
/proc/cpuinfo cache data
  cache size : 512 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
From /proc/meminfo
MemTotal: 32703464 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

/usr/bin/lsb_release -d Ubuntu 20.04.2 TIS

```
From /etc/*release* /etc/*version*
debian_version: bullseye/sid
os-release:
    NAME="Ubuntu"
    VERSION="20.04.2 LTS (Focal Fossa)"
    ID=ubuntu
    ID_LIKE=debian
    PRETTY_NAME="Obuntu 20.04.2 LTS"
    VERSION_ID=*20.04"
    HOME_URL=*https://www.ubuntu.com/"
    SUPPORT_URL="https://help.ubuntu.com/"
uname -a:
```

x86_6/4 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled

Linux t/me-x 5.10.0-1044-oem #46-Ubuntu SMP Wed Aug 11 09:50:57 UTC 2021 x86_64 x86_64

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SPECspeed®2017_int_base

SPECspeed®2017_int_peak_4 Mot Run

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Test Sponsor: My Corporation **Tested by:** My Corporation Test Date: Oct-2021 Hardware Availability:

Software Availability:

Platform Notes (Continued)

```
via prctl and seccomp
                                         Mitigation: usercopy/swapgs barriers and __user
CVE-2017-5753 (Spectre variant 1):
                                           pointer sanitization
                                           Mitigation: Full AMD retpoline, IBPB:
CVE-2017-5715 (Spectre variant 2):
                                           conditional, STIBP; conditional, RSB filling
                                           Not affected
srbds:
                                           ot affected
tsx_async_abort:
run-level 5 Aug 26 10:31
SPEC is set to: /u/home/hettwer/cpu2017
                                                          Size Used Avail Use% Mounted
   Filesystem
                                                     Type
   nasil10.informatik.tu-muenchen.de:/sxv/1110/home nfs
```

6.9T 5.3T 1.4T 80% /u/home

From /sys/devices/virtual/dmi/id BIOS: LENOVO S07KT1FA 05/19/2021

Vendor: LENOVO

Product Family: ThinkStation P620

Cannot run dmidecode; consider saying (as root) chmod +s /usr/sb/in/dmidecode

--disable-libsanitizer --disable-nls --disable-bootstrap --src=../../riscv-gnu-toolchain/riscv-gcc --disable-multilib --with-abi=lp64d --with-arch=rv64imafdc --with-tune=rocket

'CFLAGS FOR TARGET=-02 -mcmodel=medlow' 'CXXFLAGS FOR TARGET=-02

(End of data from systinfo program)

Compiler Version Notes

```
C
          600 perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base)
          625.x264_s(base) 657.xz_s(base)
Using built-in specs.
COLLECT_GCC=/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-gcc
COLLECT LTO_WRAPPER=/u/home/hettwer/toolchains/rv64g/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper
Target: riscv64-unknown-linux-gnu
Configured with:
  /u/home/hettwer/build-rv64q/../riscv-qnu-toolchain/riscv-qcc/configure
  --target/=riscv64-unknown-linux-gnu
  --pref\(\frac{4}{x}\)=/u/home/hettwer/toolchains/rv64g
  --with-sysroot=/u/home/hettwer/toolchains/rv64q/sysroot --with-system-zlib
  --enable-shared --enable-tls --enable-languages=c,c++,fortran
  --disable-libmudflap --disable-libssp --disable-libquadmath
```

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SPECspeed®2017_int_base

SPECspeed®2017_int

Mot Run

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Hardware Availability: Software Availability:

Compiler Version Notes (Continued)

```
-mcmodel=medlow'
Thread model: posix
Supported LTO compression algorithms: zlib
gcc version 11.1.0 (GCC)
_____
                                               620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base)
        641.leela_s(base)
Using built-in specs.
COLLECT_GCC=/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-g++
COLLECT_LTO_WRAPPER=/u/home/hettwer/toolchains/rv64s/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper
Target: riscv64-unknown-limux-gru
Configured with:
 /u/home/hettwer/build-rv64g/...\riscv-gnu-toolchain/riscv-gcc/configure
  --target=riscv64-unknown-linux-gru
 --prefix=/u/home/hettwer/toolchains/rv64g
  --with-sysroot=/u/home/nettwer/toolchains/rv64g/sysroot --with-system-zlib
 --enable-shared --enable-tlanguages=c,c++,fortran
 --disable-libmudflap --disable-libsp --disable-libquadmath
  --disable-libsanitizer --disable-nls --disable-bootstrap
  --src=../../riscv-gnu-toolchain/riscv-gcc --disable-multilib
  --with-abi=lp64d --with-arch=rv64imafdc --with-tune=rocket
  'CFLAGS_FOR_TARGET=-02
                        -mcmodel=medlow' 'CXXFLAGS_FOR_TARGET=-02
  -mcmodel=medlow'
Thread model: posix
Supported LTO compression algorithms: zlib
gcc version 11.1.0 (GCC)
 ----
Fortran |
         648.exchange2_s(base)
Using built-in specs.
COLLECT_SCC=/14/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-gfortran
COLLECT_LTO_WRAPPER=/u/home/hettwer/toolchains/rv64g/libexec/gcc/riscv64-unknown-linux-gnu/11.1.0/lto-wrapper
Target: niscv64-unknown-linux-gnu
Configured with:
  /u/home/hettwer/build-rv64g/../riscv-gnu-toolchain/riscv-gcc/configure
  --target=riscv64-unknown-linux-gnu
  --prefix=/u/home/hettwer/toolchains/rv64g
  --with-sysroot=/u/home/hettwer/toolchains/rv64g/sysroot --with-system-zlib
 --enable-shared --enable-tls --enable-languages=c,c++,fortran
  --disable-libmudflap --disable-libssp --disable-libquadmath
  --disable-libsanitizer --disable-nls --disable-bootstrap
  --src=../../riscv-gnu-toolchain/riscv-gcc --disable-multilib
```

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SPECspeed®2017_int_base

SPECspeed®2017 int **Not Run**

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My Corporation **Test Sponsor:** Tested by: My Corporation Test Date: Oct-2021

Hardware Availability: Software Availability:

Compiler Version Notes (Continued)

--with-abi=lp64d --with-arch=rv64imafdc --with-tune-rocket

-mcmodel=medlow' 'CXXFLAGS_FOR_TARGET=-02 'CFLAGS FOR TARGET=-02

-mcmodel=medlow'

Thread model: posix Supported LTO compression algorithms: z

gcc version 11.1.0 (GCC)

Base Unknown Flags

600.perlbench_s: "/u/home/hettwer/toolchains/ry64g/bin/risev64-unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA OPTIMIZE)

602.gcc_s: "/u/home/hettwer/toolchains/xv64g/bjh/riscv64-unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolchains/rvo4g/bin/risov64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA_OPTIMIZE)

605.mcf_s: "/u/home/het/wer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolohains/ry64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static"(in OPTIMIZE)
"-fno-openmp"(in EXTRA_OPTIMIZE)

620.omnetpp_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in CXX)

"/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon static" (in OPTIMIZE)

-fno-openmp" (in EXTRA_OPTIMIZE)

623.xalancbrok_s."/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in CXX)

"/w/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

-fcommon -static (in OPTIMIZE)

"-fno-openmp" (in EXTRA_OPTIMIZE)

625.x264_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in CC)

"/u/home/hgttwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA OPTIMIZE)

631.deepsjeng_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-" (in CXX)

"/u/home/hettwer/toolchains/rv64q/bin/riscv64-unknown-linux-qnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA OPTIMIZE)

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SPECspeed®2017_int_base =

SPECspeed®2017_int_peak 7 Not Run

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Hardware Availability: Software Availability:

Base Unknown Flags (Continued)

641.leela_s: "/u/home/hettwer/toolchains/rv64g/bin/riscv64-anknown-linux-gnu-" (in CXX)

"/u/home/hettwer/toolchains/rv64g/bin/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA_OPTIMIZE)

648.exchange2_s: "/u/home/hettwer/toolchains/rv64g/bin/xiscv64-unknown-linux-gnu-" (in FC)

"/u/home/hettwer/toolchains/rv64g/bin/riscy64-unknown-linux-gnu-" (in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA_OPTIMIZE)

657.xz_s: "/u/home/hettwer/toolchains/ry64g/bin/risev64/unknown-linux-gnu-" (in CC)

"/u/home/hettwer/toolchains/rv64g/bih/riscv64-unknown-linux-gnu-"(in LD)

"-fcommon -static" (in OPTIMIZE)

"-fno-openmp" (in EXTRA_OPTIMIZE)

Base Compiler Invocation

C benchmarks:

gcc

C++ benchmarks:

g++

Fortran benchmarks:

gfortran

Base Portability Flags

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64

602.gcc_s: -DSPEC_LP64 605.mcf_s: DSPEC_LP64

620.omnetpp_s: DSPEC_LP64

623.xalancomk_s: -DSPEC_LINUX -DSPEC_LP64

625.x264_s/DSPEC LP64

631.deepsjeng_s: -DSPEC_LP64

641.leela_s: -DSPEC_LP64

648.exchange2_s: -DSPEC LP64

657.xz_s: -DSPEC_LP64

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SPECspeed®2017_int_base =

SPECspeed®2017 int peak 7 Not Run

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Test Date: Oct-2021

Hardware Availability: Software Availability:

Base Optimization Flags

C benchmarks:

-std=c99 -03 -fno-unsafe-math-optimizations -DSPEC_SUPPRESS_OPENME

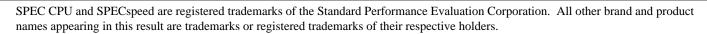
-fno-strict-aliasing -fgnu89-inline

C++ benchmarks:

-std=c++03 -O3 -fno-unsafe-math-optimization -DSPEC_SUPPRESS_OPENMP

Fortran benchmarks:

-O3 -fno-unsafe-math-optimizations -DSPEC_SUPPRESS_OPENME



For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.0 on 2021-10-12 01:37:42+0200. Report generated on 2021-10-12 08:22:51 by CPU2017 PDF formatter v6255.

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