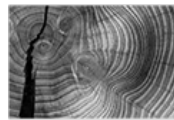
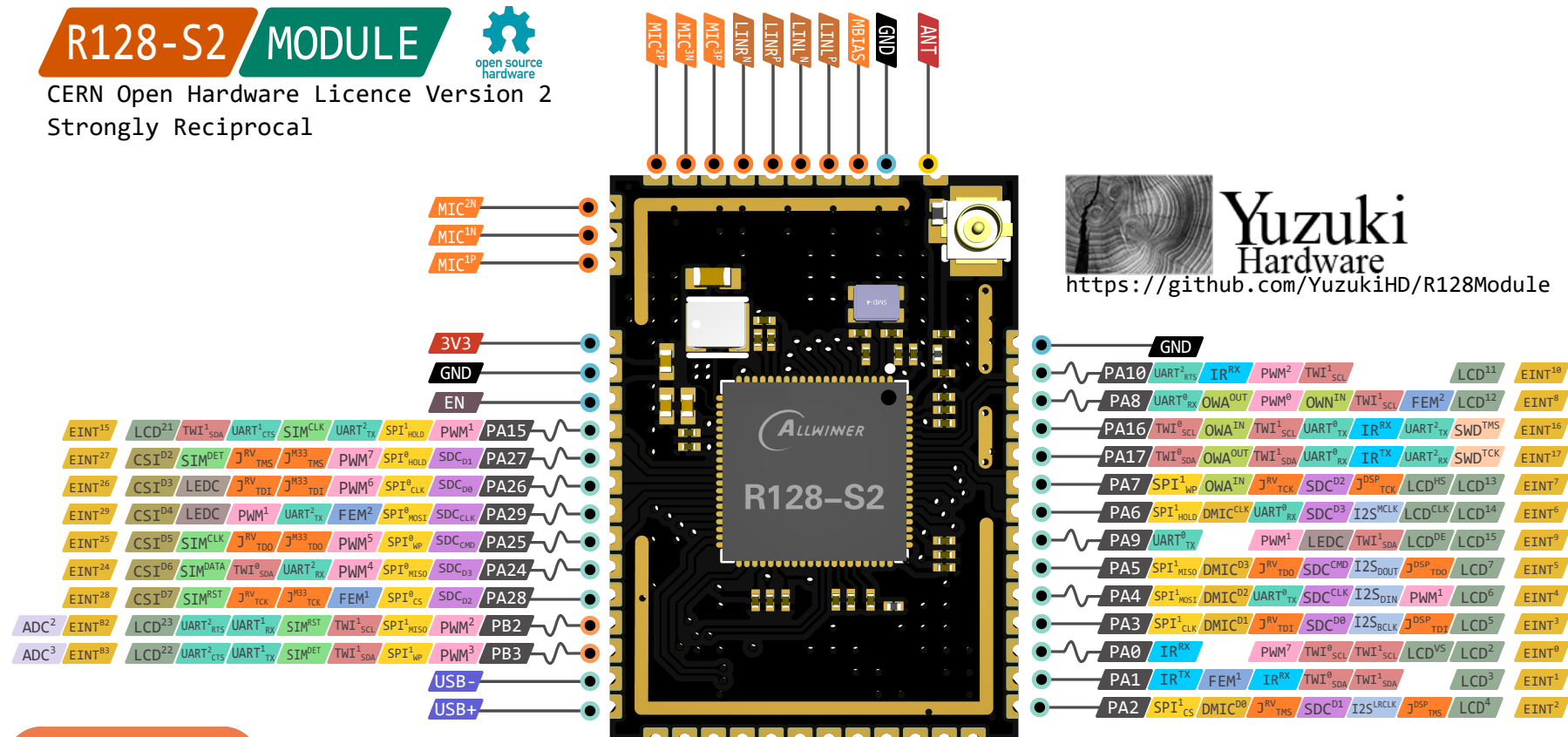


**R128-S2** **MODULE**  
CERN Open Hardware Licence Version 2  
Strongly Reciprocal

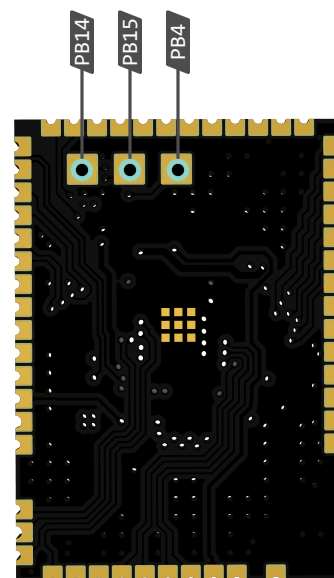
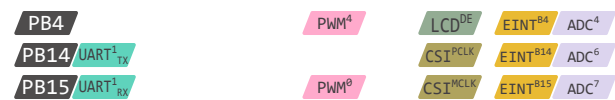
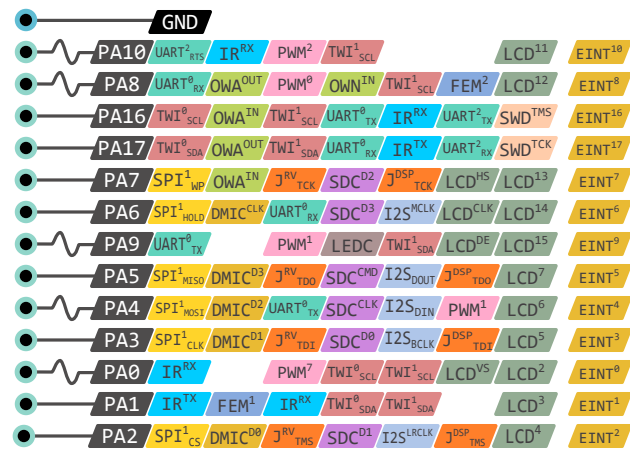
## CERN Open Hardware Licence Version 2 Strongly Reciprocal
























# Yuzuki

## Hardware

<https://github.com/YuzukiHD/R128Module>



- |   |  |  |
|---|--|--|
|  | Pulse Width Modulation (PWM)           |  |
|  | Secure Digital Input and Output (SDIO) |  |
|  | Serial Peripheral Interface (SPI)      |  |
|  | Two Wire Interface (TWI)               |  |
|  | Serial UART                            |  JTAG             |
|  | IC Card Reader                         |  IR               |
|  | DMIC                                   |  DVP CSI          |
|   |  |  ADC              |
|  | I2S                                    |  RGB / i80 LCD    |
|  | External Interrupt                     |  SWD              |
|  | Intelligent Control LED                |  One Wire Audio   |
|  | Front End Module Control               |  |
|  | Internal Clock Output                  |  |
|  | External 32K OSC                       |  External Wake Up |

