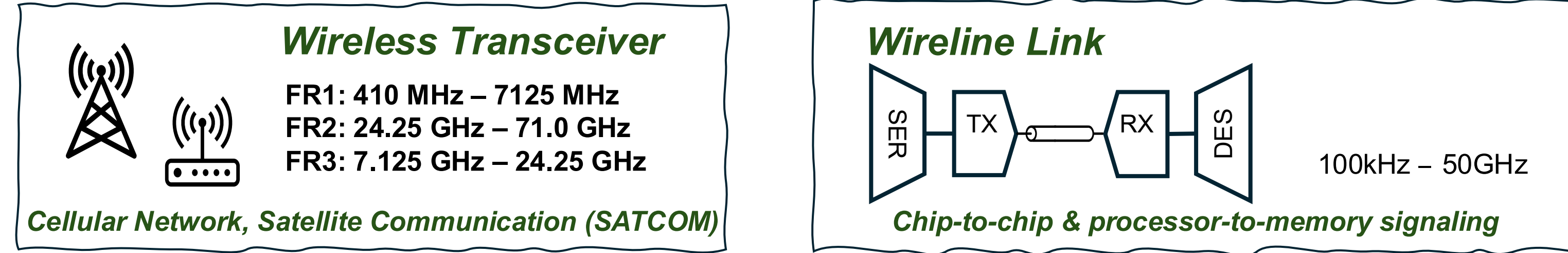
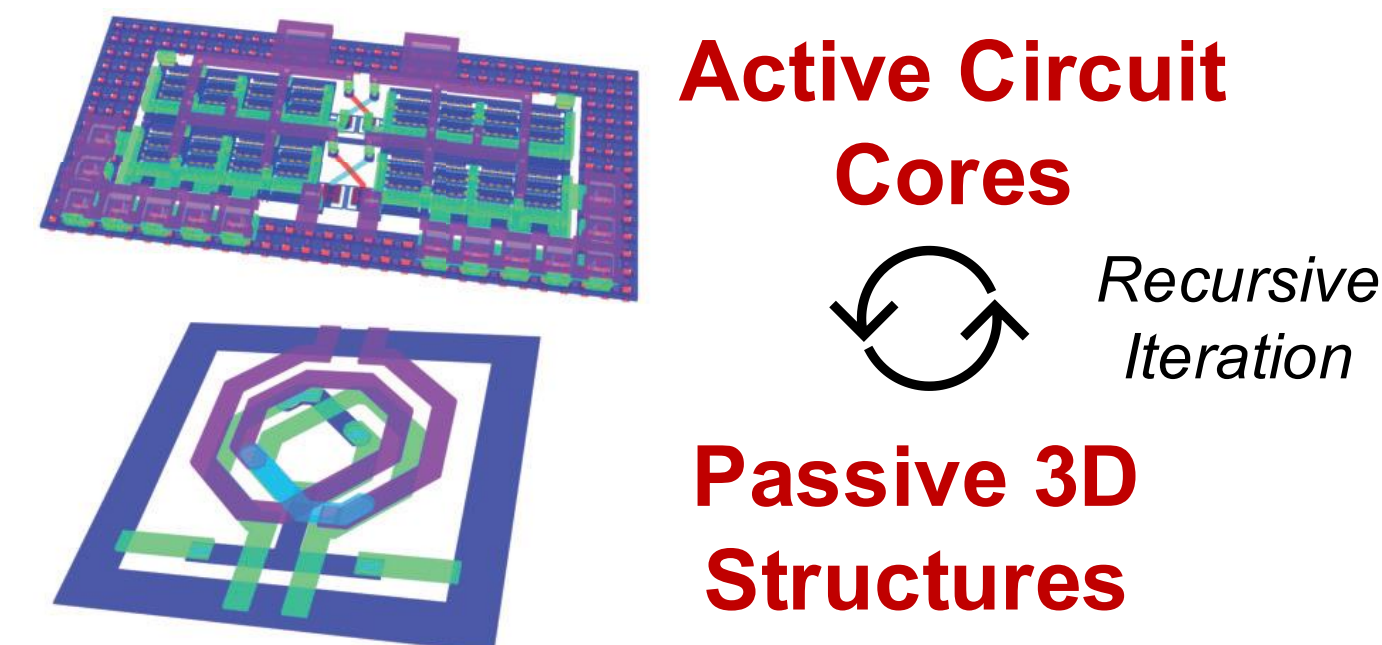


1. Introduction

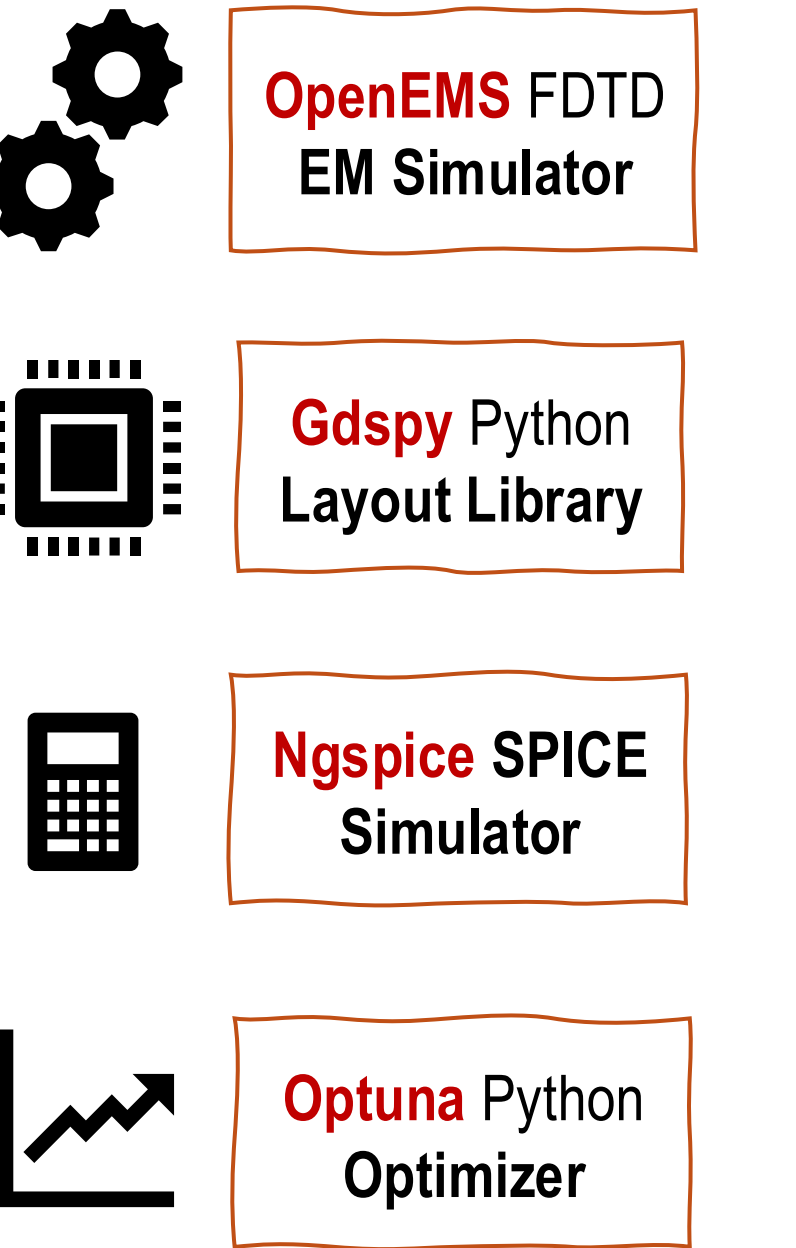
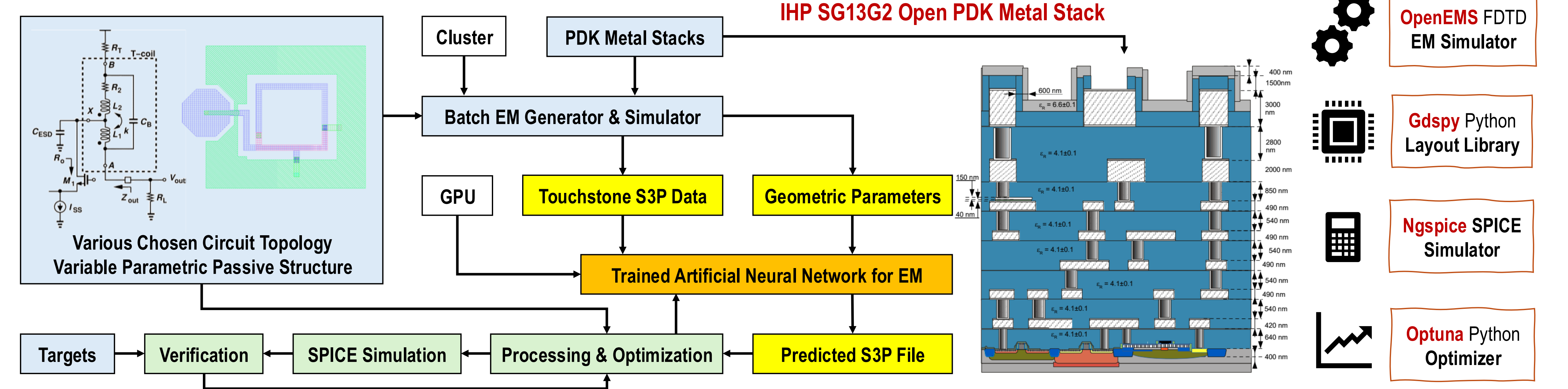


Challenges of High Frequency RFIC Design

- Variable **active** circuit topology
- Costly **passive** EM simulation
- Different frequency range configuration
- Recursive iteration for tuning & optimization



2. Proposed Workflow



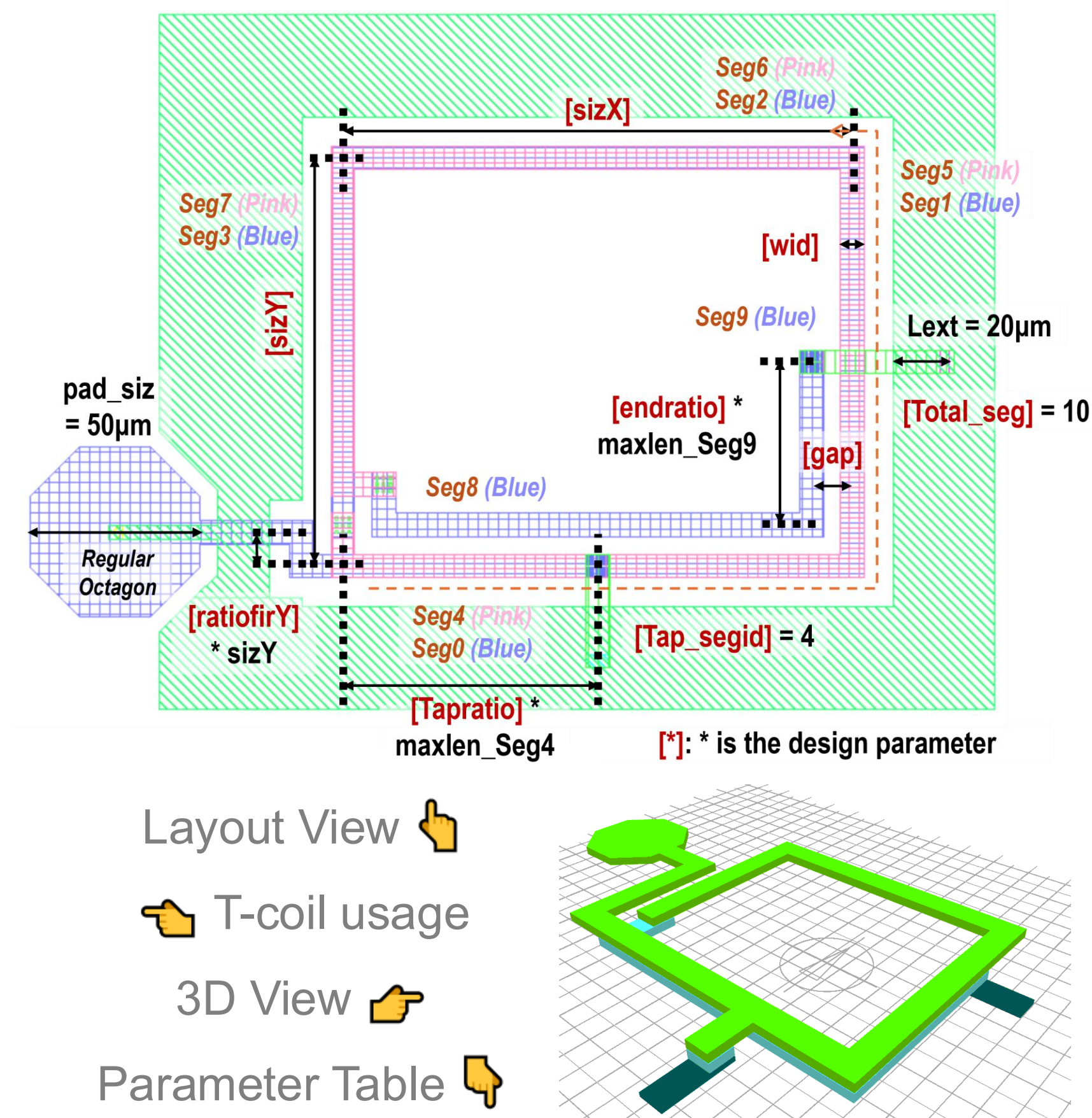
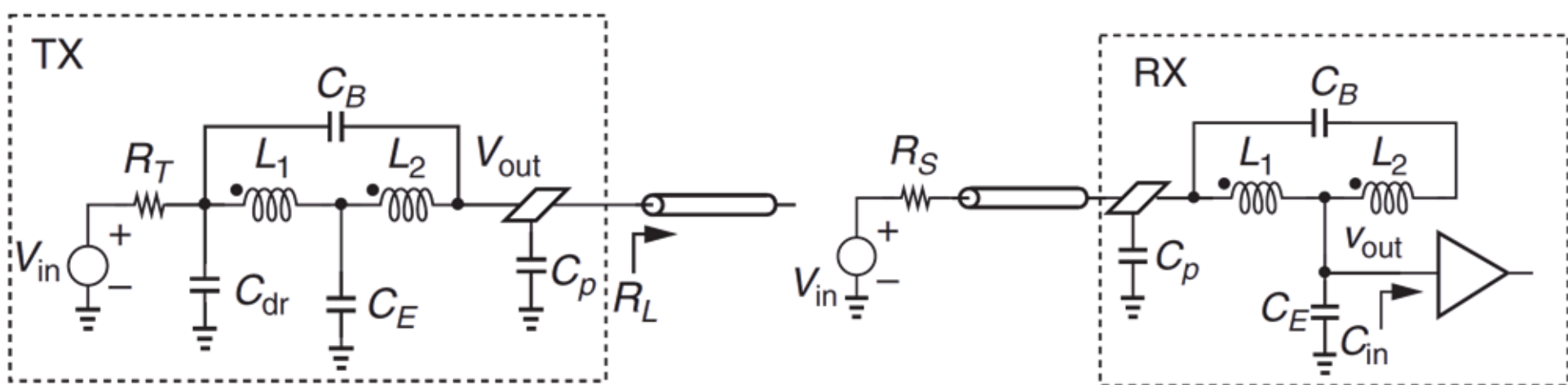
3. Design Examples: T-Coil Peaked Wireline Driver

Traditional Design Challenges

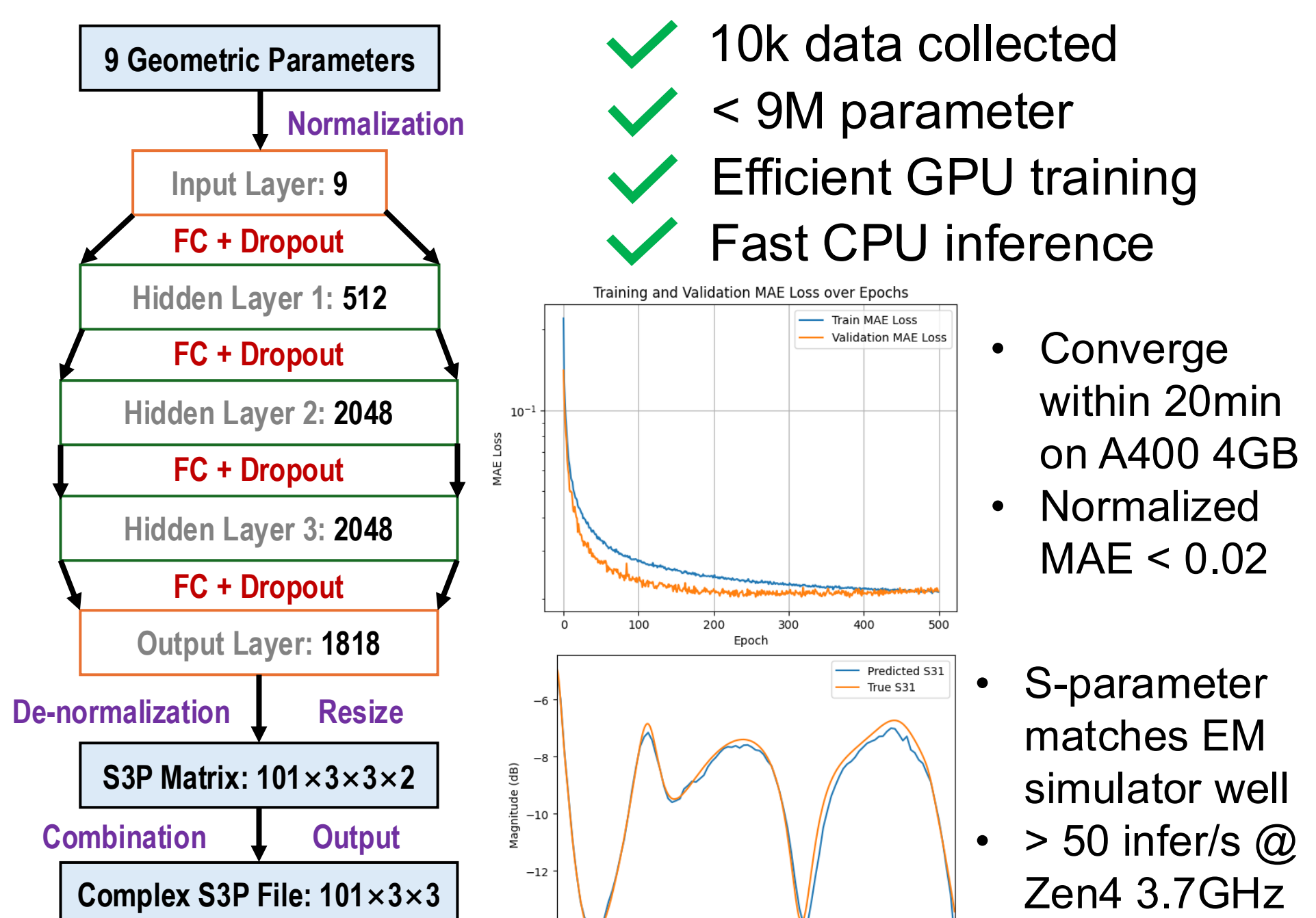
- Low-order **multi-port passive modeling**
- Iterative design** between active & passive

AI-Driven Automatic Design Flow

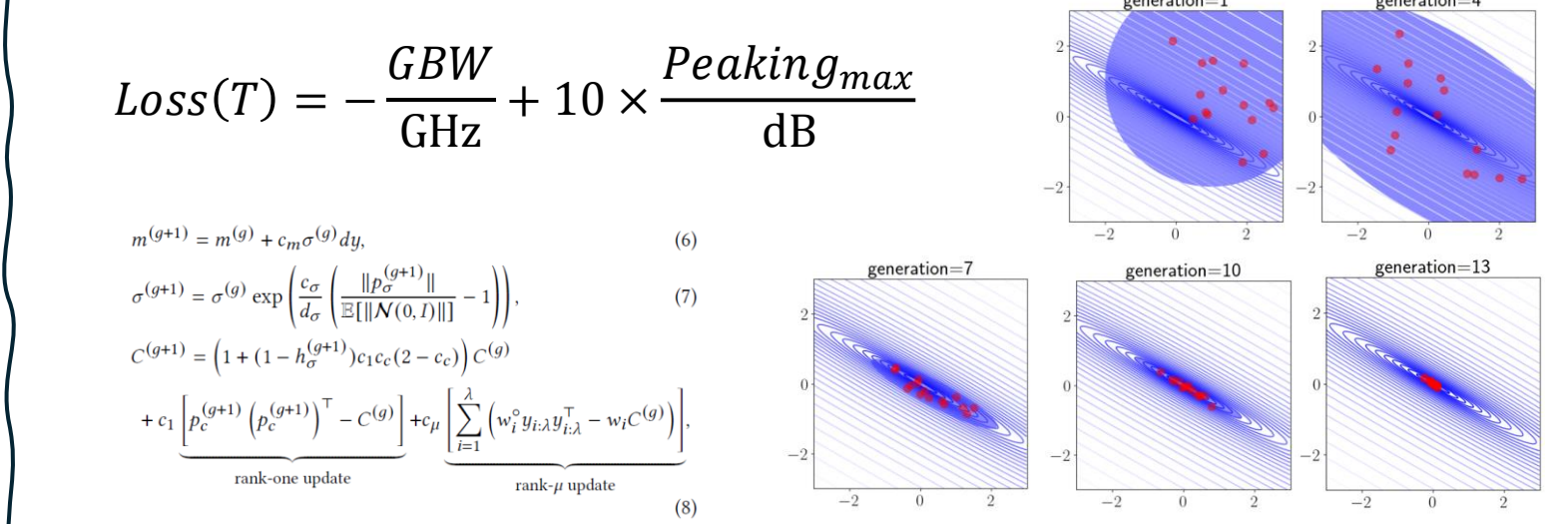
- Direct **S-parameter modeling**
- Co-designed** with given active CML core
- 9 free** geometrical parameter
- DRC compliant** during parameter definition



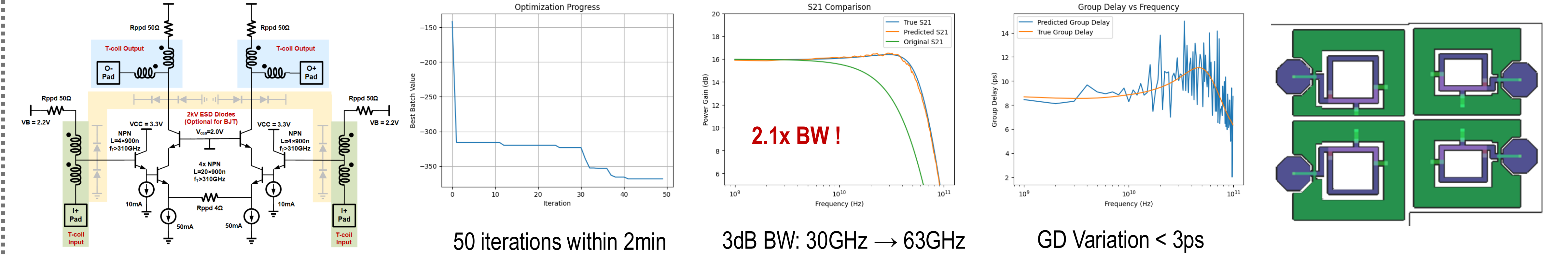
Light-weight MLP-based EM Emulator



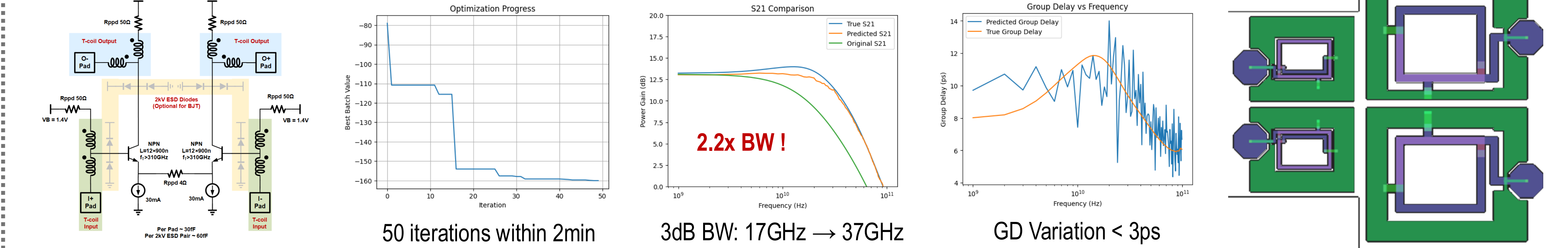
CMA Evolutionary Strategy Optimizer



Design #1: EF + Cascode

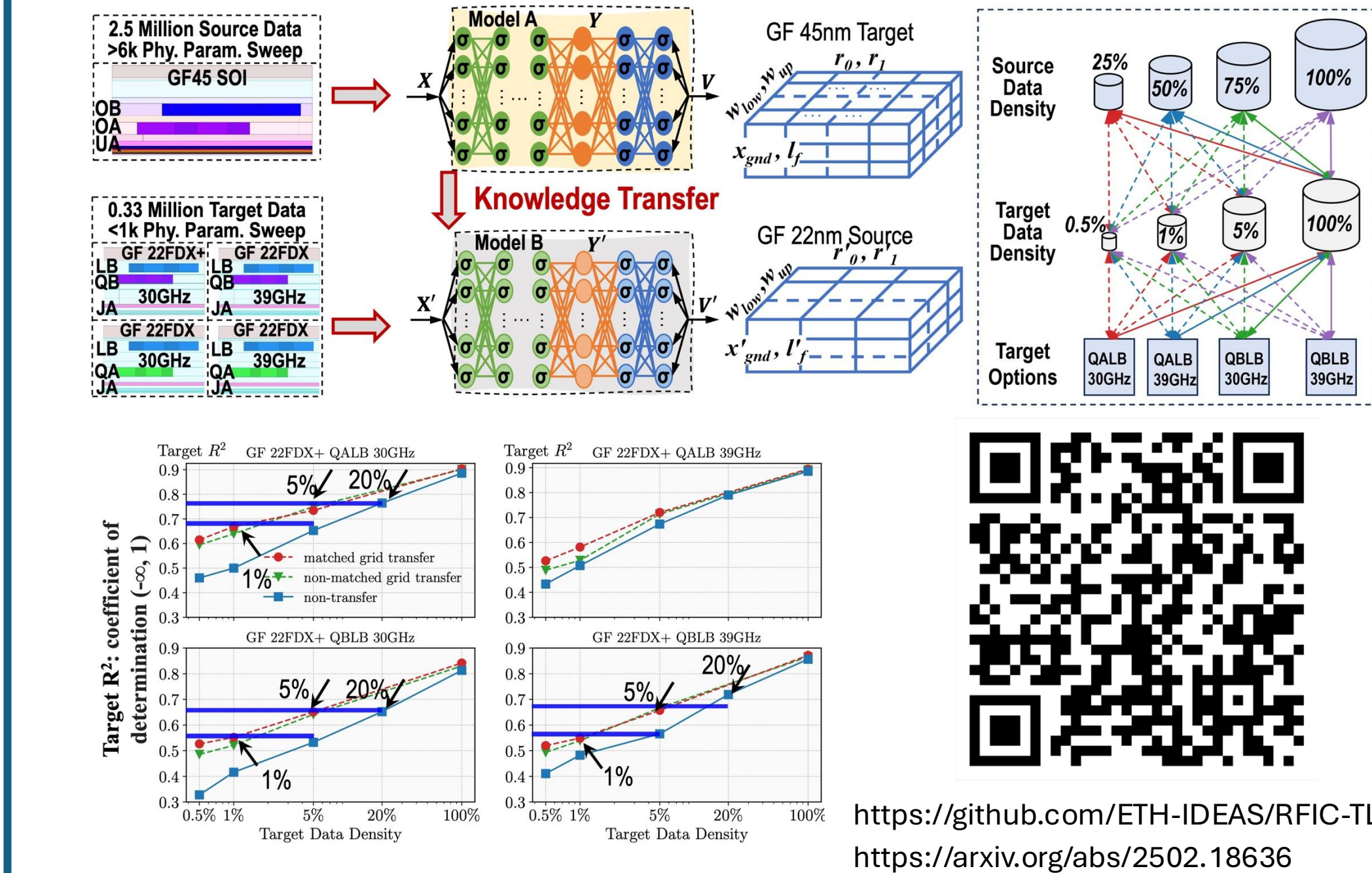


Design #2: Single CML

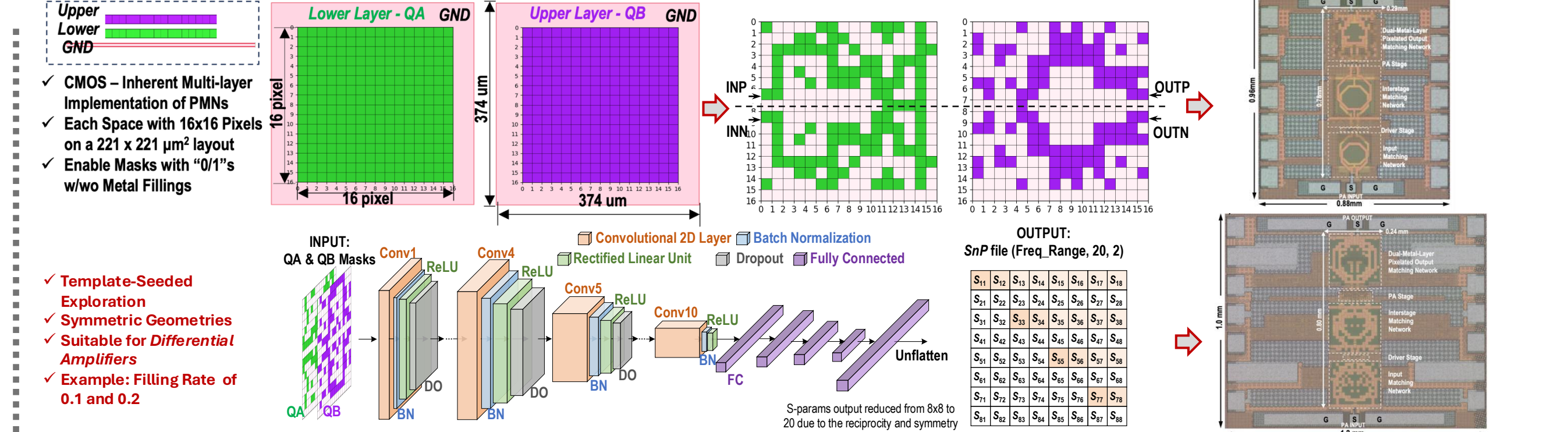


4. ETH IDEAS Group AI4RF Work

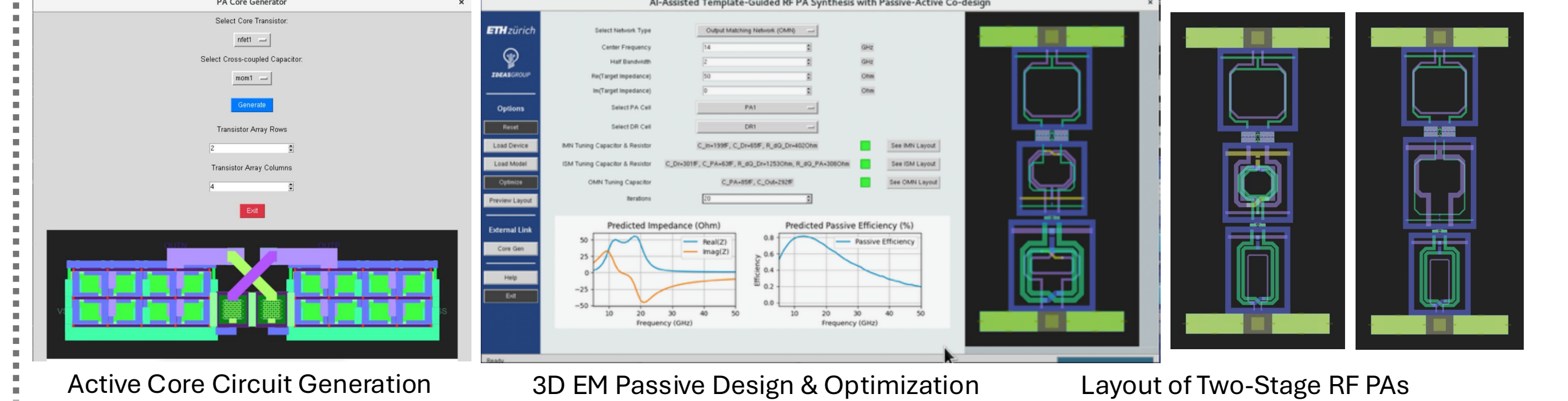
I. RFIC-TL: End-to-End Transfer Learning Assisted RFIC Passives



II. Template-Seeded Pixelated Multi-Metal-Layer Passives for Efficient Data Gen.



III. AI-Assisted Templated-Guided RF PA Synthesis with Passive-Active Co-Design



[1] S. Galal and B. Razavi, "Broadband ESD protection circuits in CMOS technology," in IEEE Journal of Solid-State Circuits, vol. 38, no. 12, Dec. 2003.
[2] B. Razavi, "The Design Of Broadband I/O Circuits [The Analog Mind]," in IEEE Solid-State Circuits Magazine, vol. 13, no. 2, pp. 6-15, Spring 2021.
[3] N. Hansen and A. Ostermeier, "Adapting arbitrary normal mutation distributions in evolution strategies: the covariance matrix adaptation," IEEE International Conference on Evolutionary Computation, Nagoya, Japan, 1996.
[4] Nomura, Masahiro, and Masashi Shibata. "cmases: A simple yet practical python library for cma-es." arXiv preprint arXiv:2402.01373 (2024).

[1] C. Chu et al., "AI-Assisted Template-Seeded Pixelated Design for Multi-Metal-Layer High-Coupling EM Structures: A Ku-Band 6G FR3 PA in 22nm FDX+," 2025 IEEE/MTT-S International Microwave Symposium, IMS 2025.
[2] C. Chu et al., "Deep Learning-Assisted RFIC Design With Dual-Metal-Layer Passive Matching Networks: A 15-22 GHz CMOS PA for 6G in 22nm FDX+," 16th German Microwave Conference, GeMiC 2025.
[3] C. Chu, Y. Mao, and H. Wang, "Transfer Learning Assisted Fast Design Migration Over Technology Nodes: A Study on Transformer Matching Network," 2024 IEEE/MTT-S International Microwave Symposium, IMS 2024.