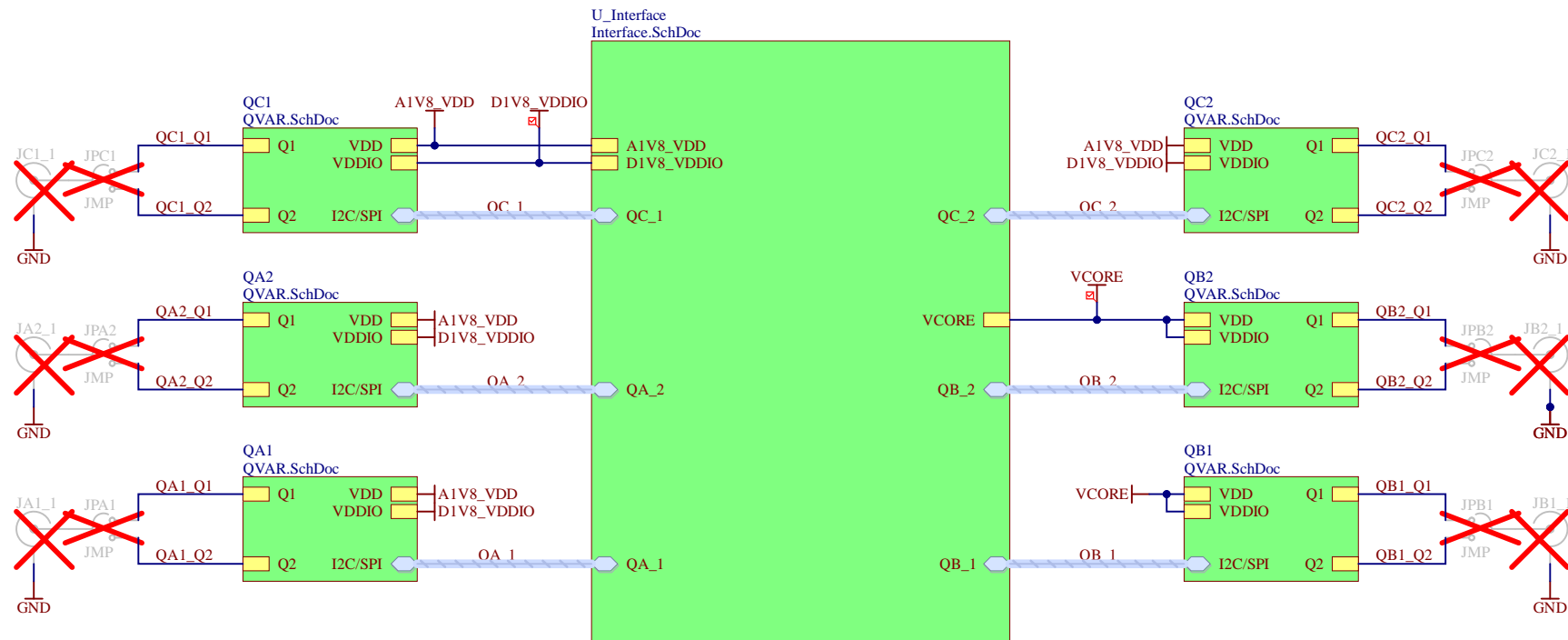
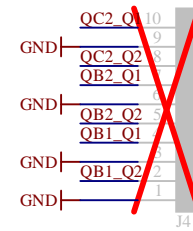
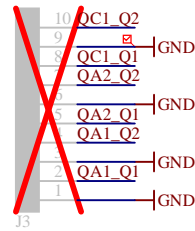
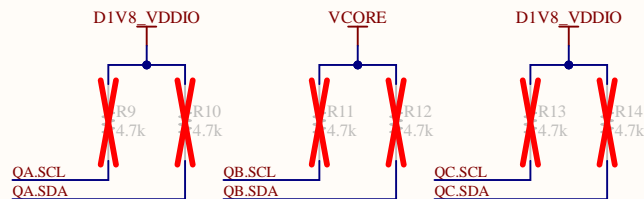
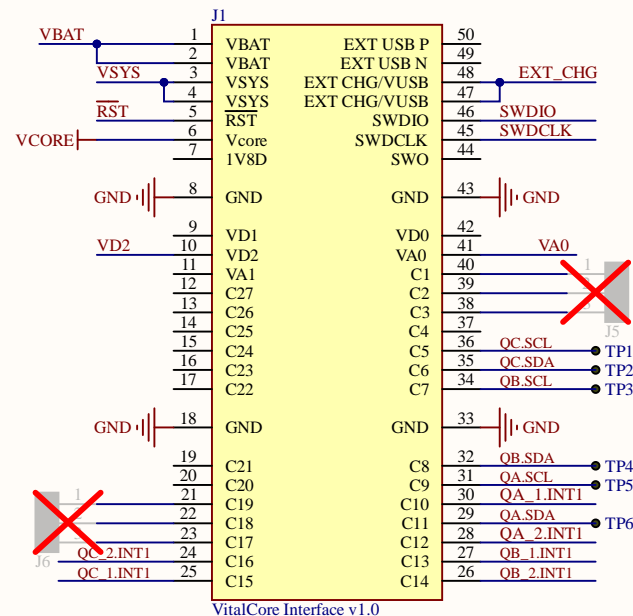
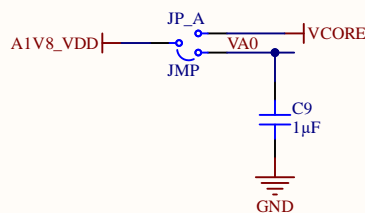
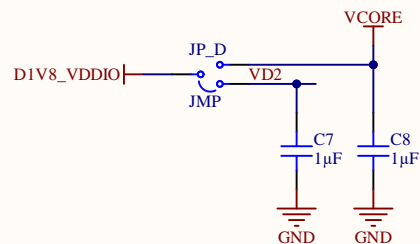
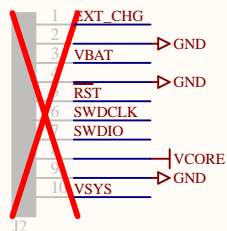
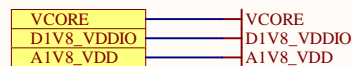
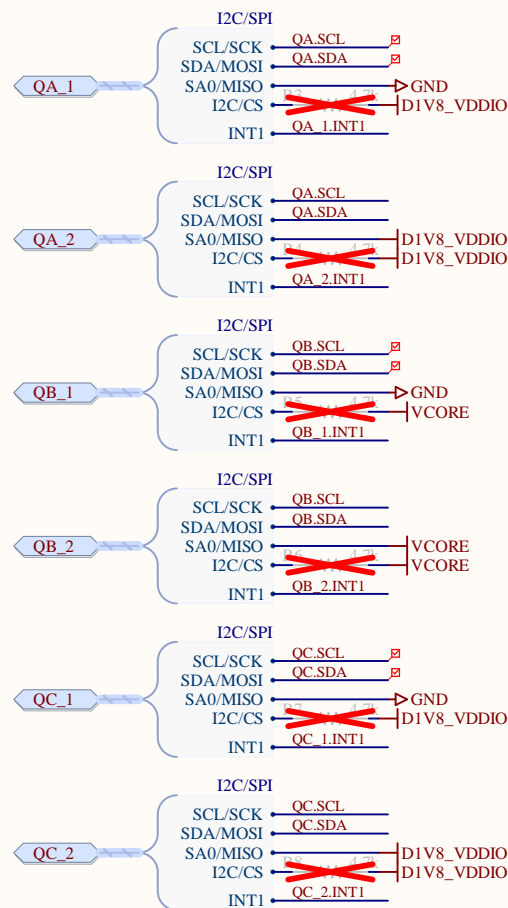


the 2 QVAR attached to I2CB have to be supplied by VCORE!



the 2 QVAR attached to I2CB have to be supplied by VCORE!

SWDIO and SWDCLK have to be connected externally in an ideal case. For emergency requirements a solderpad is supplied



ETH

Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Laboratory: *

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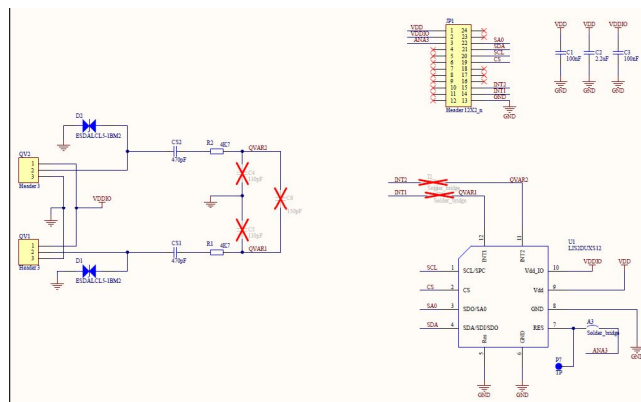
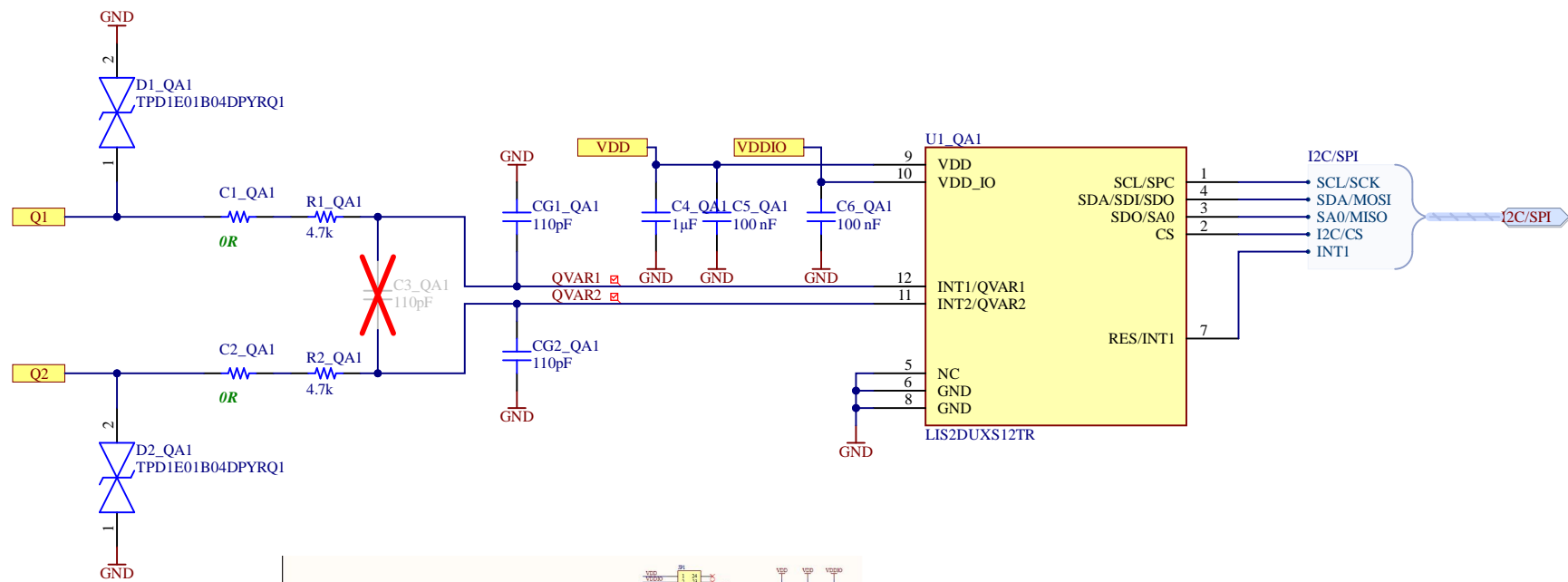
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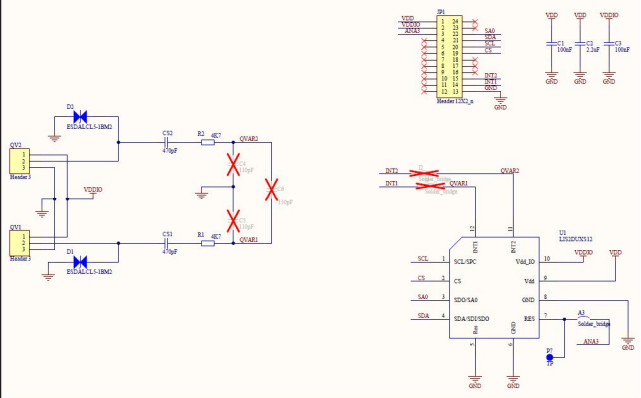
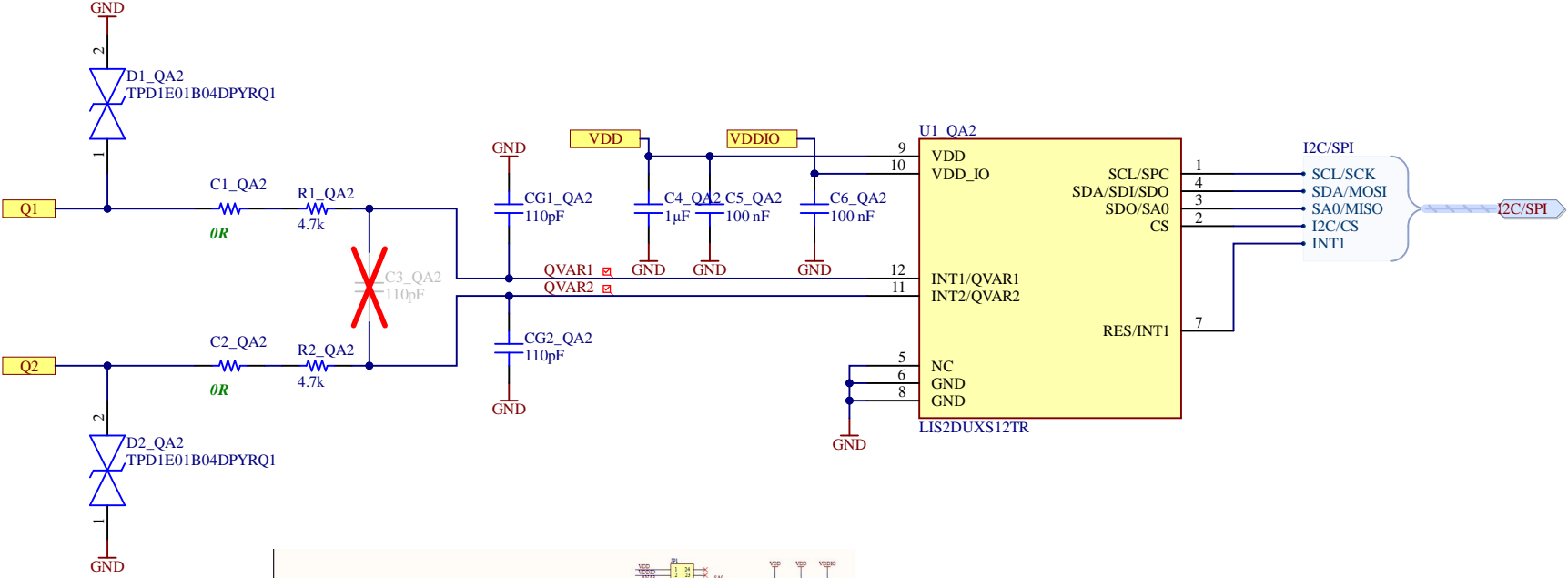
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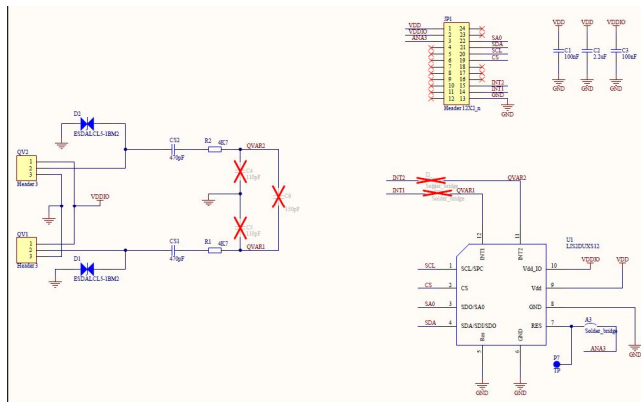
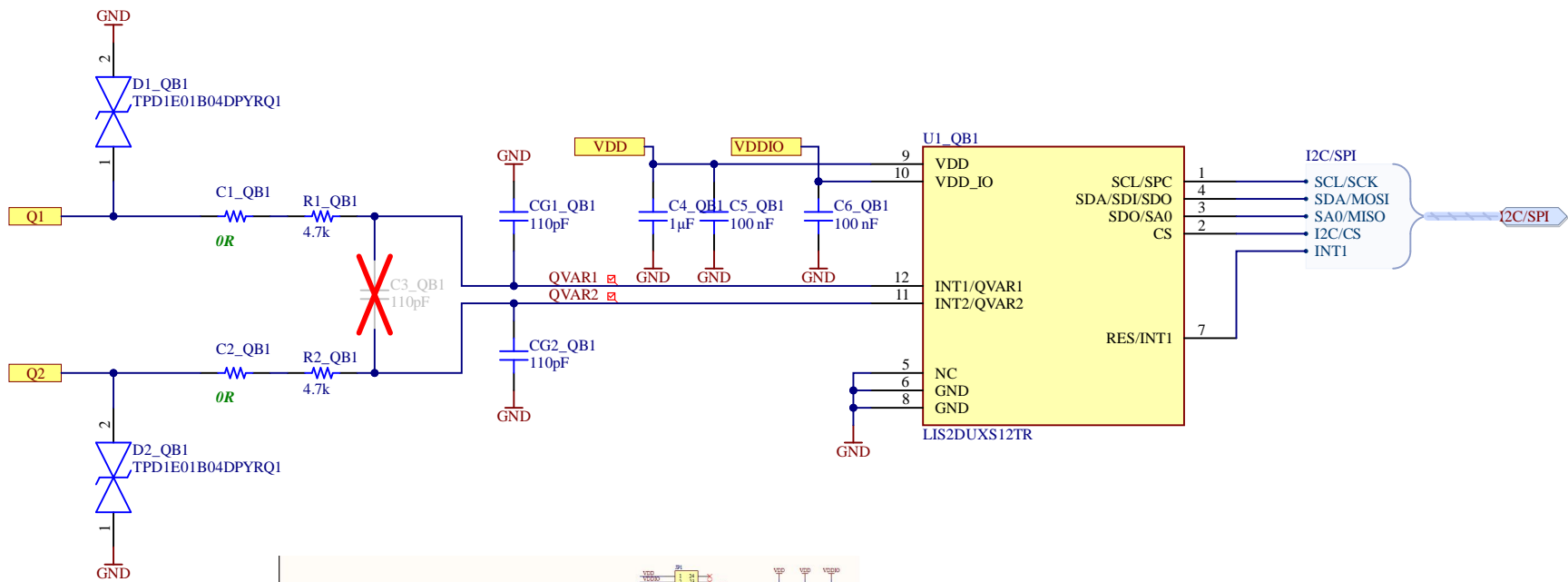
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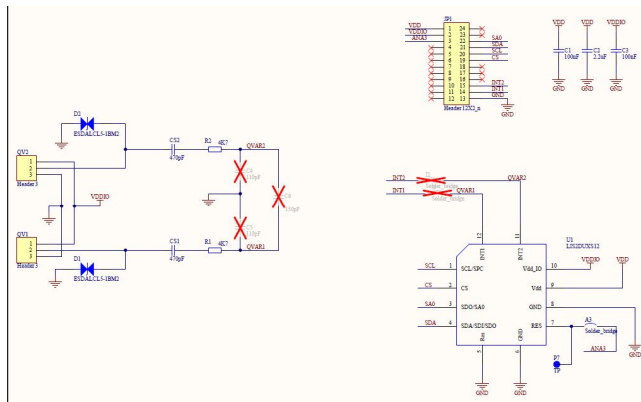
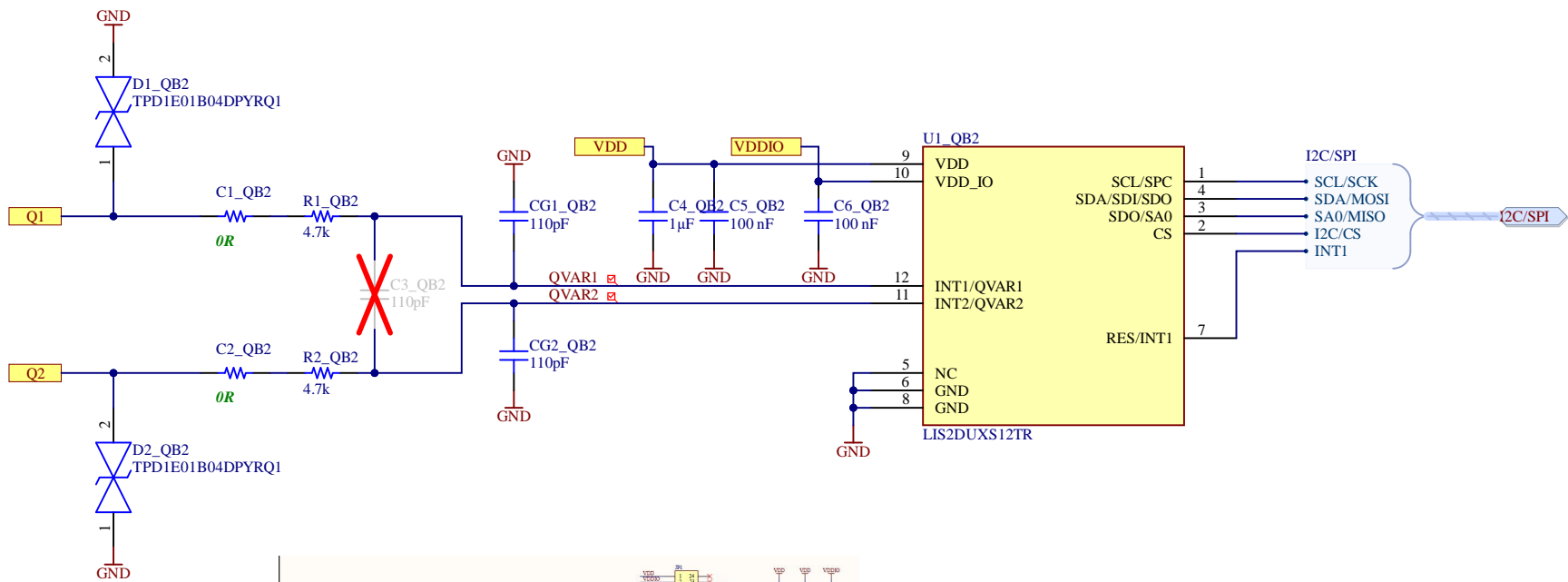




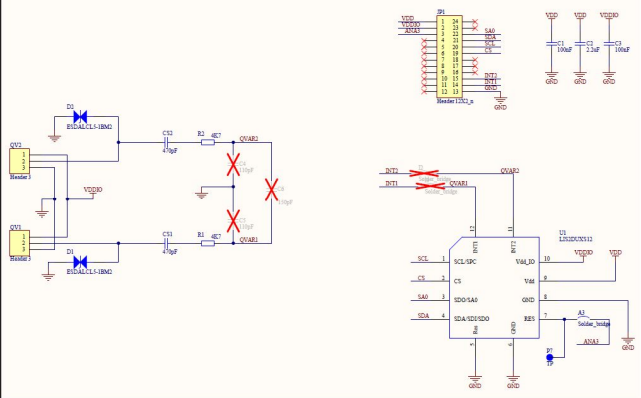
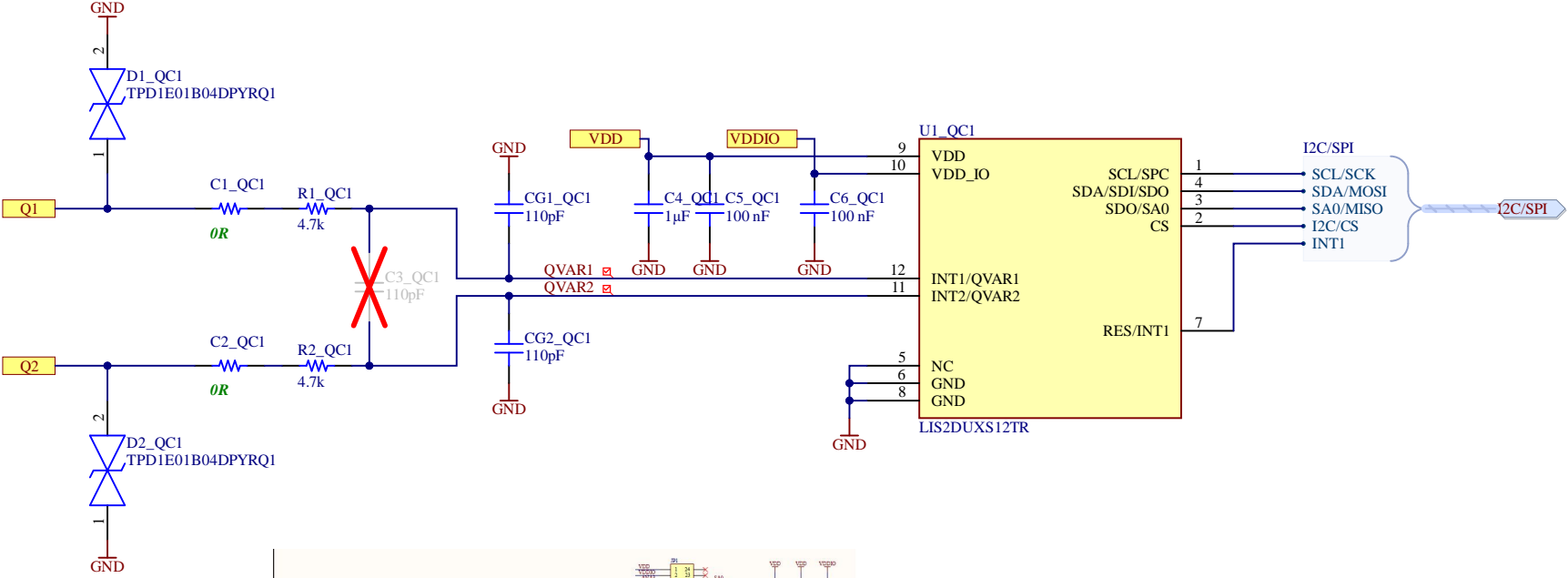
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