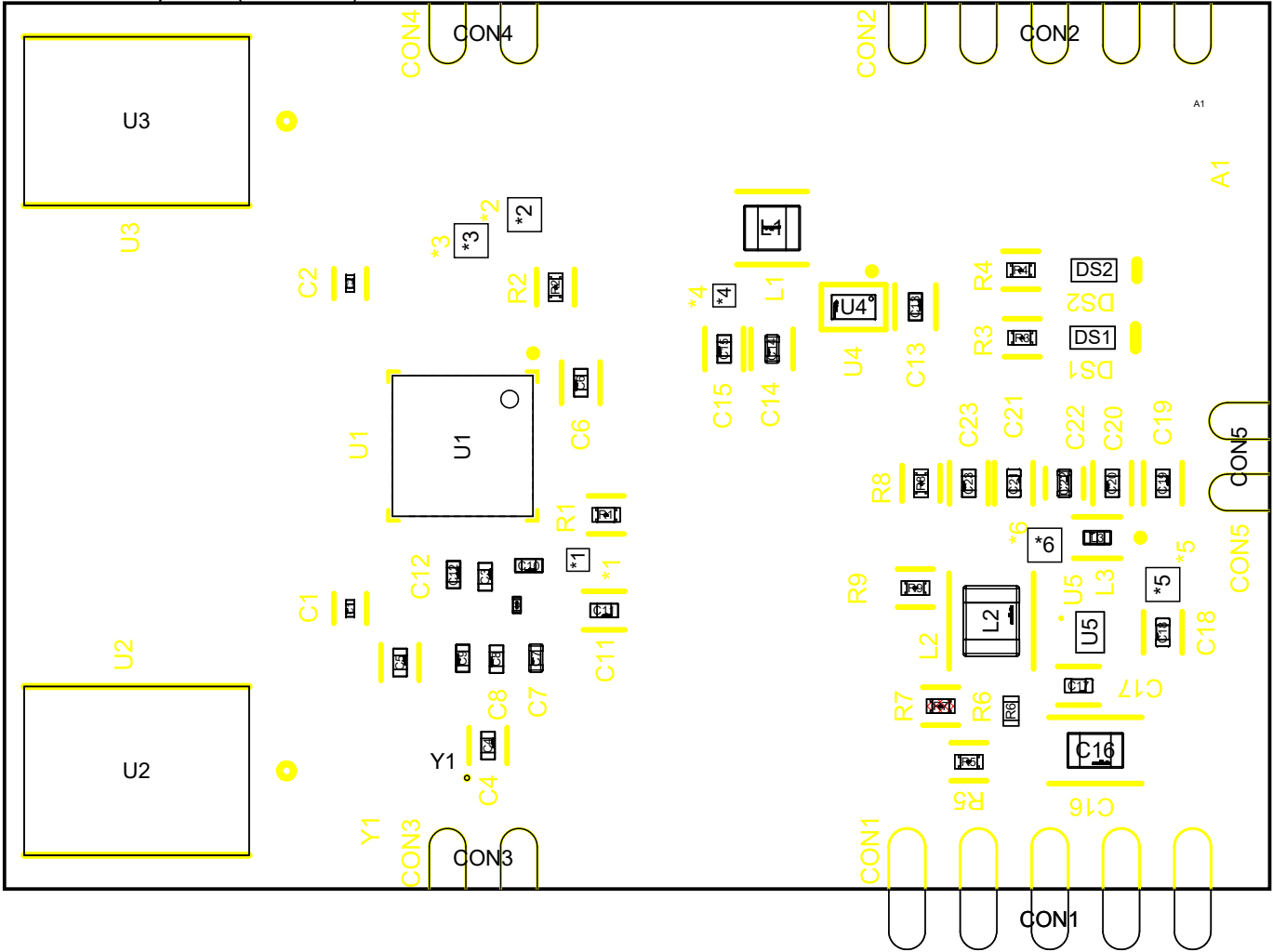


View from Top side (Scale 4:1)



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Swiss Federal Institute of Technology Zurich

Project:

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A4 Q

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File: C:\Users\topolonelli\Documents\Students\dw3000-triangulation-hardware\DW3120\PCB1_Layout_Final.PCBDwf

A

B

C

D

A


B

C

D

Bill Of Materials

Line #	Designator	Comment	Quantity
1	C1, C2	GJM0335C1E2R0WB01D	2
2	C3	GRM155R71C224KA12D	1
3	C4, C5	GJM1555C1H3R3BB01D	2
4	C6, C8, C11, C15, C20, C23	GRM155R71E104ME14D	6
5	C7, C14	GRM155R60J106ME47D	2
6	C9, C10, C12	GRM1555C1H472GE01D	3
7	C13	GRM155C80J475MEAAD	1
8	C16	GRM21BR60J107ME15L	1
9	C17, C18, C21	GRM155R60J475ME47D	3
10	C19	GRM1555C1H102JA01D	1
11	C22	CC0402KRX7R7BB103	1
12	CON1, CON2	Con_Pad	2
13	CON3, CON4, CON5	Con_Pad_GND	3
14	DS1	TLMS1100-GS08	1
15	DS2	TLMY1100-GS08	1
16	FB1	MPZ0603S330HT000	1
17	L1	1285AS-H-2R2M=P2	1
18	L2	LQM2HPN1R0MJ0L	1
19	L3	LQG15HS3N6B02D	1
20	R1, R2, R3, R4	CRCW0402100KFKED, [NoValue]	4
21	R5, R8, R9	CRCW04020000Z0ED	3
22	R6	CPF0402B47K5E1	1
23	U1	DW3220	1
24	U2, U3	AH086M555003-T	2
25	U4	TPS62743YFPR	1
26	U5	TPS62233DRYR	1
27	Y1	8Y38472012	1



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File: C:\Users\topolonelli\Documents\Students\dw3000-triangulation-hardware\DW3120\PCB1_Layout_Final.PCBDwf

Format: **A4 Q**

Laboratory: *

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Sheet: PCB1_Layout_Final.PCBDwf

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A

B

C

D

A

B


C

D

Layer Stack Legend

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
	Surface Material			Solder Resist	GTS
	Copper	0.01mm	Solder Resist	Solder Mask	GTL
	Top Layer	0.04mm		Signal	GP1
	Prepreg	0.09mm	Megtron 4	Dielectric	
	CF-004	0.04mm		Internal Plane	GP2
		0.32mm	Megtron 4	Dielectric	
	CF-004	0.04mm		Internal Plane	GP2
	Power	0.04mm		Internal Plane	GP2
	Prepreg	0.09mm	Megtron 4	Dielectric	
	Copper	0.04mm		Signal	GBL
	Bottom Layer	0.04mm		Signal	GBL
	Surface Material	0.01mm	Solder Resist	Solder Mask	GBS
	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO

Total thickness: 0.66mm



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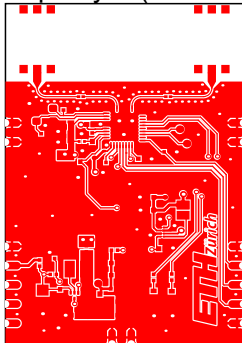
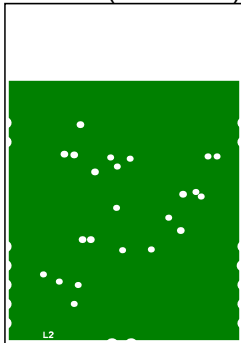
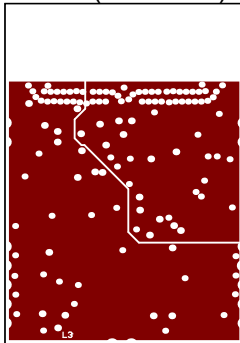
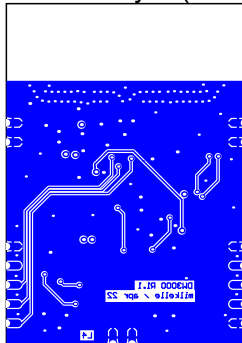

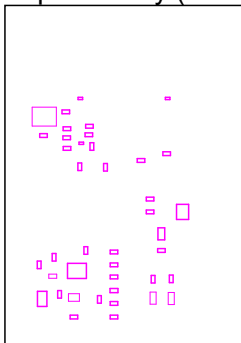



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File: C:\Users\topolonelli\Documents\Students\dw3000-triangulation-hardware\DW3120\PCB1_Layout_Final.PCBDwf

	1	2	3	4																
A	<div>Top Layer (Scale 1:1)</div> 	<div>Ground (Scale 1:1)</div> 	<div>Power (Scale 1:1)</div> 	<div>Bottom Layer (Scale 1:1)</div> 																
B																				
C	<div>Top Overlay (Scale 1:1)</div> 	<div>Top 3D Body (Scale 1:1)</div> 																		
D	<table><tr><td colspan="2"><div> Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich</div></td><td colspan="2">Project: <div>=title</div></td></tr><tr><td>Drawing number: =documentnumber</td><td>Rev: =revision</td><td>Format: A4 Q</td><td>Laboratory: * Drawn by: *</td></tr><tr><td>Date: 16/04/2024 18:35</td><td colspan="2"></td><td>Sheet: PCB1_Layout_Final.PCBDwf</td></tr><tr><td colspan="2">File: C:\Users\topolonelli\Documents\Students\dw3000-triangulation-hardware\DW3120\PCB1_Layout_Final.PCBDwf</td><td colspan="2">Page =sheetnumber / sheettotal</td></tr></table>				<div> Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich</div>		Project: <div>=title</div>		Drawing number: =documentnumber	Rev: =revision	Format: A4 Q	Laboratory: * Drawn by: *	Date: 16/04/2024 18:35			Sheet: PCB1_Layout_Final.PCBDwf	File: C:\Users\topolonelli\Documents\Students\dw3000-triangulation-hardware\DW3120\PCB1_Layout_Final.PCBDwf		Page =sheetnumber / sheettotal	
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