

A

A

B

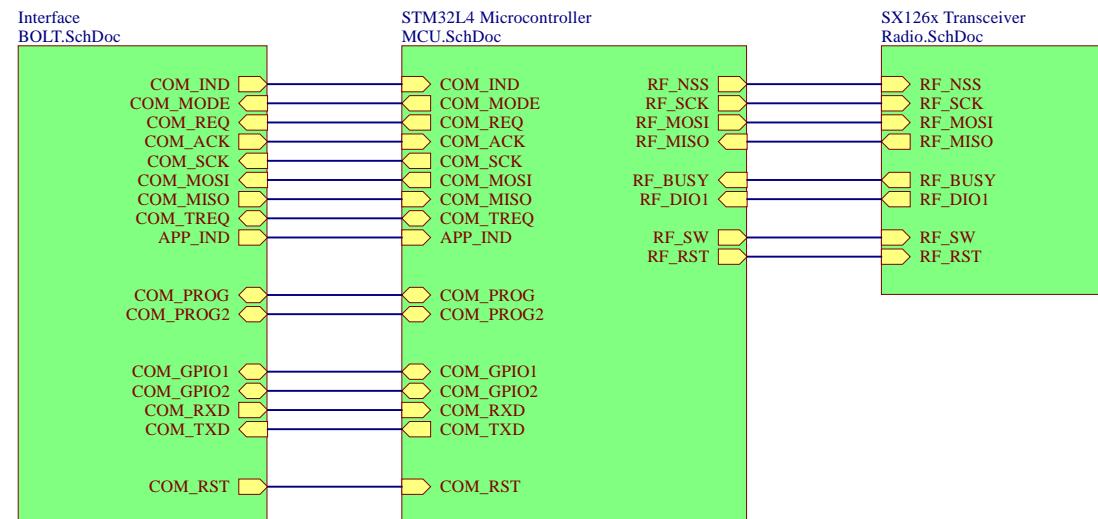
B

C

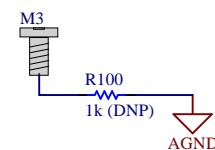
C

D

D



ETHZurich
Logo ETH Zurich

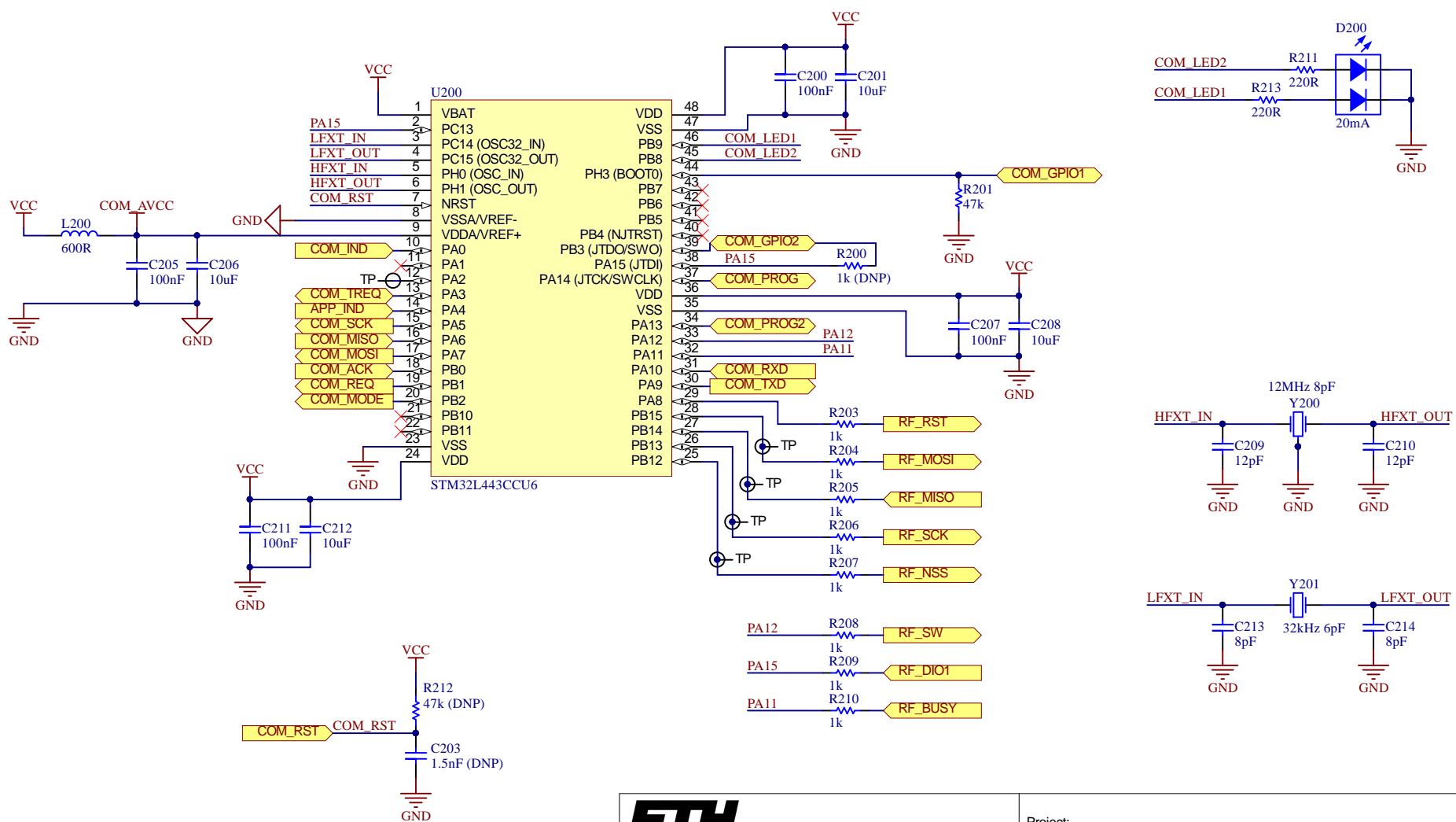


Eidgenössische Technische Hochschule Zürich
Swiss Federal Institute of Technology Zurich

Project:

DPP2 LoRa ComBoard Overview

| | | | | | | | | |
|------------------------------------------------------------------------------------------|------------|----------|-----|---------|-------------|---------------------------------|--------|-----------------|
| Drawing number: | 1 | Rev. | 1.1 | Format: | Laboratory: | Computer Engineering Group, TIK | Sheet: | Overview.SchDoc |
| Date: | 09/05/2018 | 15:19:29 | | A4 Q | Drawn by: | Reto Da Forno | Page | 1 of 4 |
| File: C:\Users\Public\Documents\Altium\Projects\DDP2_ComBoard_LoRa_rev_1\Overview.SchDoc | | | | | | | | |

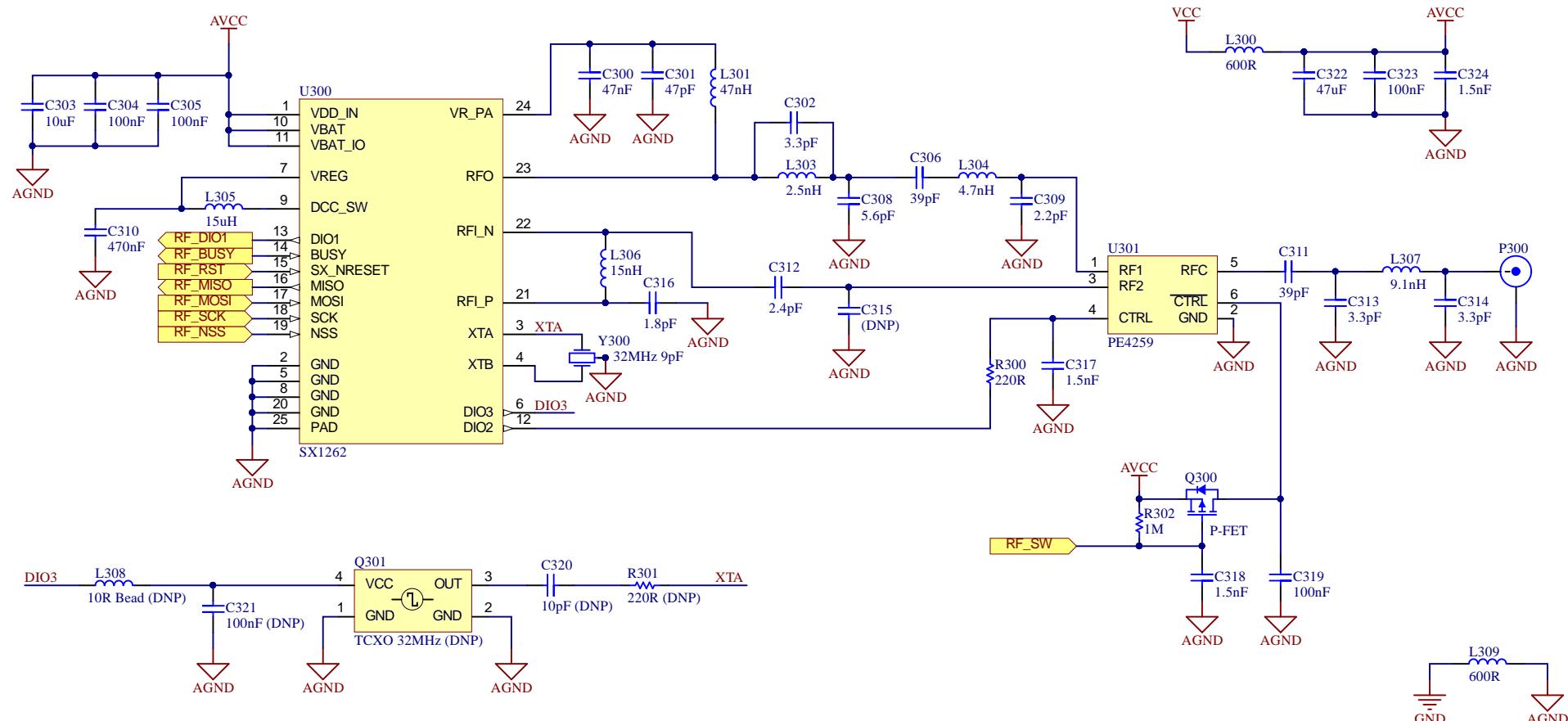


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Project:

STM32L4 Microcontroller

| | | | | | | | | |
|------------------------------------------------------------------------------------|------------|----------|-----|---------|-------------|---------------|--------|------------|
| Drawing number: | 2 | Rev. | 1.1 | Format: | Laboratory: | * | Sheet: | MCU.SchDoc |
| Date: | 09/05/2018 | 15:19:29 | | A4 Q | Drawn by: | Reto Da Forno | Page | 2 of 4 |
| File: C:\Users\Public\Documents\Altium\Projects\DP2_ComBoard_LoRa_rev_1\MCU.SchDoc | | | | | | | | |

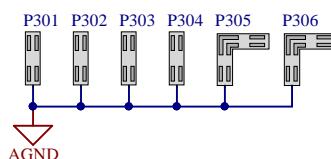


PCB parameters:

- 1.2mm standard FR4, 4 layers, 35um copper
- 0.15mm clearance, 0.24mm spacing btw. top and ground layer, 0.4mm wave guide width

RF shield (20x15mm): Harwin S02-20150300

RF shield clips: Harwin S0971-46R and S0981-46R



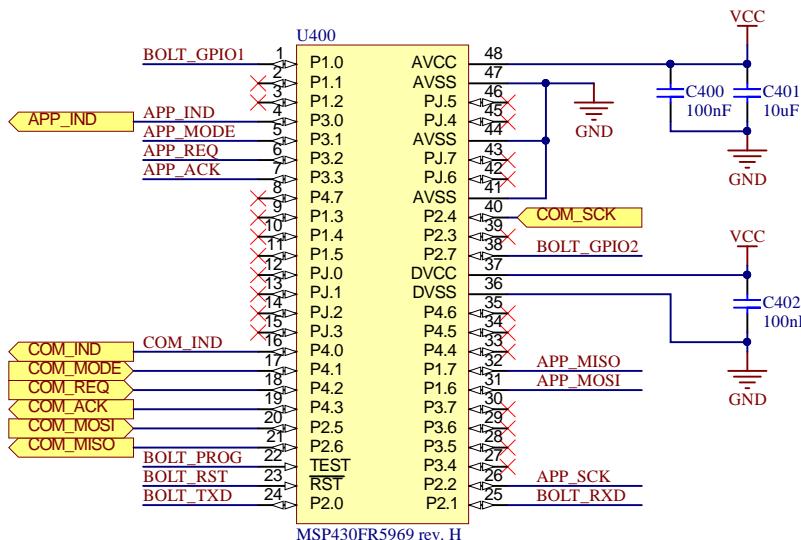
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Project:

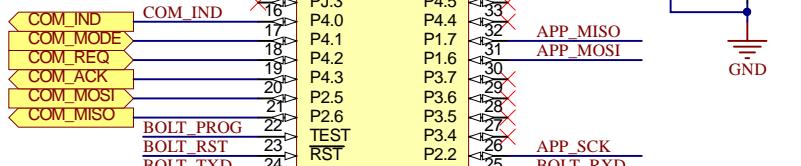
SX1262 868MHz Radio Transceiver

| | | | | |
|---------------------------------------------------------------------------------------|----------|-------------------------|---------------|---------------------|
| Drawing number: 3 | Rev. 1.1 | Format: A4 Q | Laboratory: * | Sheet: Radio.SchDoc |
| Date: 09/05/2018 15:19:29 | | Drawn by: Reto Da Forno | | Page 3 of 4 |
| File: C:\Users\Public\Documents\Altium\Projects\DP22_ComBoard_LoRa_rev_1\Radio.SchDoc | | | | |

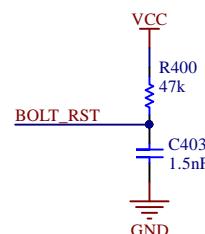
A



B

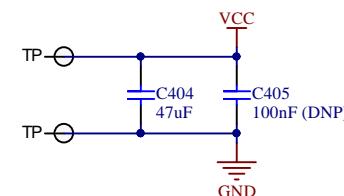
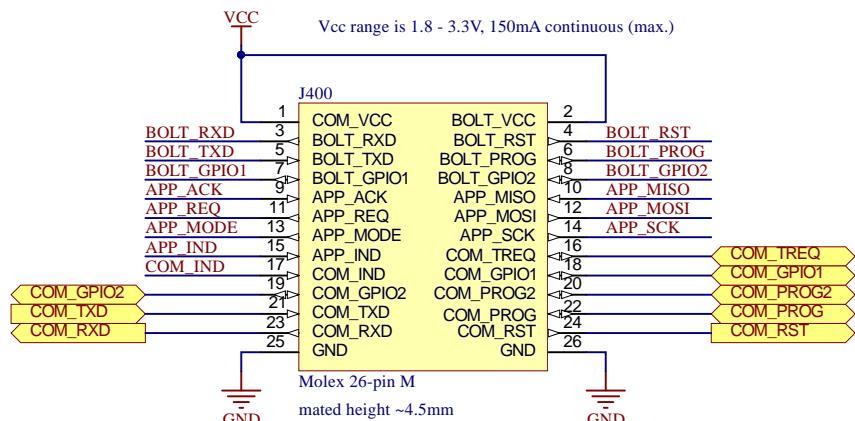


C



Note: The MSP430FR5969 MCU should be rev. H where errata PMM24 (lockup during wakeup from LPM4) has been fixed!

VCC range is 1.8 - 3.3V, 150mA continuous (max.)

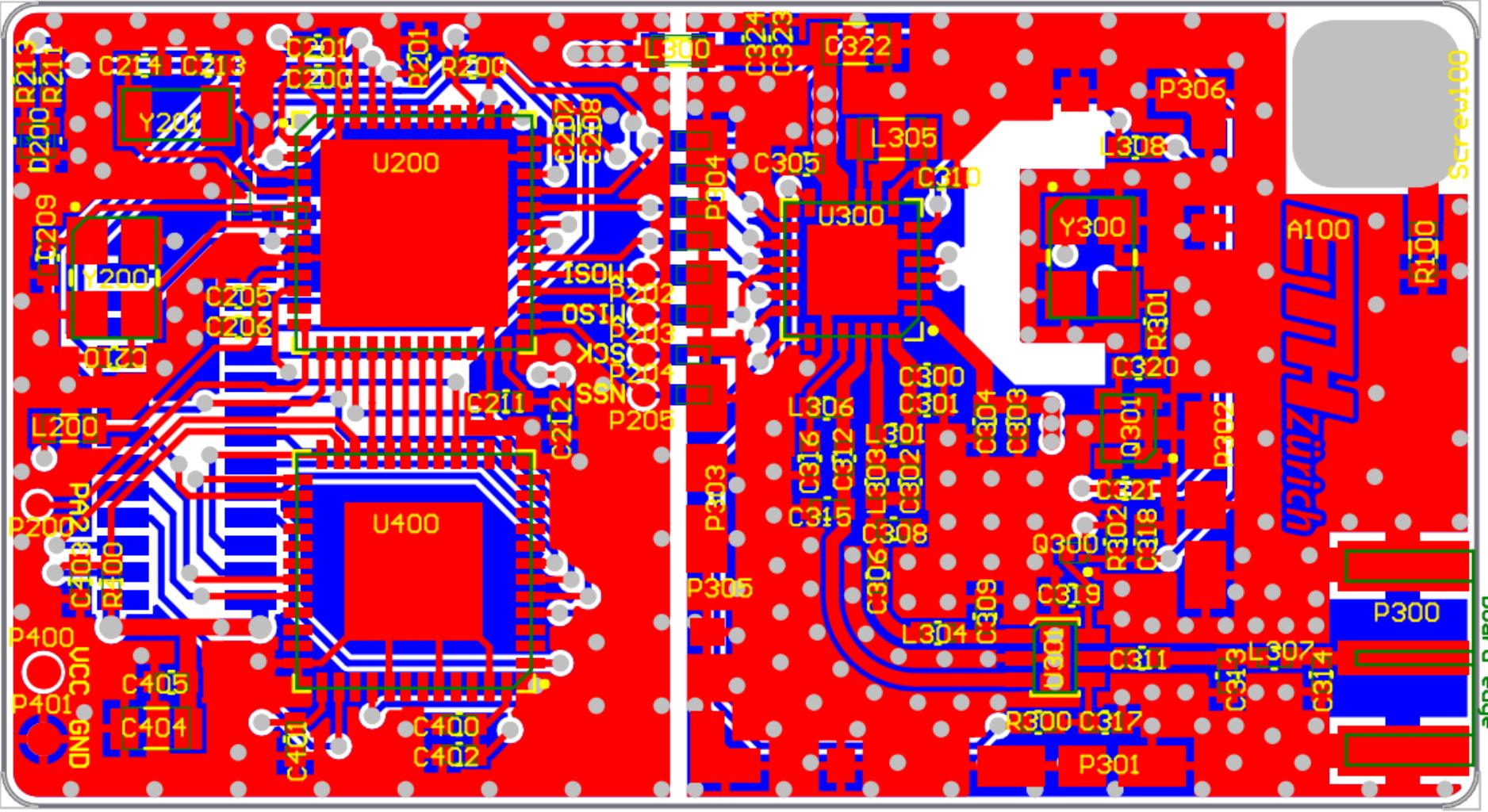


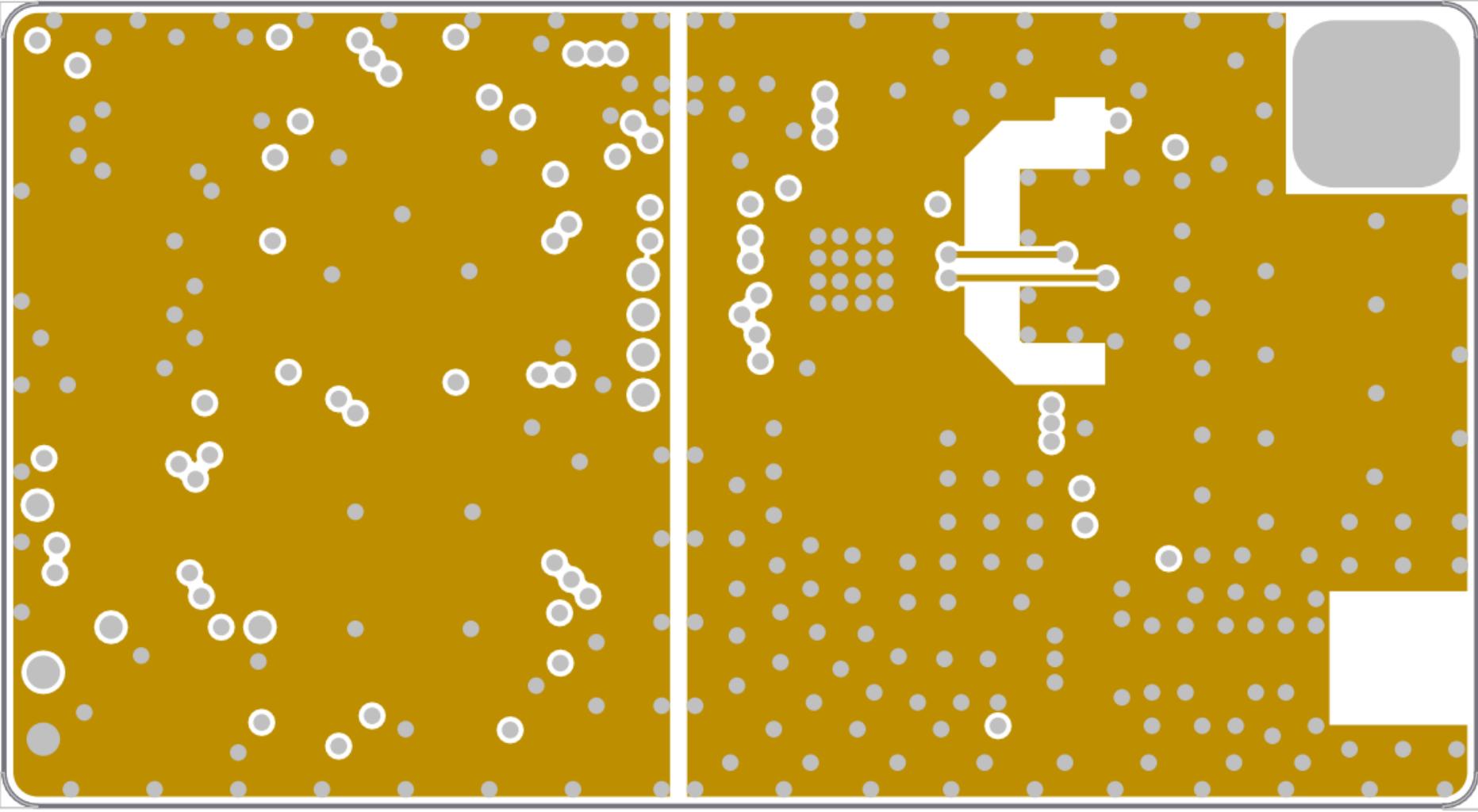
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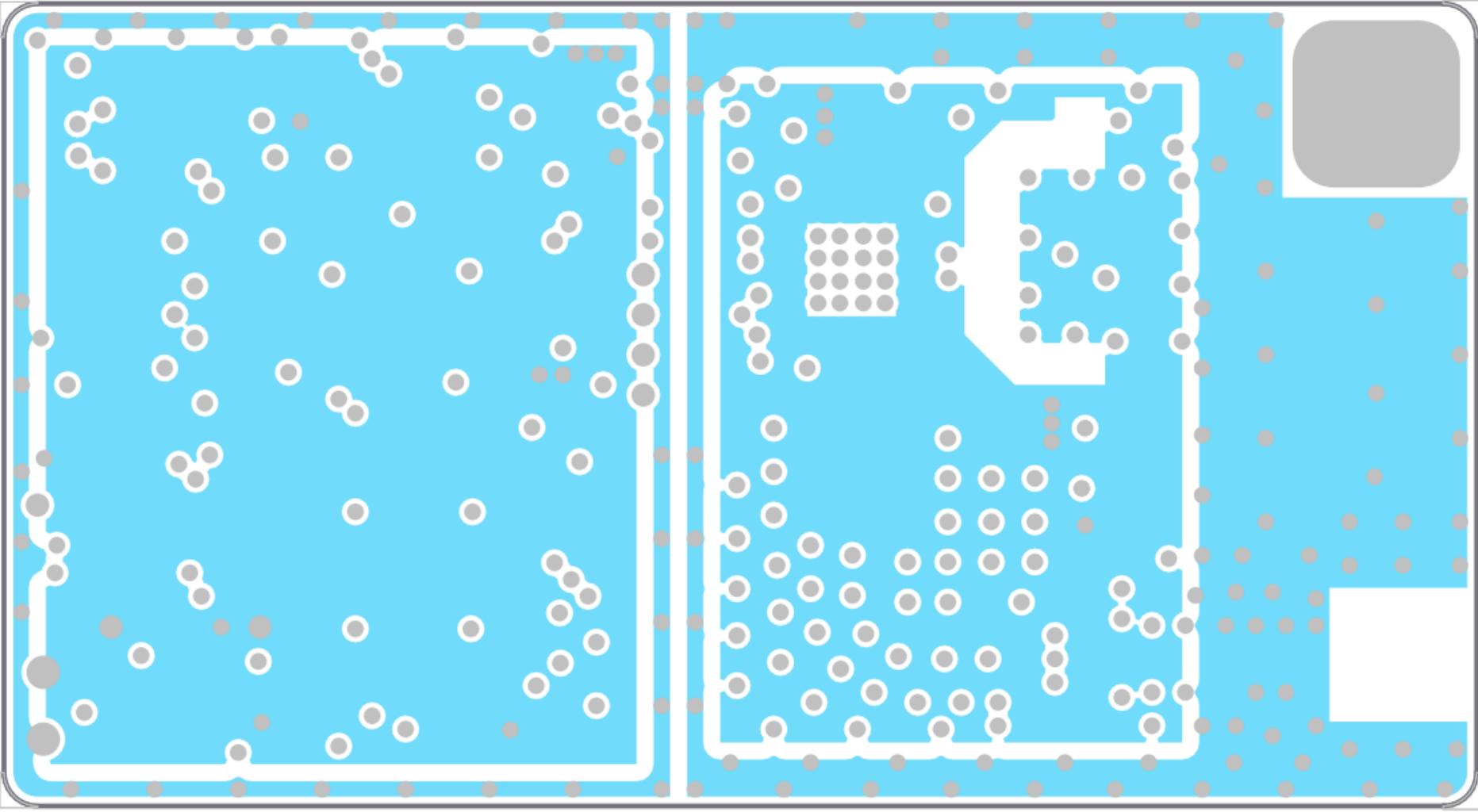
Project:

BOLT and Board-to-Board Connector

| | | | | | | | | |
|-----------------|---------------------------------------------------------------------------------|----------|-----|---------|-------------|---------------------------------|--------|-------------|
| Drawing number: | 4 | Rev. | 1.1 | Format: | Laboratory: | Computer Engineering Group, TIK | Sheet: | BOLT.SchDoc |
| Date: | 09/05/2018 | 15:19:29 | | A4 Q | Drawn by: | Reto Da Forno | Page | 4 of 4 |
| File: | C:\Users\Public\Documents\Altium\Projects\DPPI2_ComBoard_LoRa_rev_1\BOLT.SchDoc | | | | | | | |







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Ббб5 ГРВЕ-САМ

L309

H1

R1C203

R208

R209

R210

R203

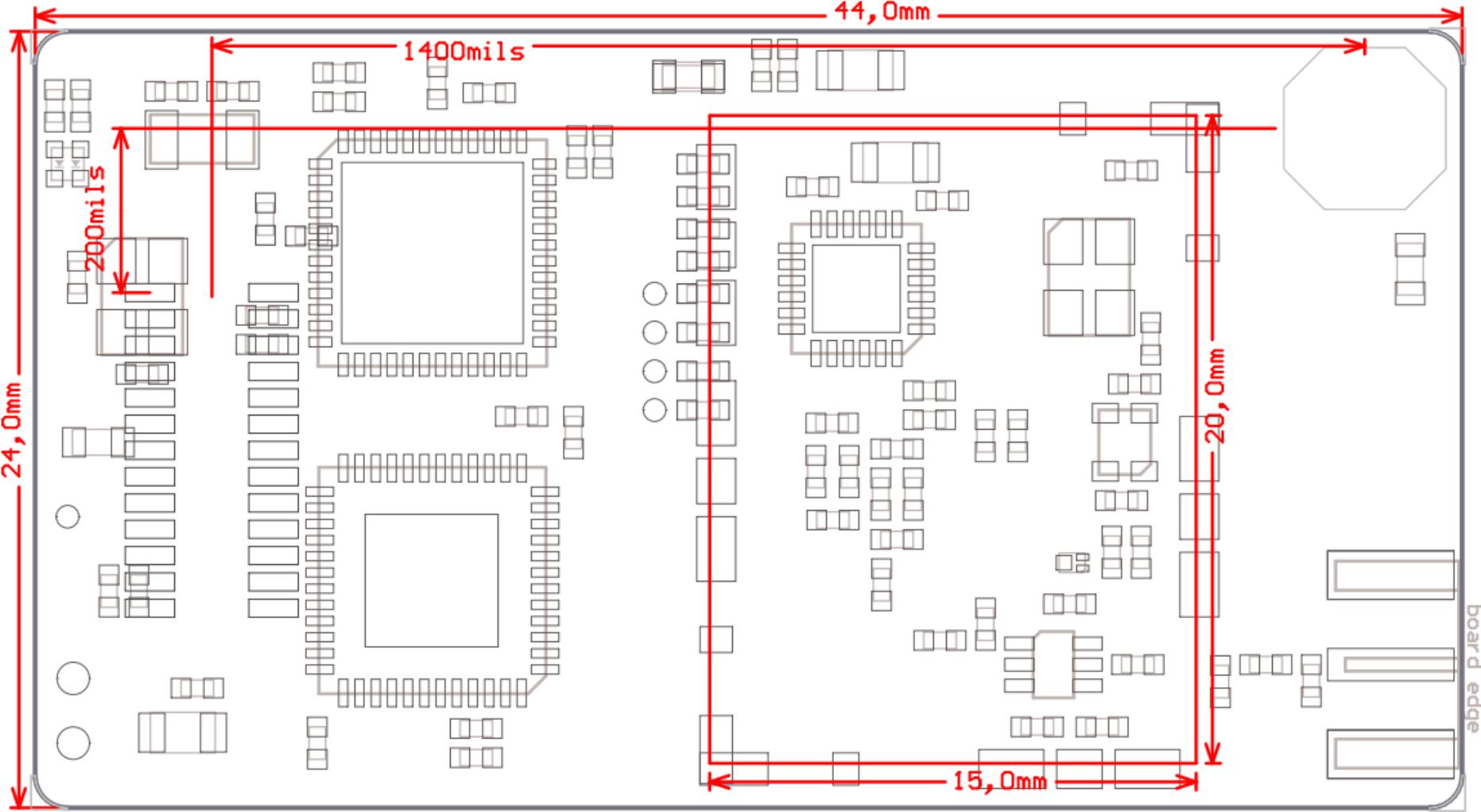
R204

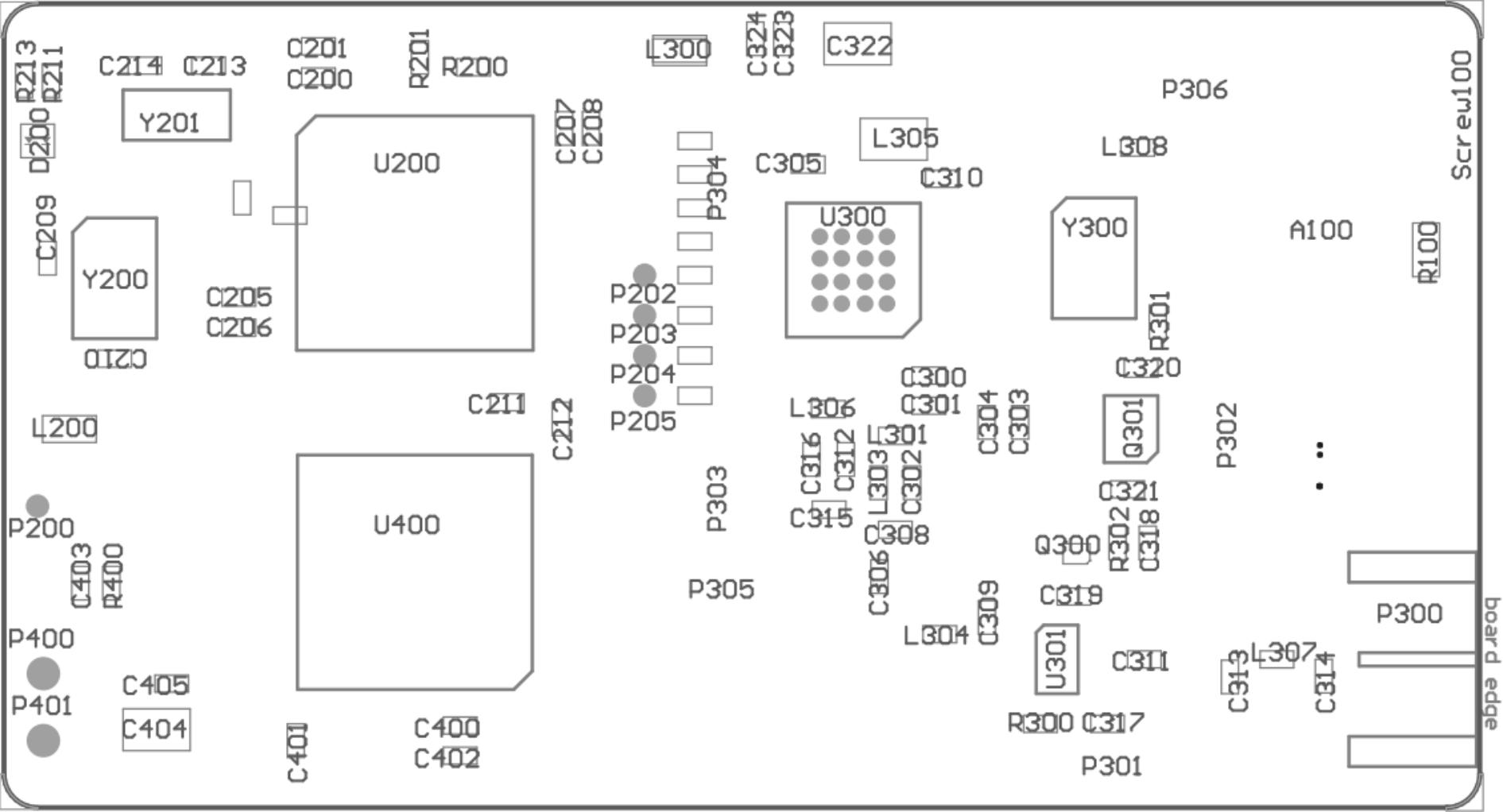
R205

R206

R207

J400





L309

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D662 1089 161.1

R212
C203

- R208
- R209
- R210
- R203
- R204
- R205
- R206
- R207



J400

A large, empty rectangular frame occupies most of the page, serving as a placeholder for an illustration or diagram that has not been provided.

