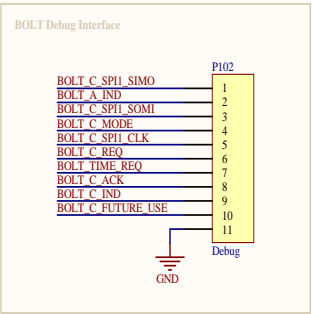
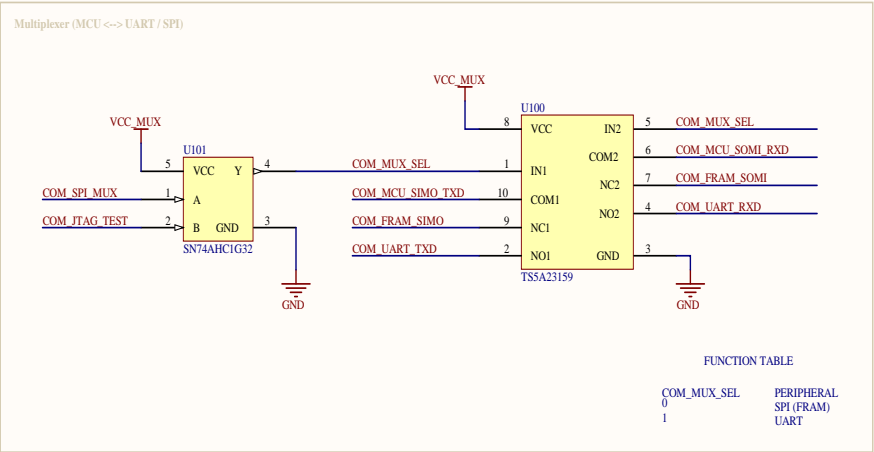
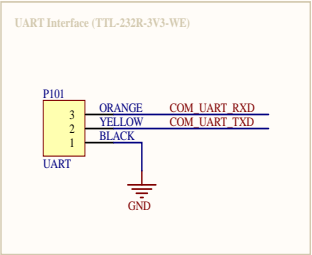
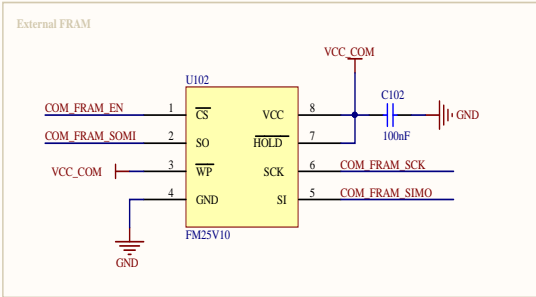
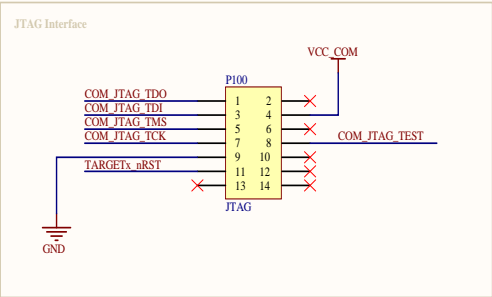
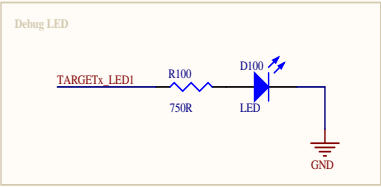
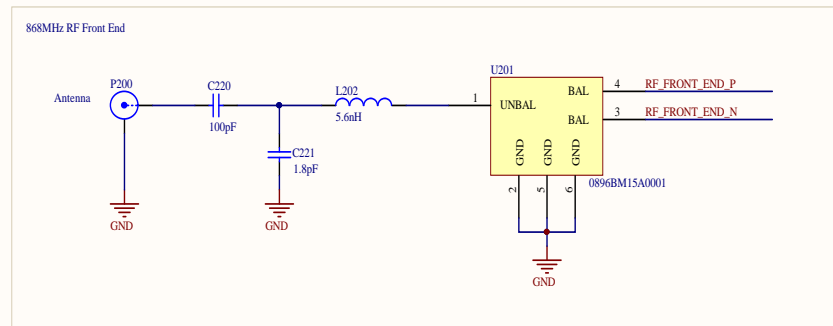
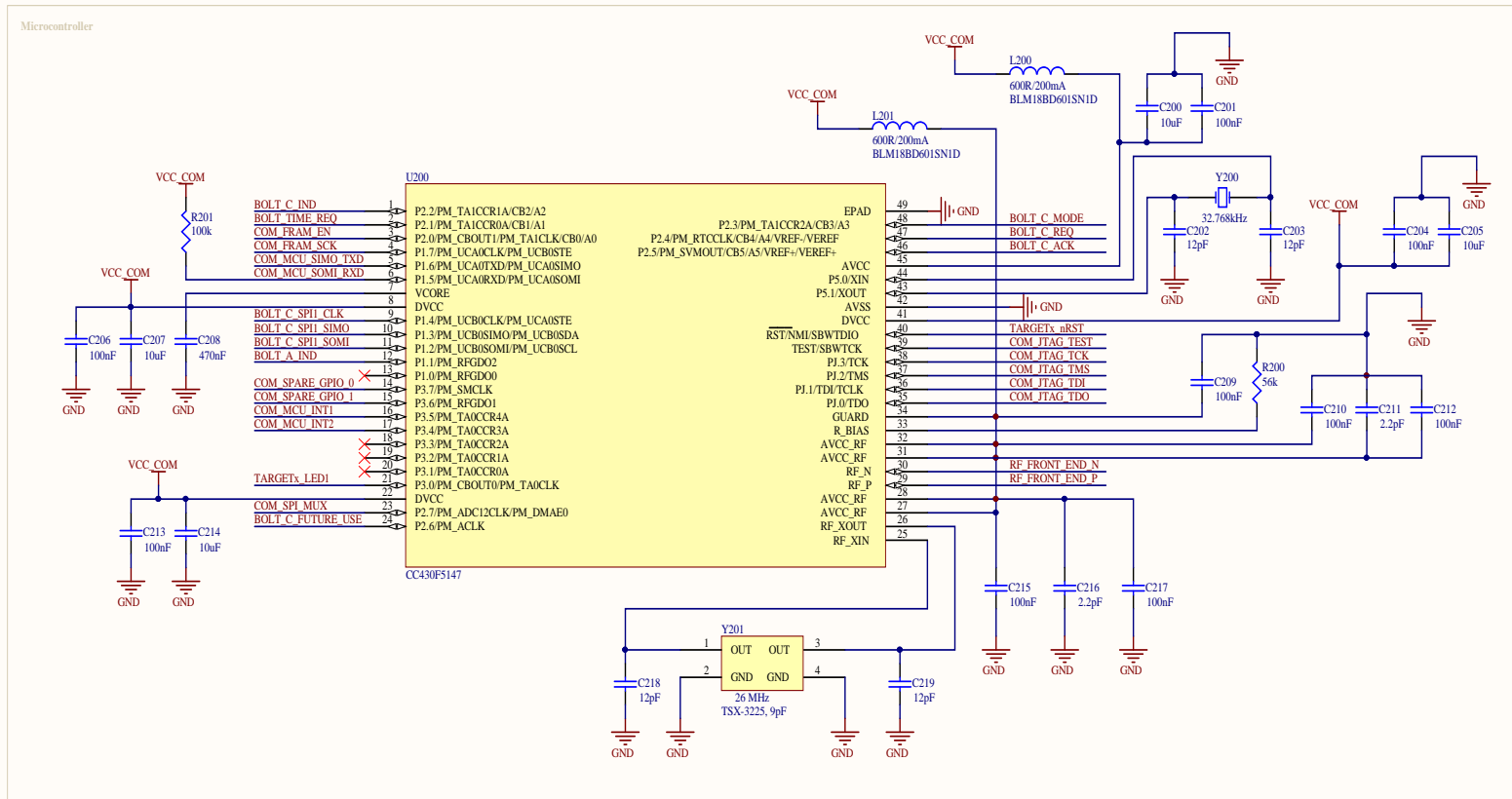


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|---|----------|--|--------------------------------|
| | | Zeichnungs-Titel: DPP - BOLT (MCU & Power) | |
| Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich | | | |
| Zeichnungsnummer: * | Rev: 1.0 | Format: A3 | Institut: * |
| Datum: 04.02.2016 10:47:24 | | Ersteller: Felix Sutton | Projekt: BOLT_MCU_Power.SchDoc |
| Seite * von * | | | |
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Coplanar Waveguide

Substrate: FR4 (Er = 4.6)
Substrate Thickness: 1mm
Substrate Loss Tangent: 0.015
Metal Thickness: 35um
Track Width: 0.76mm
Track Spacing: 0.125mm
Frequency: 868MHz
Impedance: 50.0063Ohm

| Component Specifications | | | |
|--------------------------|---------------------------|---------|---------------|
| SMA Edge Socket | Emerson 142-0711-841 | Farnell | 1019329 |
| 868/915MHz Balun | Johanson 0896BM15A0001 | Digkey | 712-1474-1-ND |
| 100pF | Murata GRM1555C1H101FA01J | Digkey | 490-7753-1-ND |
| 5.6nH | Johanson L-07CSN6SV6T | Digkey | 712-1462-1-ND |
| 1.8pF | Johanson 500R07S1R8BV4T | Digkey | 712-1271-1-ND |

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|---|----------|---|-----------------------------------|
| ETH Eidgenössische Technische Hochschule Zürich Swiss Federal Institute of Technology Zurich | | Zeichnungs Titel: DPP - Communication Processor (MCU) | |
| Zeichnungsnummer: * | Rev: 1.0 | Format: A3 | Institut: * |
| Datum: 04.02.2016 10:47:24 | | Ersteller: Felix Sutton | Projekt: Communication_MCU.SchDoc |
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