
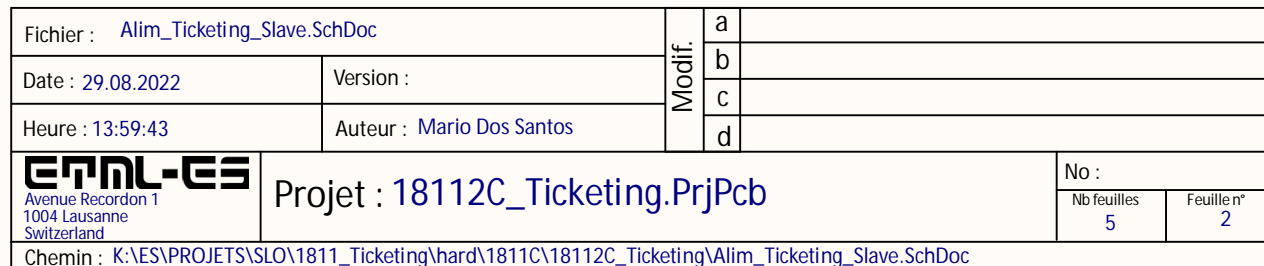
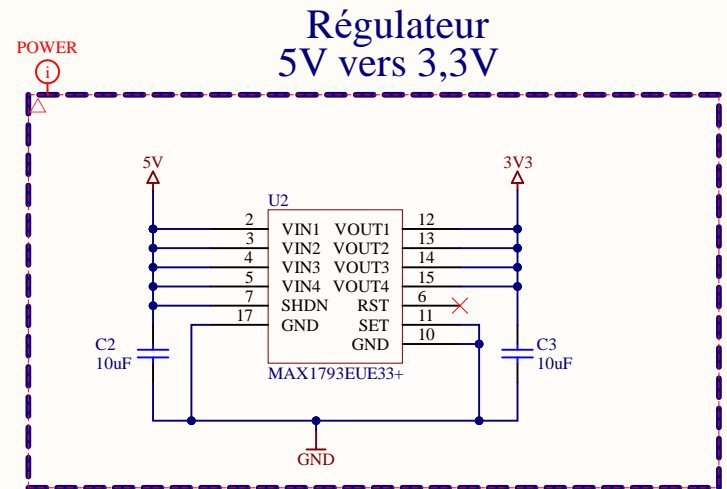
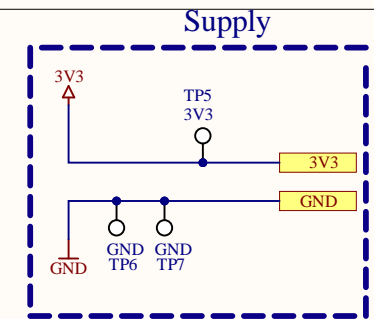


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Date : 29.08.2022			b	
Heure : 13:59:43			c	
Version :			d	
Auteur : Mario Dos Santos				
 Avenue Recordon 1 1004 Lausanne Switzerland	Projet : 18112C_Ticketing.PrjPcb			No : C
				Nb feuilles 5
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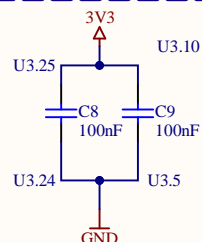
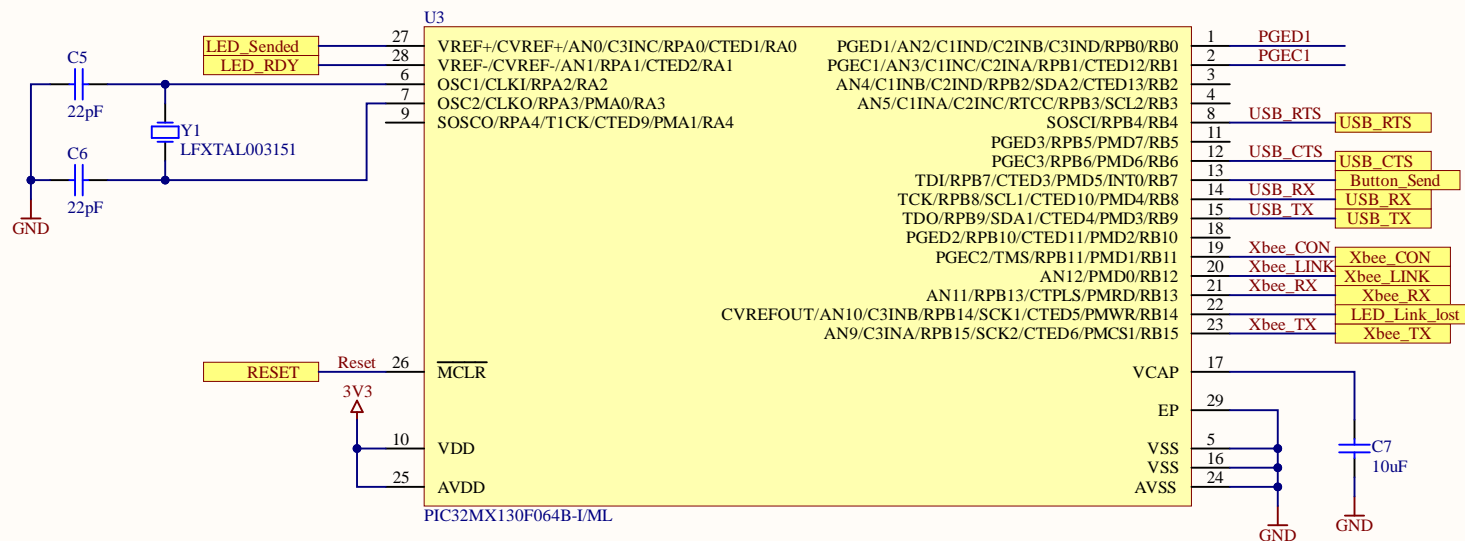
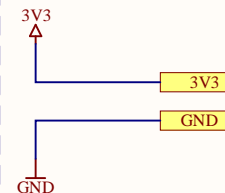
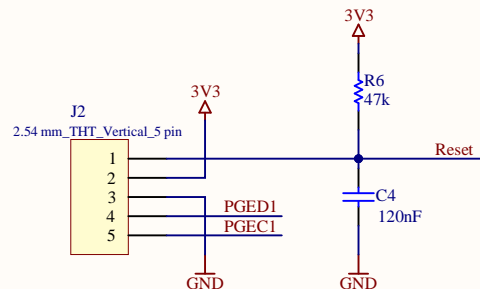



# Debug Connector

The diagram illustrates the wiring for a Debug Connector (J2). The connector has five pins, numbered 1 through 5. The connections are as follows:

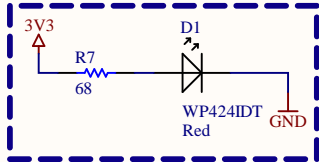
- Pin 1: Connected to the 3V3 power supply.
- Pin 2: Connected to the node between the 47k resistor (R6) and the 120nF capacitor (C4).
- Pin 3: Connected to the Reset signal line.
- Pin 4: Connected to the PGED1 signal line.
- Pin 5: Connected to the PGEC1 signal line.

The 3V3 power supply is connected to the 47k resistor (R6) and ground (GND). The Reset signal is connected to the 120nF capacitor (C4) and ground (GND).

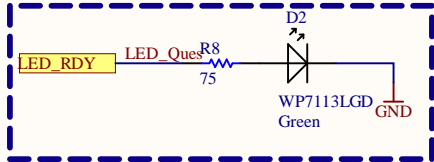


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Auteur : <a href="#">Mario Dos Santos</a>				
 Avenue Recordon 1 1004 Lausanne Switzerland	Projet : <a href="#">18112C_Ticketing.PrjPcb</a>			No :
				Nb feuilles 5
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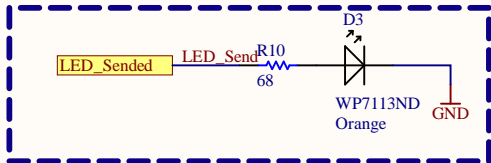
3V3 Indicator



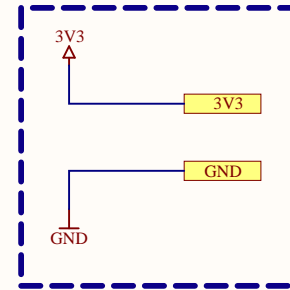
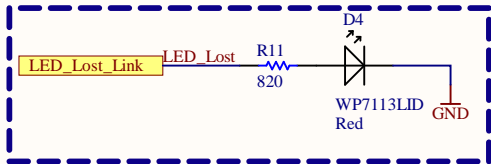
Ready to Send Indicator



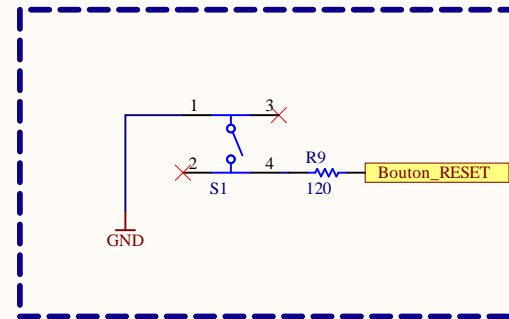
Quesiton Sended Indicator



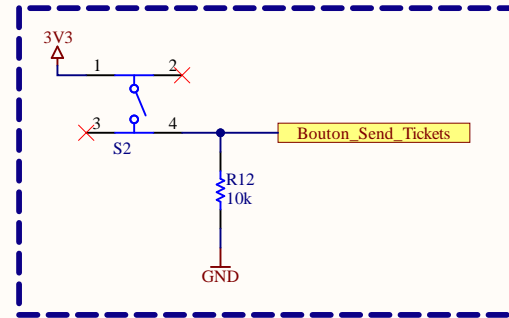
Link Lost Indicator



Buton\_RESET



Buton\_Send\_Tickets



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**ETNL-ES**  
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Switzerland

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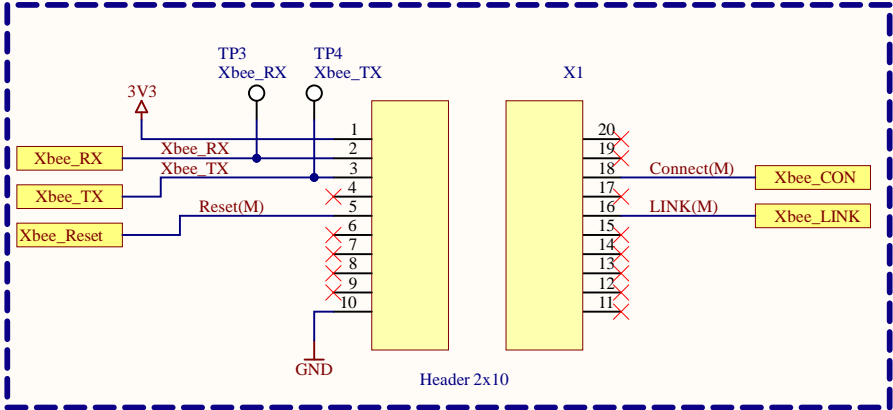
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Nb feuilles  
5

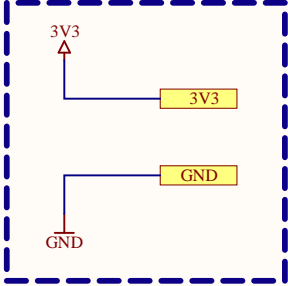
Feuille n°  
4


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Xbee



Supply



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				Nb feuilles 5	Feuille n° 5
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