

		Basic	Intermed	Adv	Want to Learn
<b>Software</b>					
Dev. methods			1		
Dev. Environ.			1		
Config. Mgmt.			1		
Debug tools			1		
Create Code			1		
Flowchart			1		
Protocols		1			
Data security		1			
Database					1
<b>Electrical</b>					
Power devices		1			
Filters		1			
Amplifiers		1			
Comms		1			
Power supply		1			
Converters		1			
Logical			1		
Simulation		1			
RF devices		1			
Prog. devices		1			
<b>Computing</b>					
Single board					1
Operating Sys		1			
Interfaces		1			
Networks		1			
Microprocessor		1			
Microcontroller					1
<b>General</b>					
Prog. Controller					1
I/O modules		1			
Sensors					1
Actuators					1
Motors					1
Solenoids					1

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<b>Engr. Business</b>					
Requirements					1
Trade Study		1			
Diagraming			1		
Tech Writing				1	
Communication			1		
Statement of Work					1
<b>Conduct Research</b>					
Library					1
Tech Journals					1
Market					1
Product analysis					1
Regulatory					1
Standards					1
<b>Project Mgmt</b>					
Team organization			1		
Minutes			1		
Metrics					1
Schedule					1
Cost		1			
Tracking		1			
Visibility					1
Presentation			1		
Reporting					1

		Need intro	Have used	Can teach	Want to Learn
Oscilloscope		1			
Logic Analyzer		1			
DVM		1			
Current probe		1			
Freq. Analyzer		1			
Spectrum Analyzer		1			

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Agile, SCRUM, Waterfall, Lean, Feature Driven, etc.			
exposure to build, test, debug, compile, etc.			
processes, tools, methodologies			
simulators, emulators, probes, etc.			
exposure Java, JSON, C++, C#, etc.			
standard symbology, can you prototype code with flowchart			
comms, streams, templates, patterns, etc.			
data at rest, during transport, I&A levels, etc.			
Oracle, Sybase, MS-SQL, SQL, Access, etc.			
FET, IGBT, NPN/PNP, MOV, etc.			
line, tank trap, HF noise, ripple, etc.			
band pass, low pass, power line, HF, LF, etc.			
communications over a serial bus of some kind, voip, slip, etc			
linear, switching, buck, boost, etc.			
analog to digital, digital to analog, voltage to current, etc.			
basic gates and representation via schematic			
schematic capture to sim, VHDL to sim, etc.			
antenna design, bluetooth, wifi, walkies, etc.			
FPGA, PLD, PLA, SOC, etc.			
optimized for specific use, general purpose, etc.			
Windows, Unix, RTOS, etc.			
OSI reference model,			
star, token ring, bus, line, tree, mesh, etc.			
RISC, multi-core, low power, 8/16/32/64 bit, multi-thread			
on-board peripherals added to microprocessor			
state machines, RLL, ladder, etc.			
dry contact in/out, AC in/out, DC in/out, analog in/out, etc.			
capacitance, ultrasonic, IR, inductive, light, heat, etc.			
linear, push-pull, rotational, etc.			
DC, brushless, AC, stepper, etc.			
translate ele to mech, control flow, control power, etc.			

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