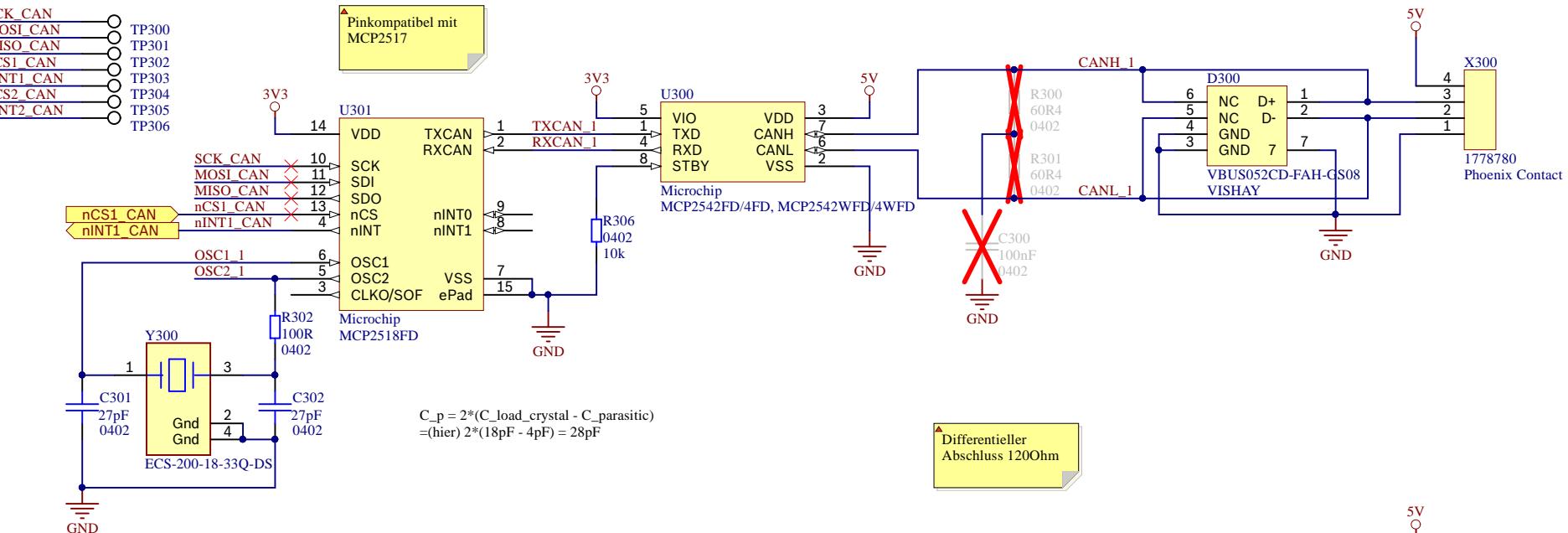
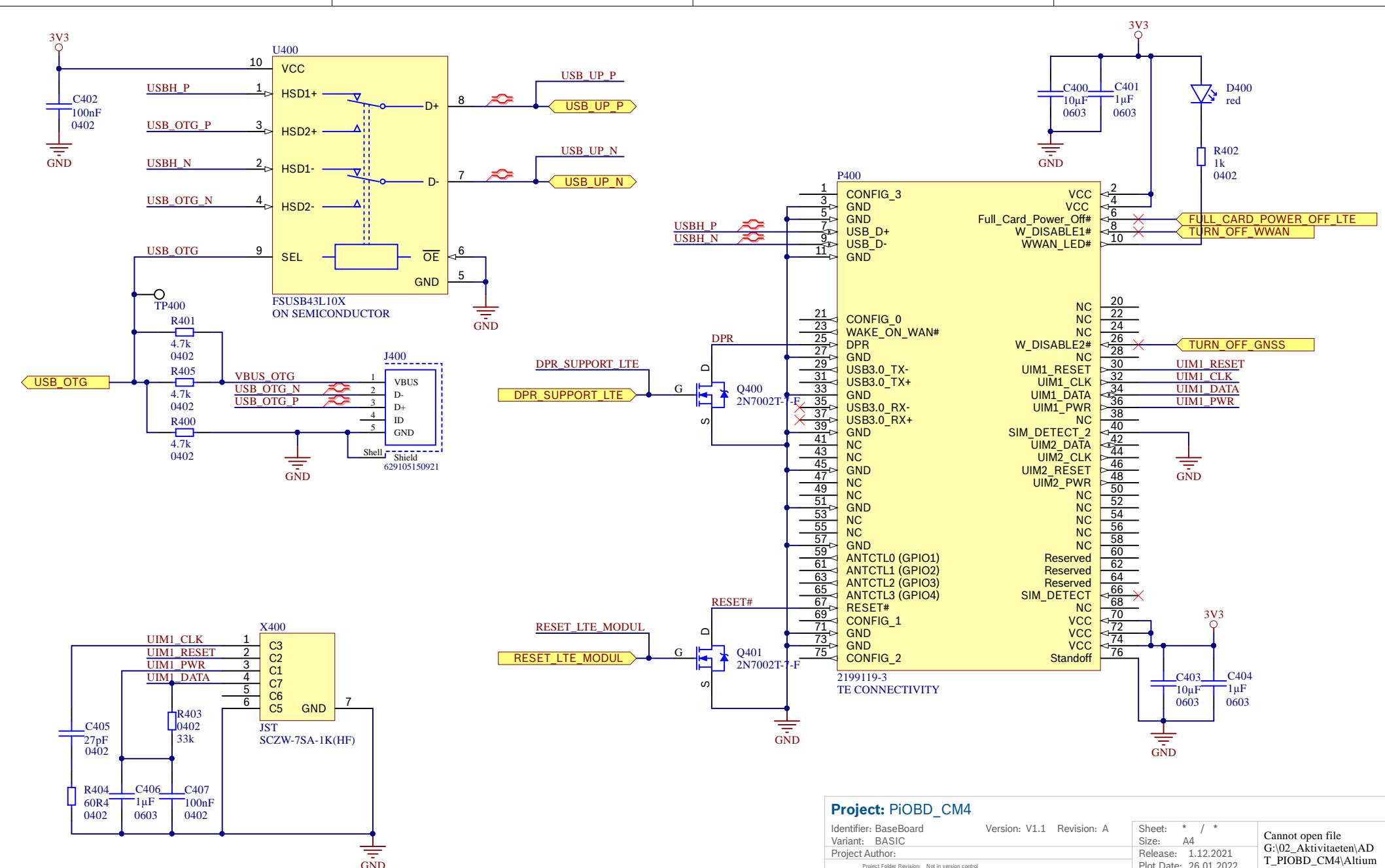


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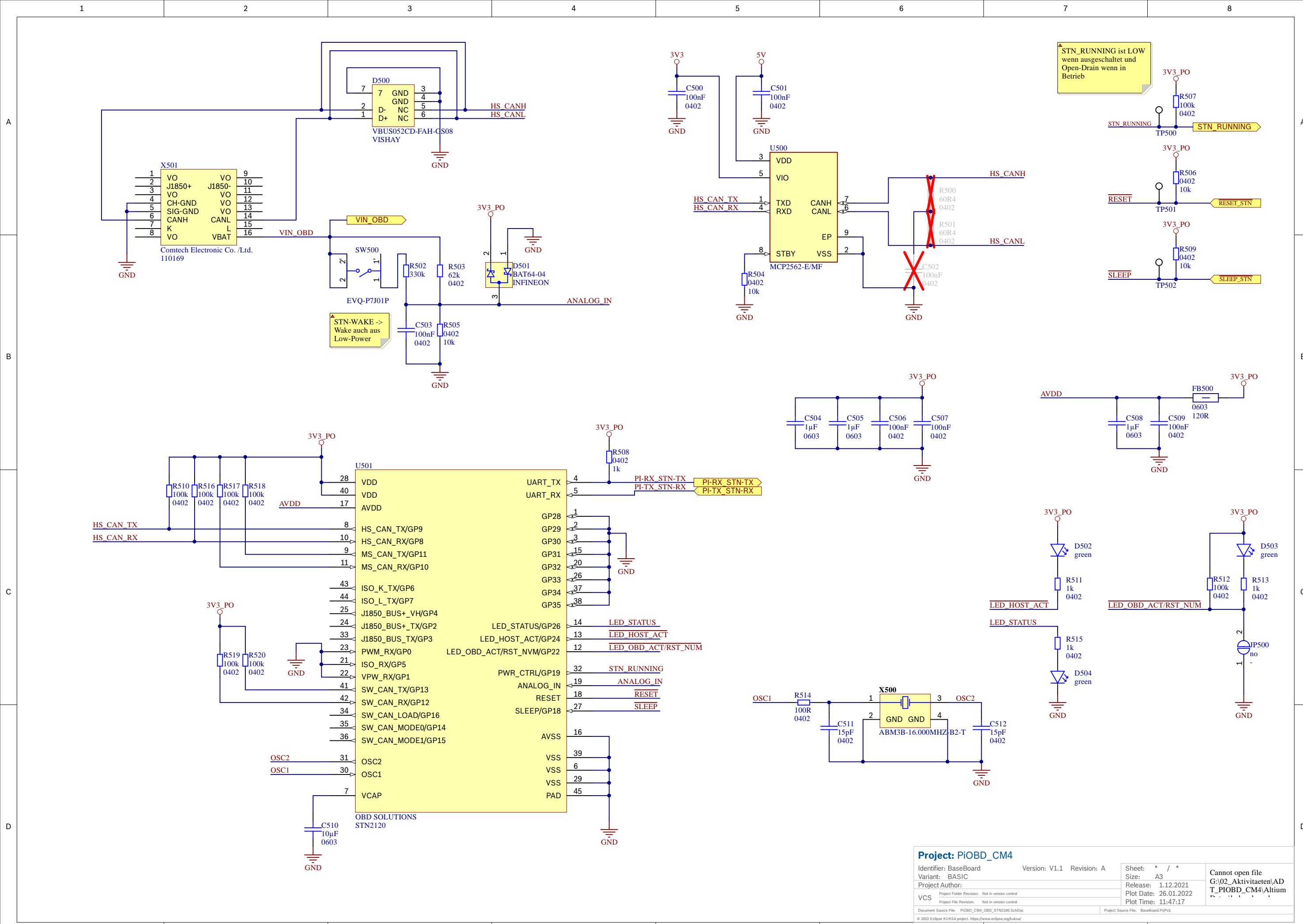
Project: PiOBD_CM4

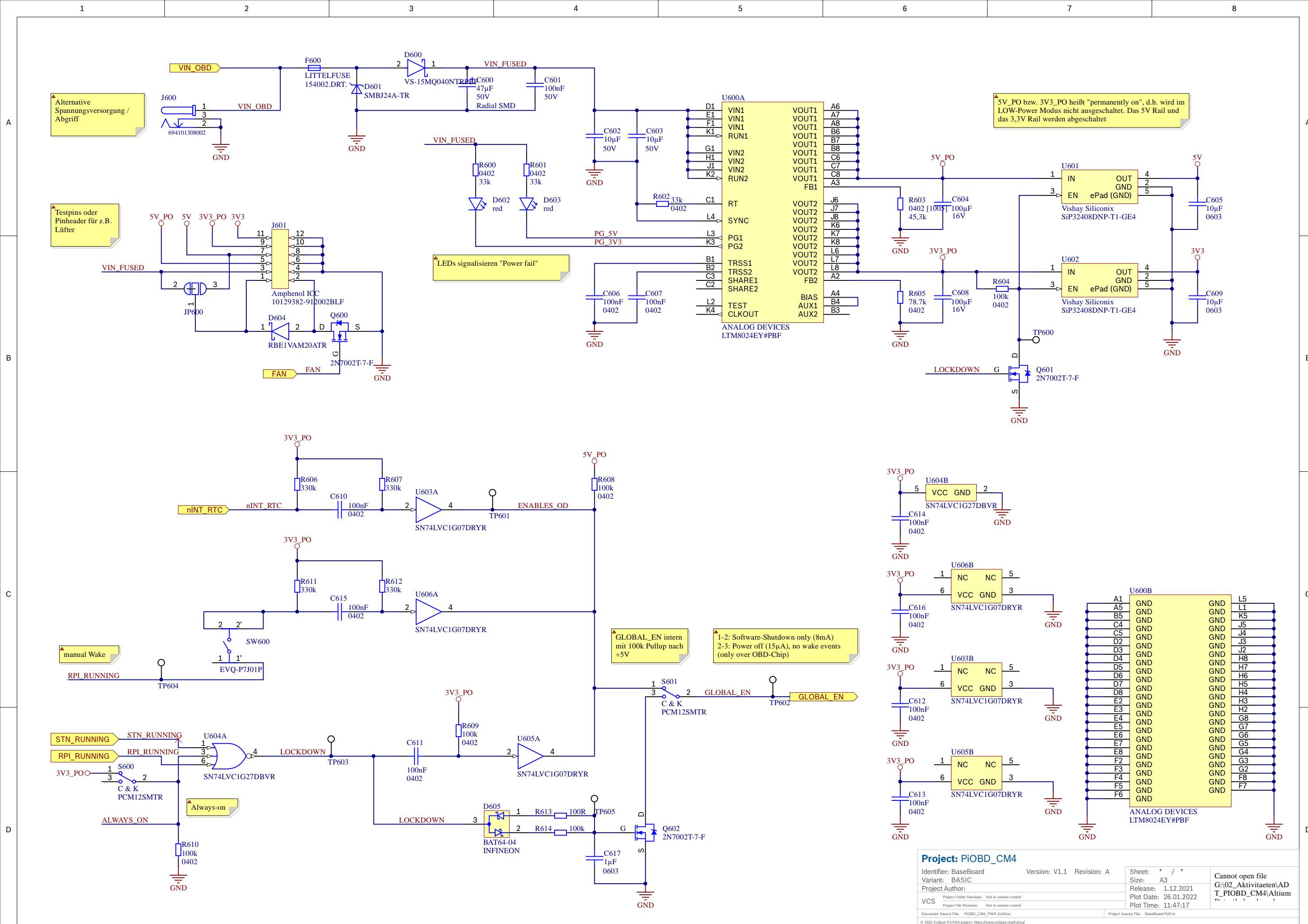
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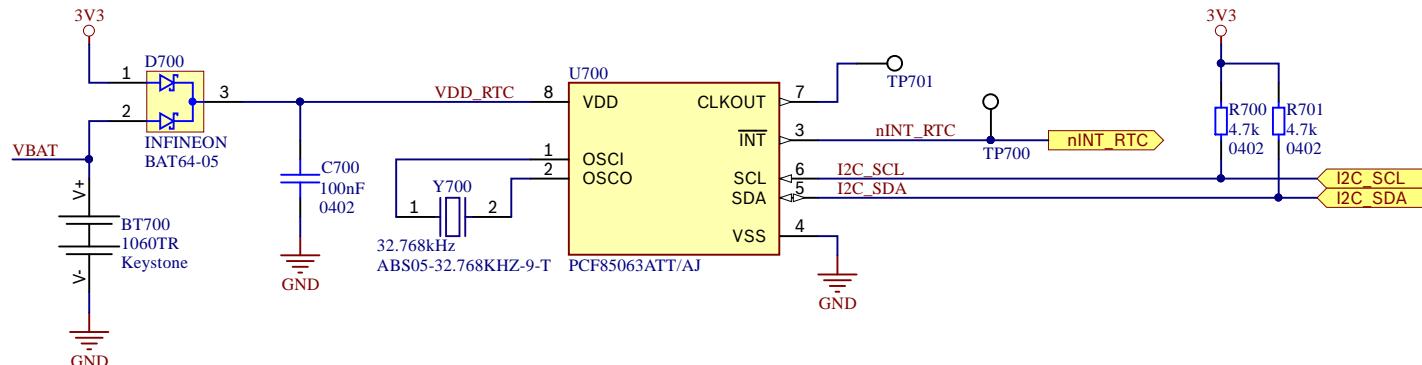


Project: PiOBD CM4

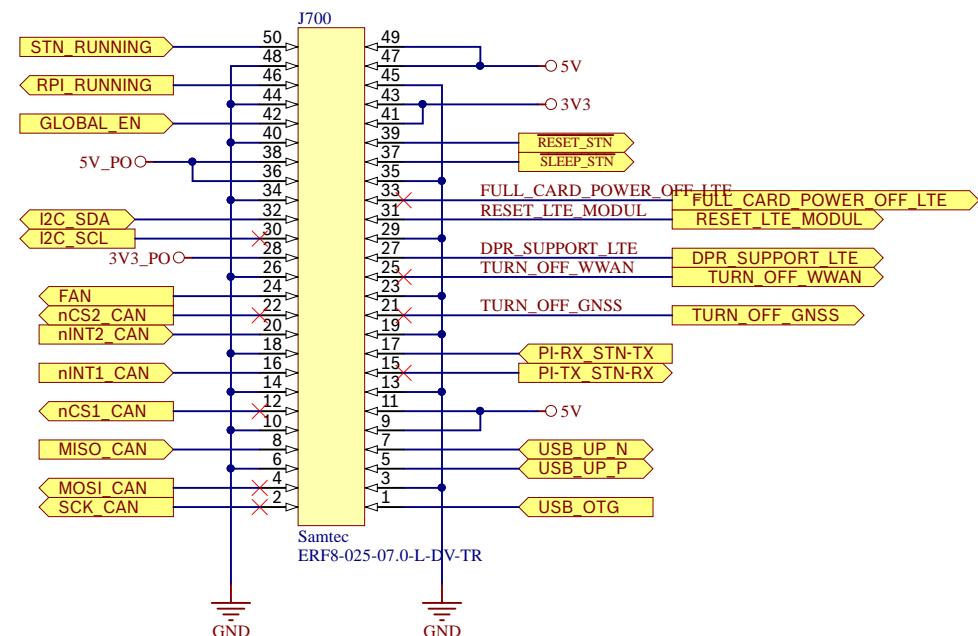
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Document Source File: PIOBD_CM4_LITE.SchDoc			Project Source File: BaseBoard.PrtPcb	
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RTC mit Weckerfunktion: Clock erzeugt eine fallende Flanke auf nINT_RTC -> AC-gekoppelt in Buffer mit Open Drain Ausgang -> RUN_PG Low, wenn RPI ausgeschaltet -> WAKE Taster als zweite Aufwach-Funktion

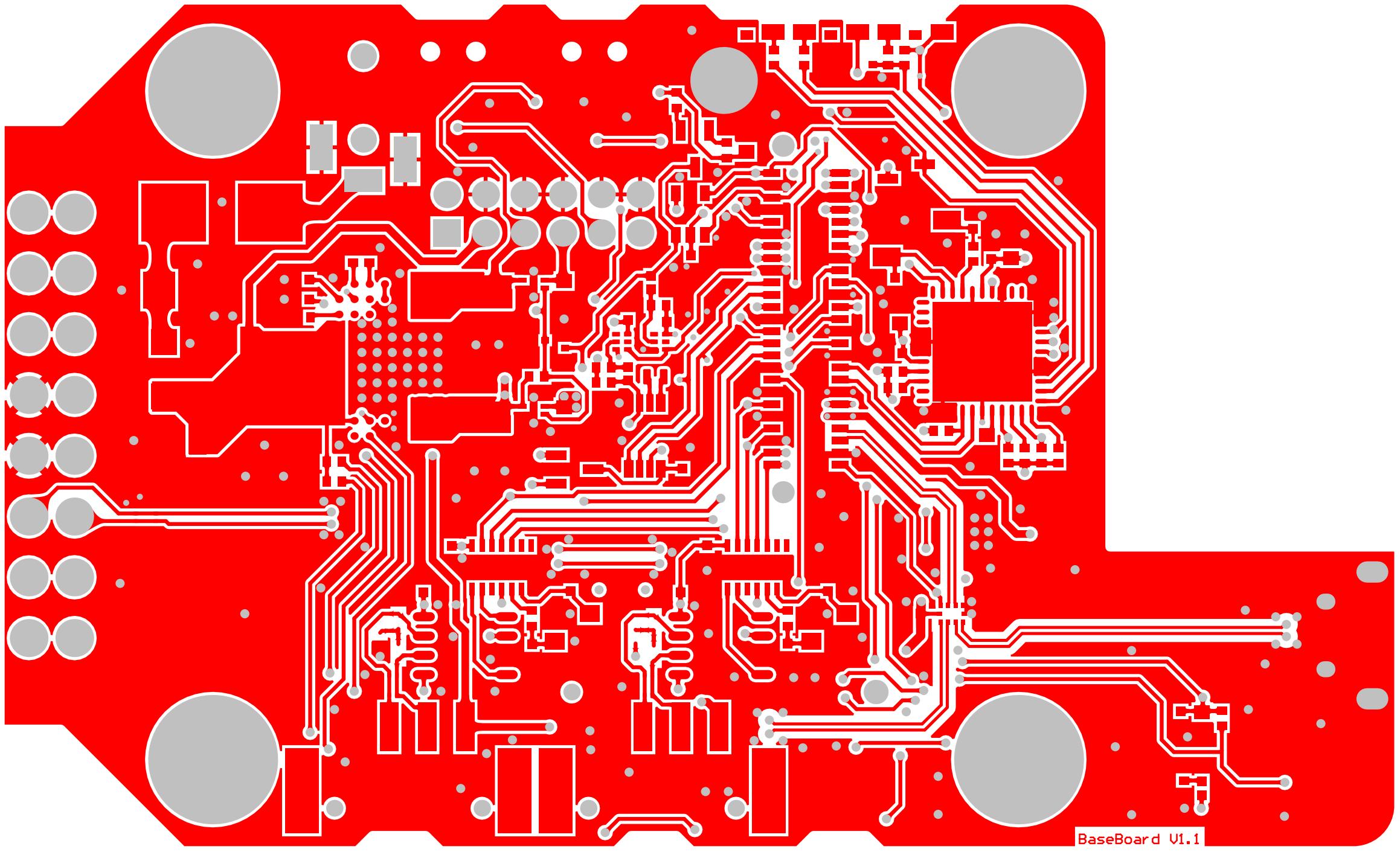


MP700
PCB mounting hole to build PE- connection
MP702
PCB mounting hole to build PE- connection
MP703
PCB mounting hole to build PE- connection

MP701
PCB mounting hole to build PE- connection
MP703
PCB mounting hole to build PE- connection

Project: PiOBD_CM4

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Document Source File: PiOBD_CM4_RTC.SchDoc		Project Source File: BaseBoard.PjPcb	Plot Time: 11:47:17



BaseBoard V1.1

