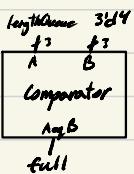
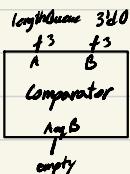
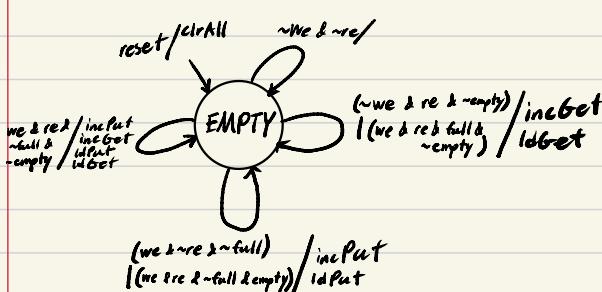
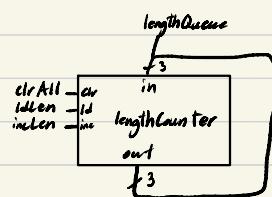
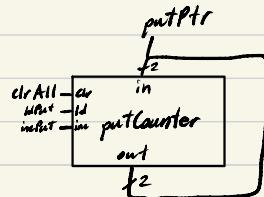
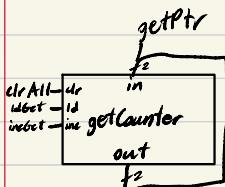
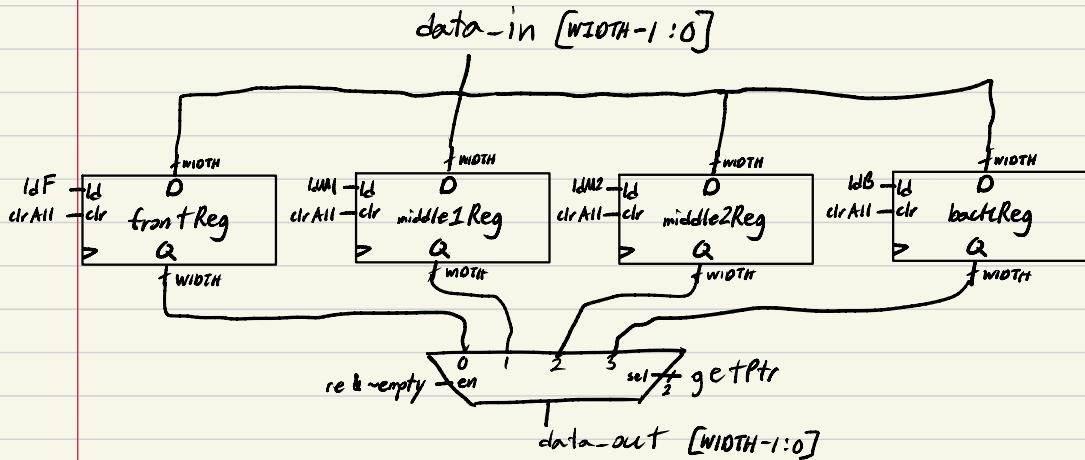
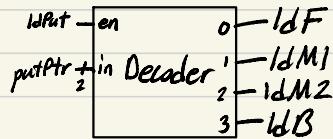


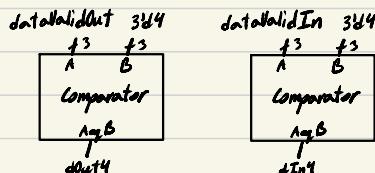
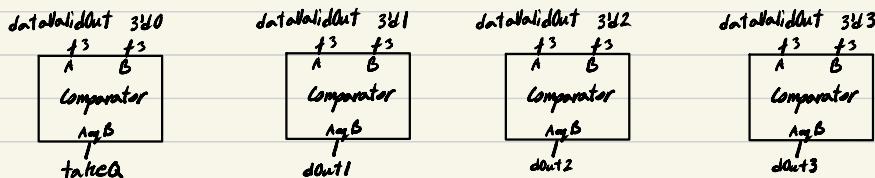
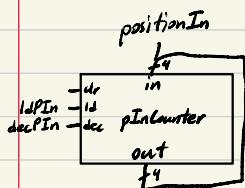
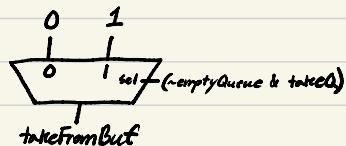
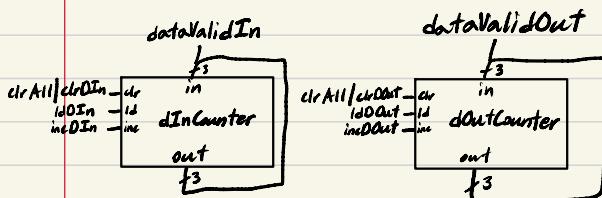
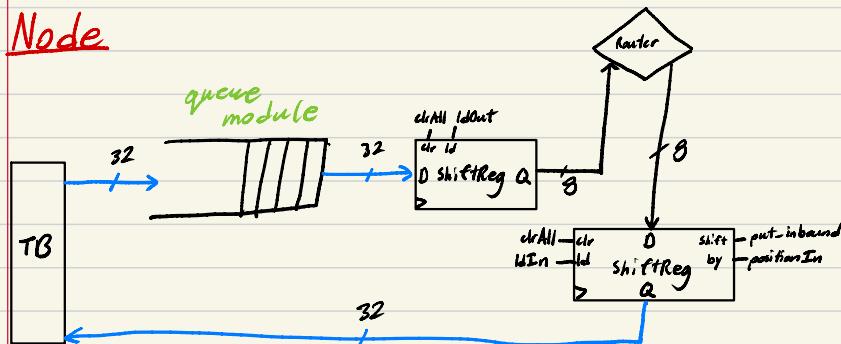
Queue/FIFO

WIDTH = 32

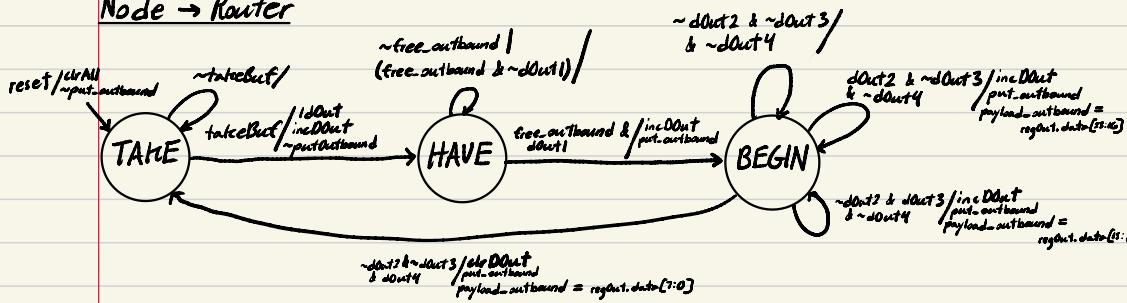
```
logic [3:0][width-1:0] Q;
logic [1:0] getPtr, putPtr;
logic [2:0] lengthQ;
```



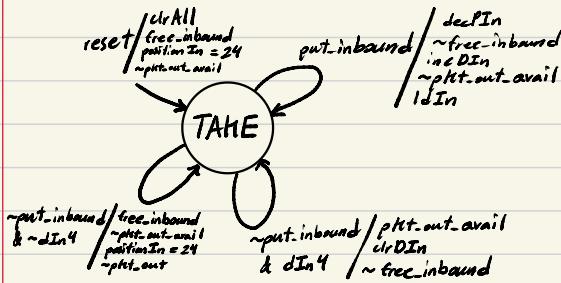
Node



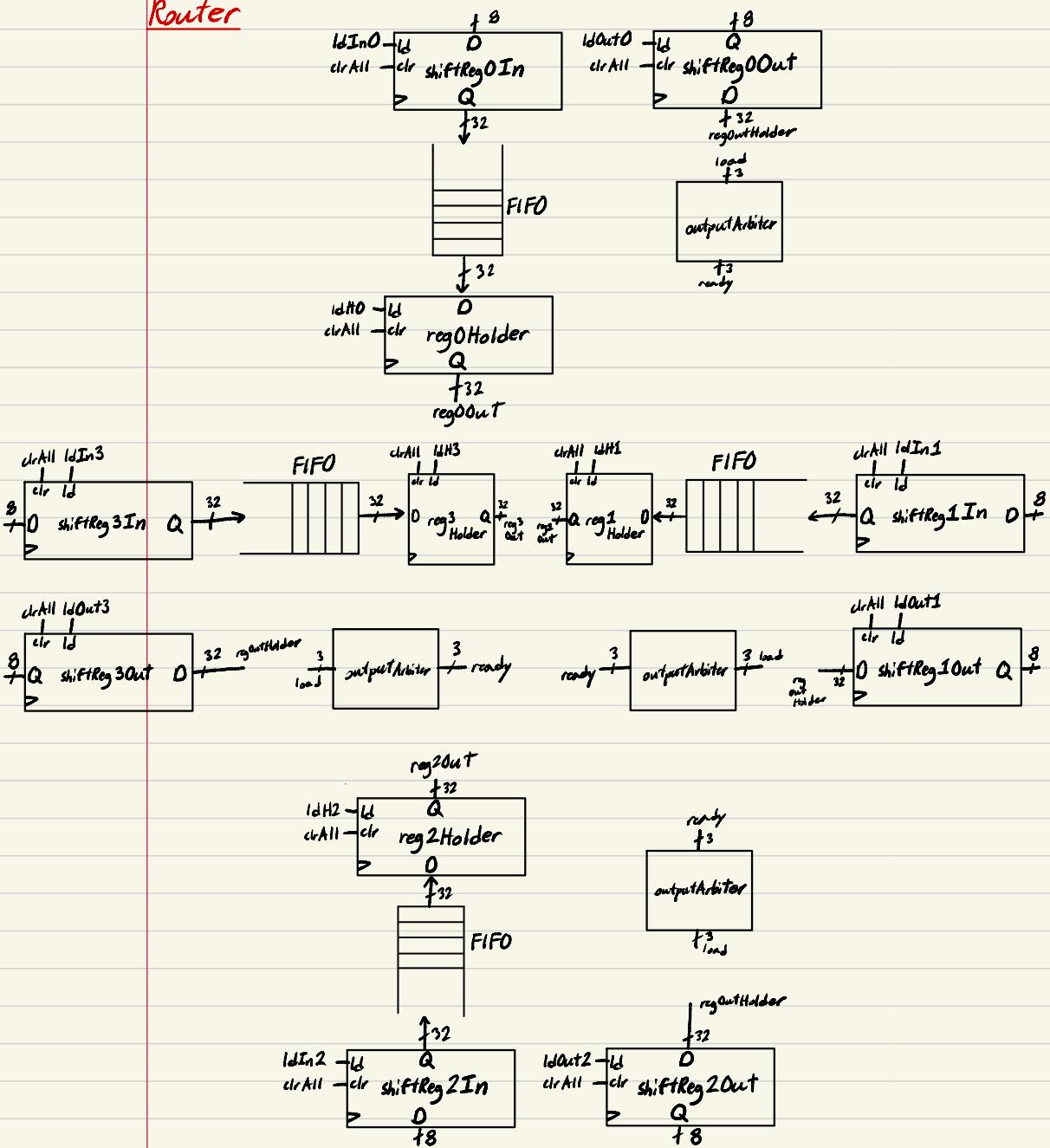
Node → Router

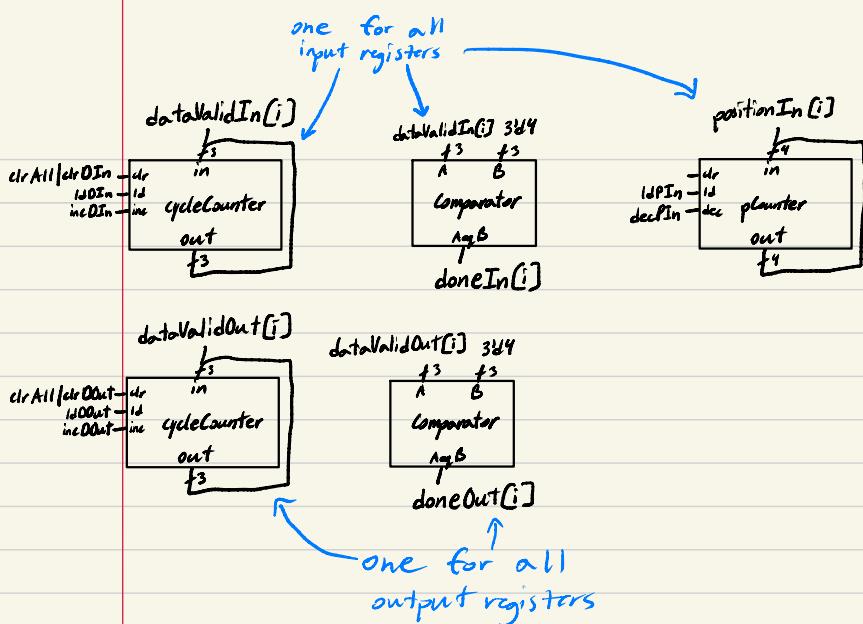


Router → TB

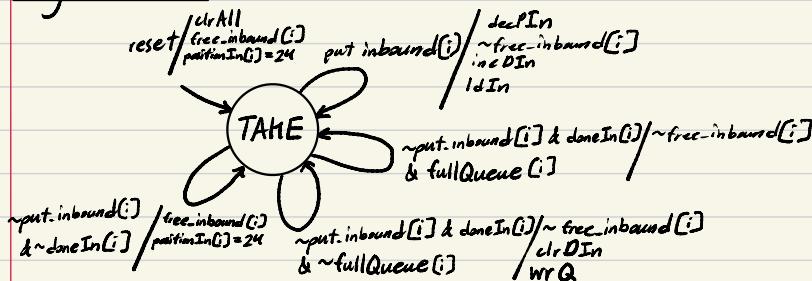


Router

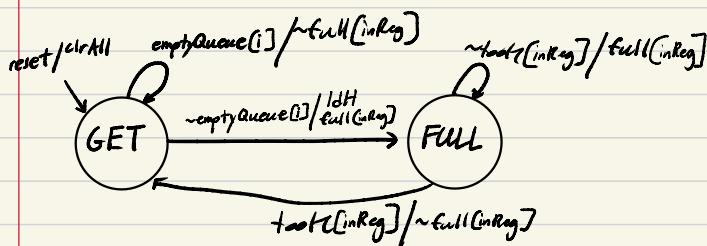




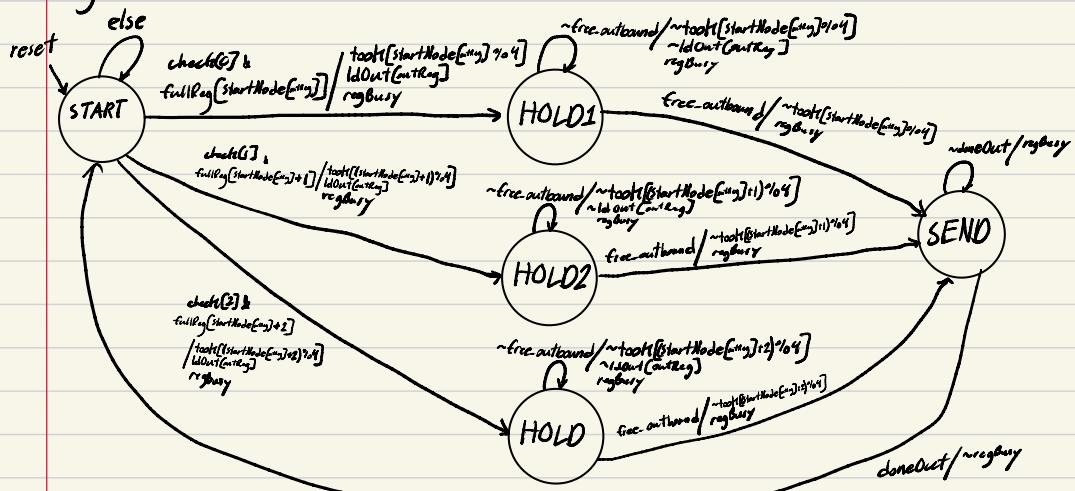
Register In



Register Holders



Register Out



Output Arbiter

