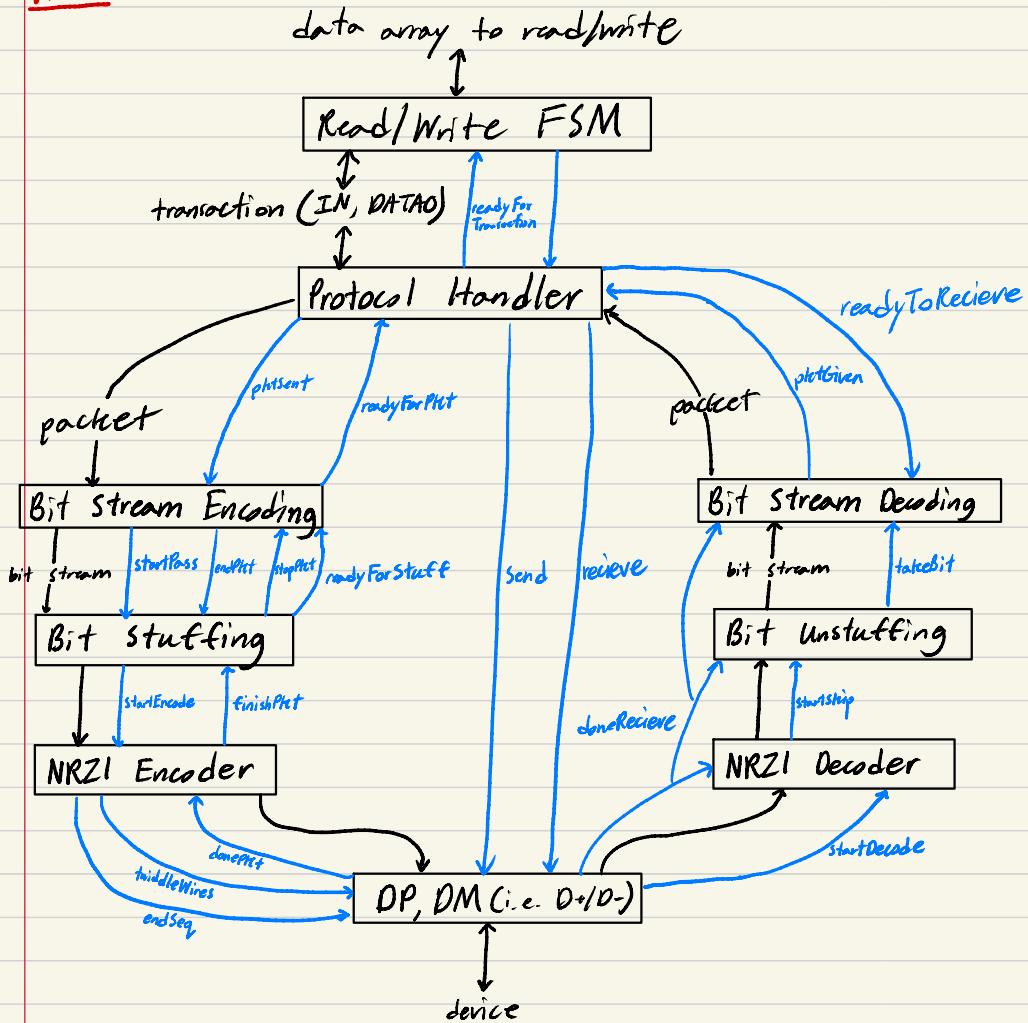
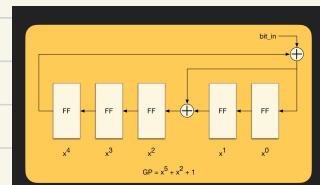
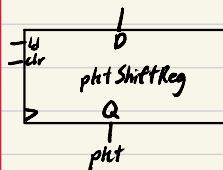


* Blue signals are the interlacing ones

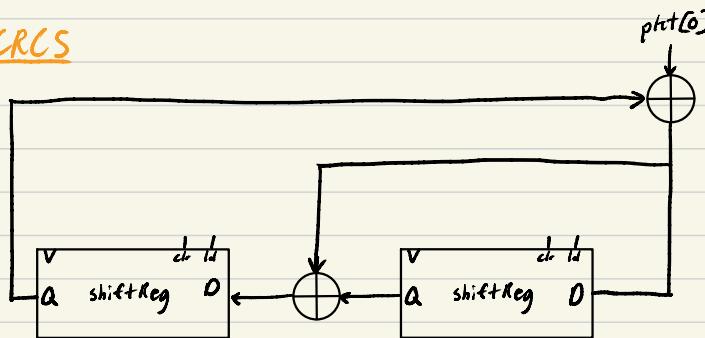
Host



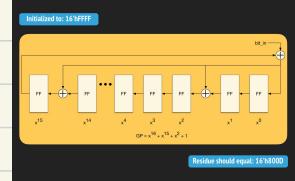
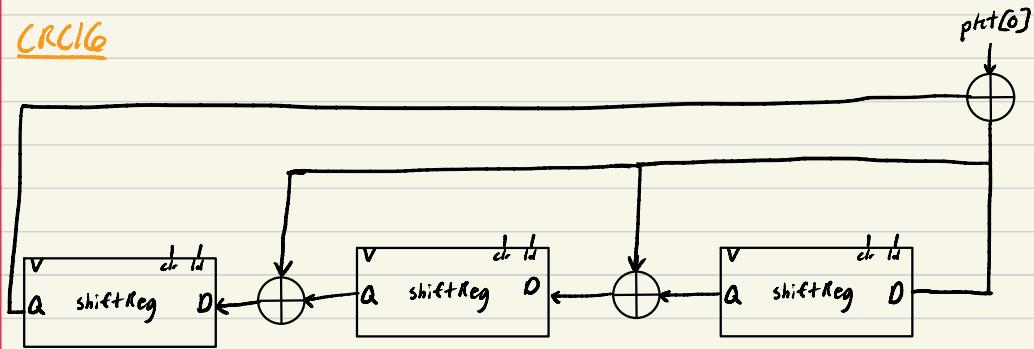
Bit Stream Encoding

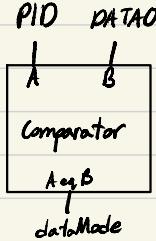
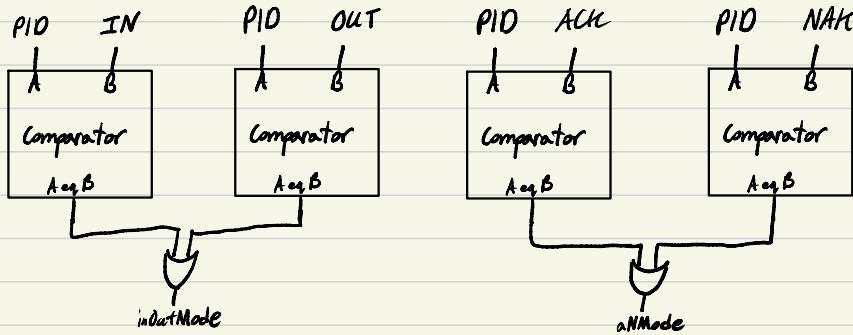
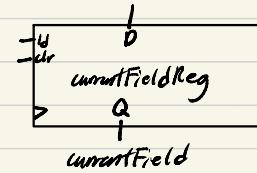
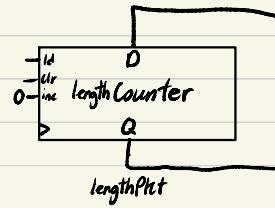
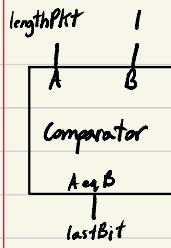


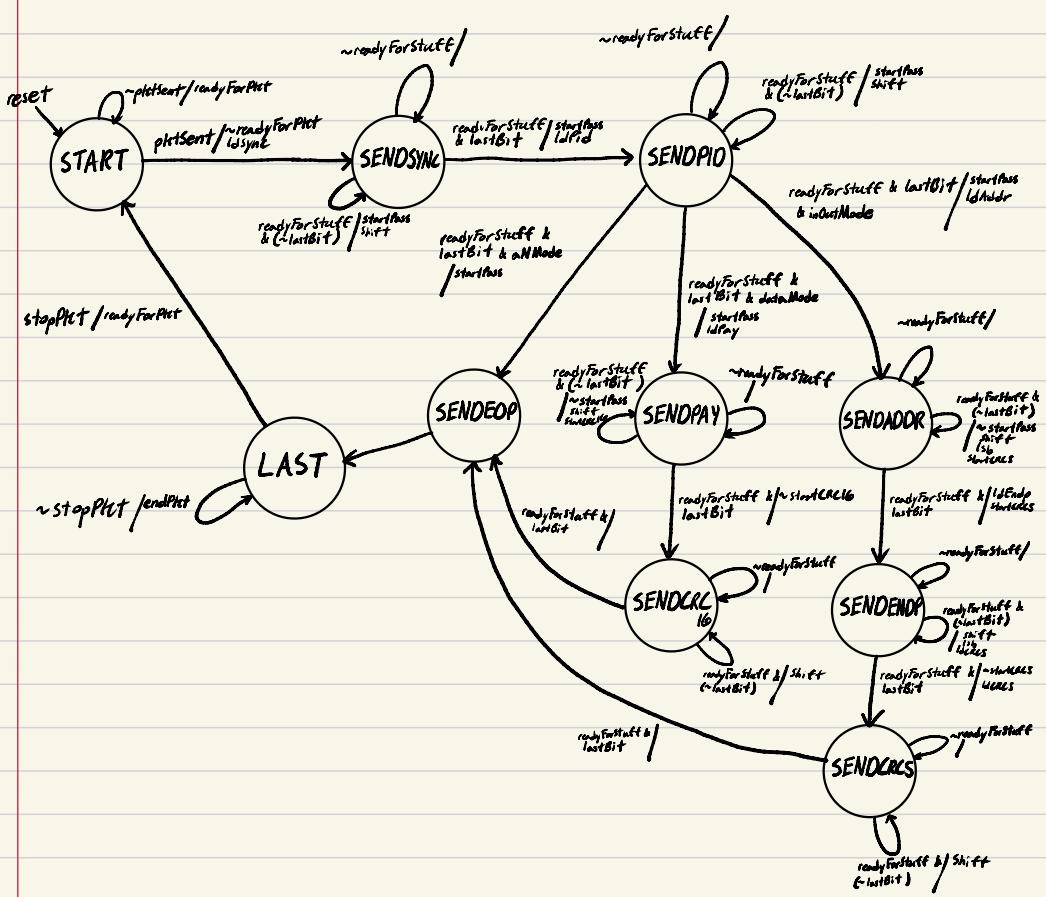
CRC8



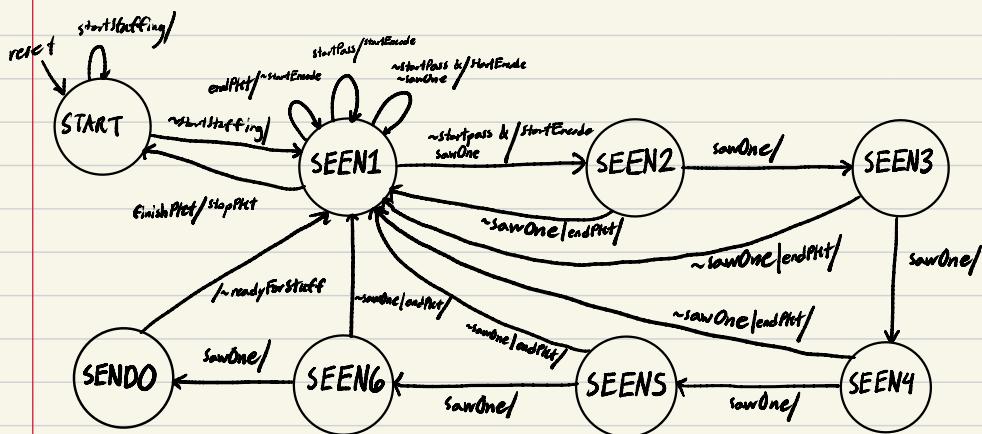
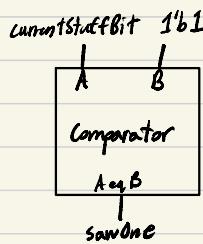
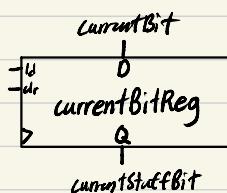
CRC16



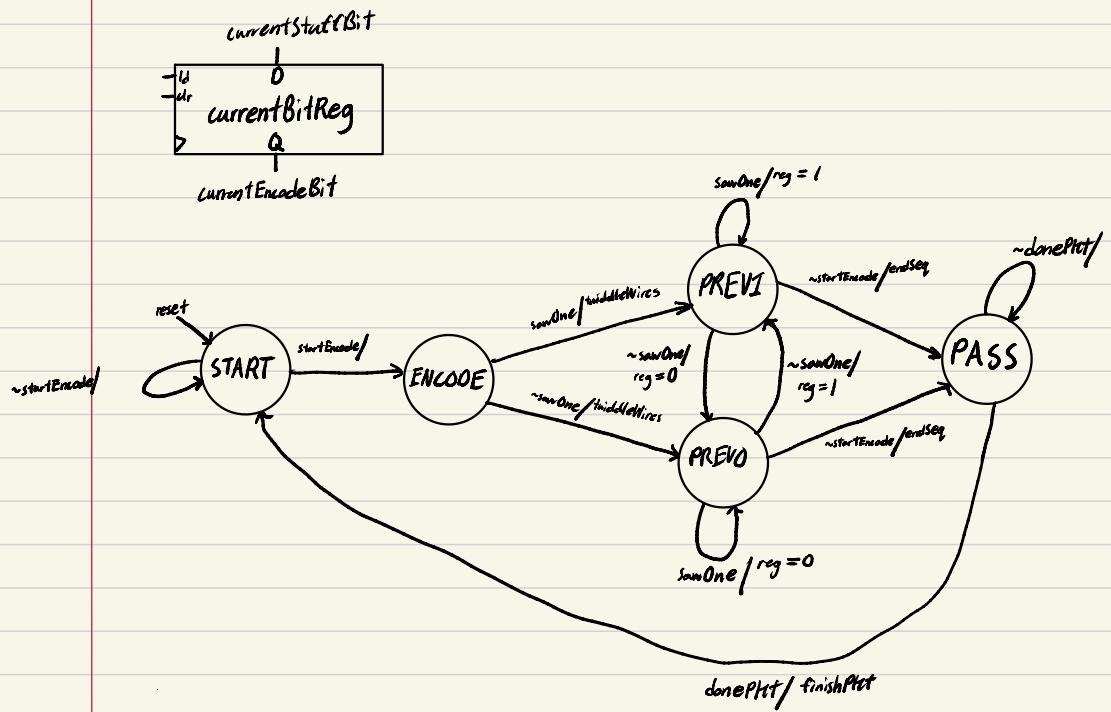




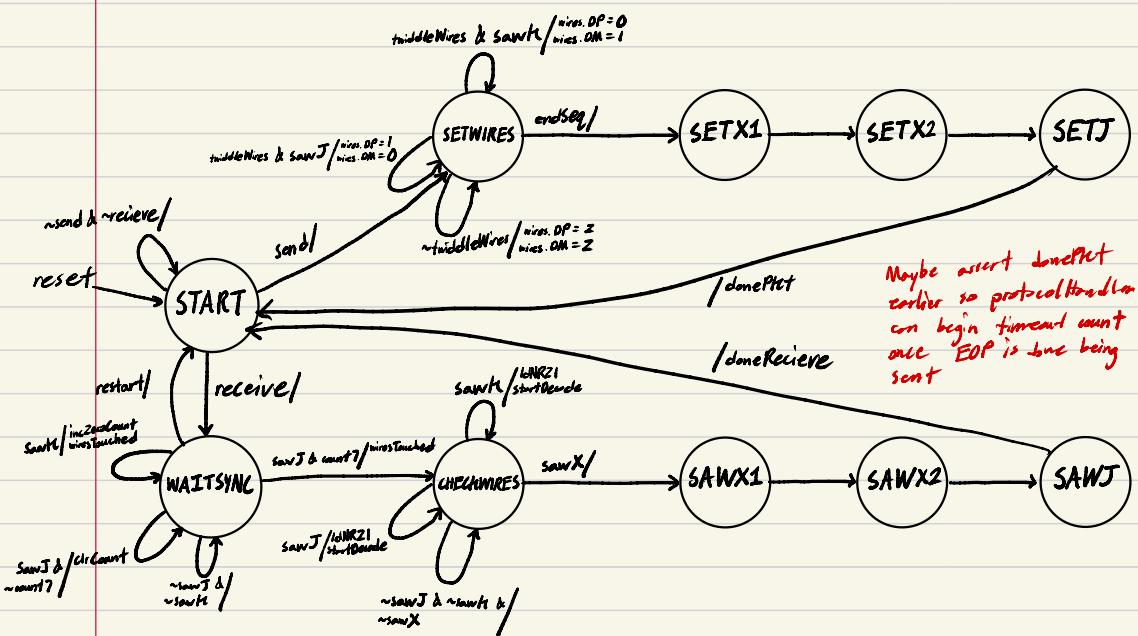
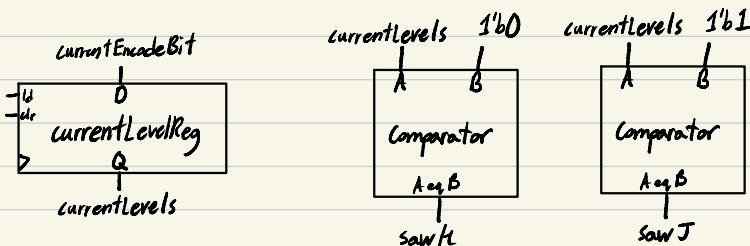
Bit Stuffing



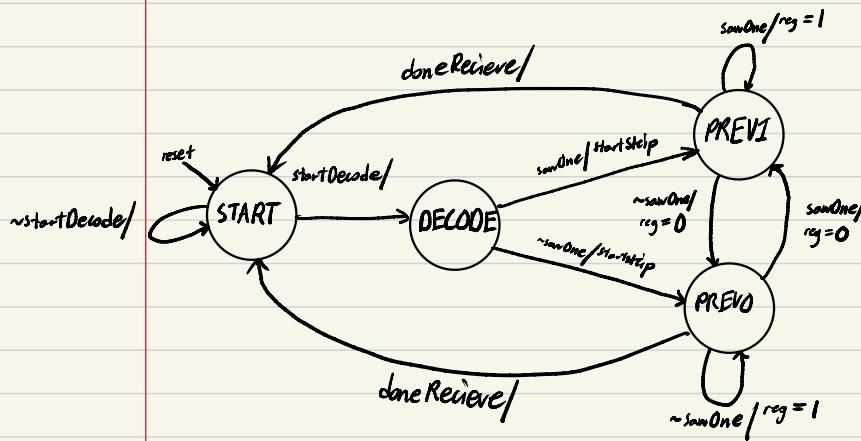
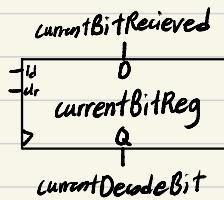
NRZI Encoding



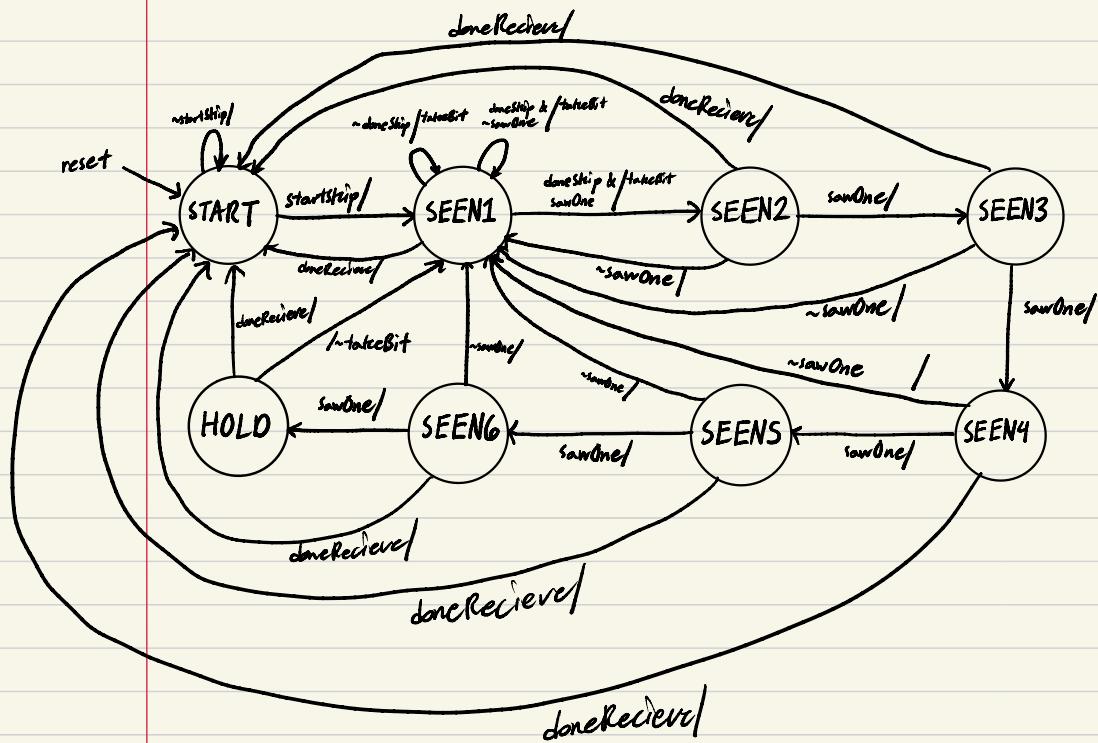
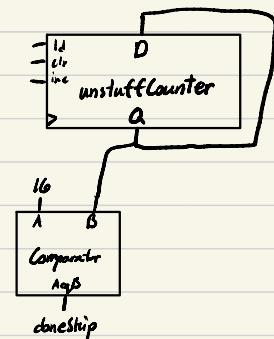
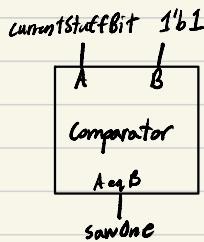
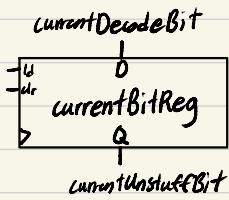
$D^0, D^{\pm}, \bar{D}^0, \bar{D}^{\pm}$



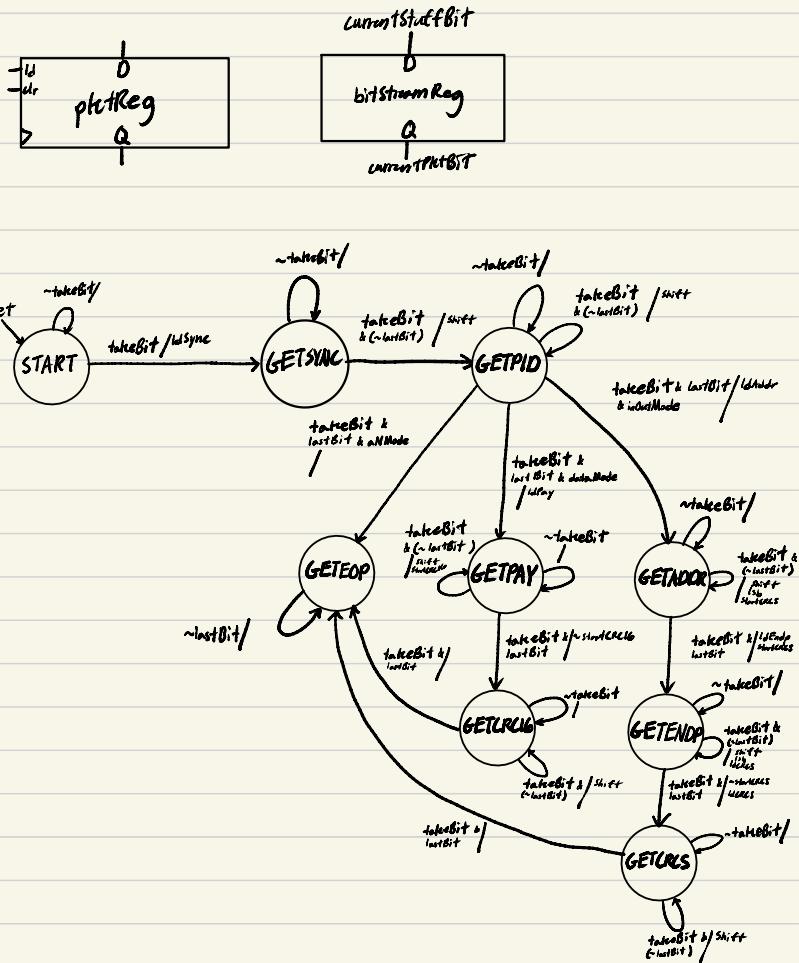
NRZI Decoding



Bit Unstuffing

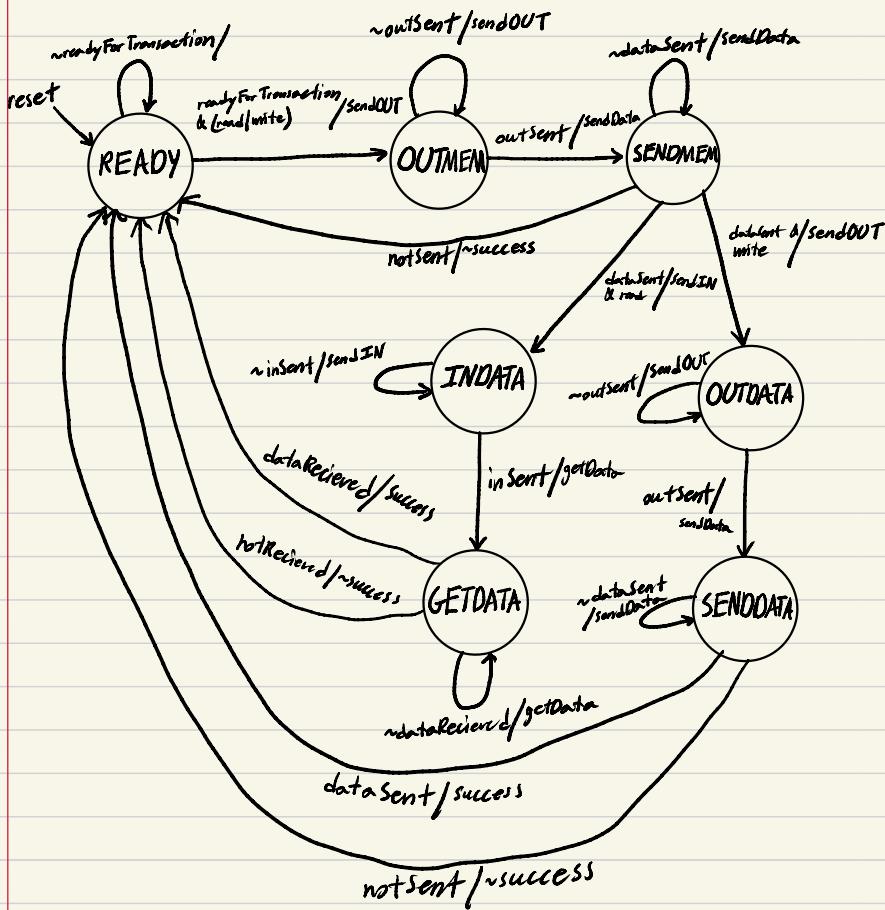


Bit Stream Decoding



read : OUT → DATA0 → IN → DATA0
write : OUT → DATA0 → OUT → DATA0

Read/Write FSM



Protocol Handler

