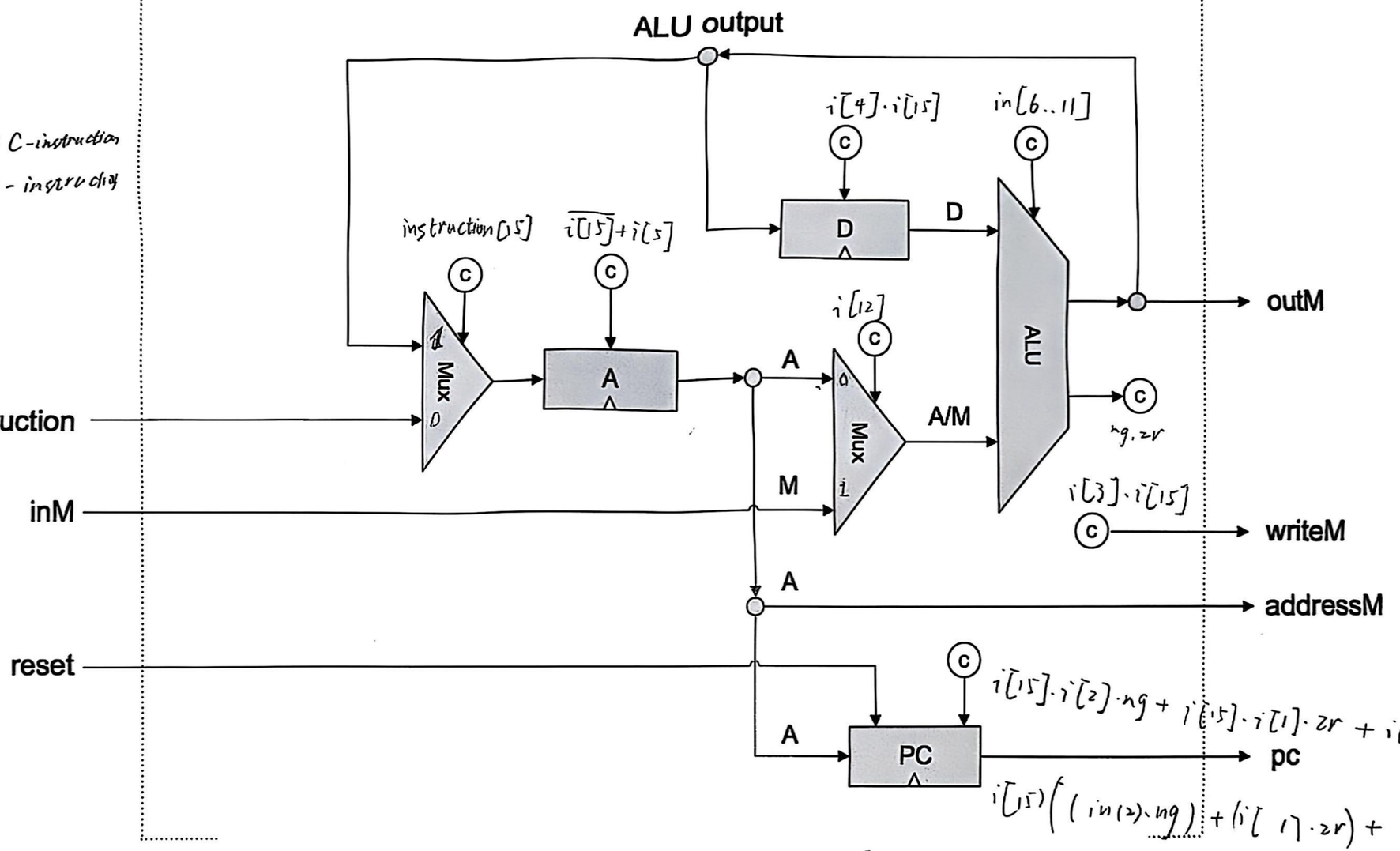
@16

0000000000010000

CPU Design

instruction[15] : True if C-instruction
else A-instruction



	comp							*dest*			*jump*			
	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
Binary:	1	1	1	a	c1	c2	c3	c4	c5	c6	d1	d2	d3	j1	j2	j3
				a=0 A,D,其他寄存	zx	nx	zy	ny	f	no	A	D	M	<	=	>
				a=1 M,D和M	f: 1=add, 0=and											