

MC60

Reference Design

GSM/GPRS/GNSS Module Series

Rev. A

Date: 2016-05-30



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About the Document

History

Revision	Date	Author	Description
A	2016-05-30	Tiger CHENG	Initial

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Preliminary

1 Introduction

1.1. Introduction

This document is a reference design for MC60 module. The schematics included in this document are preliminary and are subject to change without prior notice.

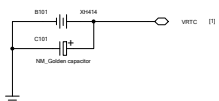
1.2. Schematics

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Preliminary

Module Design

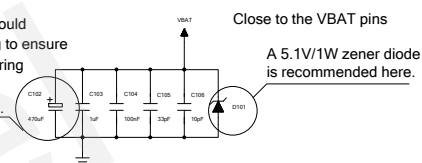
Charge golden capacitor or battery when VBAT is applied.

If unused, keep VRTC open.



As only powering the VRTC pin to keep the RTC will lead an error for about 5 minutes a day, it is recommended to power VBAT and VRTC pin at the same time when RTC function is needed.

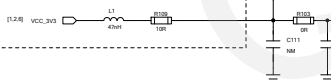
Capacitance of C102 should be chosen by debugging to ensure the max voltage drop during the burst transmission does not exceed 400mV.



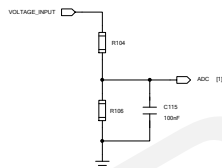
- VBAT ranges from 3.3V to 4.6V.
- Module drains the maximum current around 1.6A in burst time (577us).
- The width of VBAT trace is recommended to be more than 2mm.
- Capacitance is arranged in ascending order, with the smallest one closing to the VBAT pins, and all capacitance as close to the VBAT pins as possible.

AntennaType	Active Antenna Power Supply Circuit
Active	Need
Passive	No need

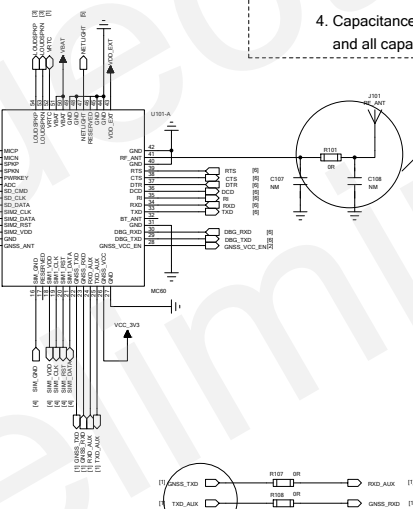
Active Antenna Power Supply Circuit



Reference Circuit of ADC



The voltage range of ADC input channel is from 0 to 2.8V. Please select a high-precision divider resistance.



For RF layout, please refer to RF_Layout_Application_Note. A Pi match circuit is recommended to be added.

NOTE

MC60 module does not support PCM / BT/SD card function

Don't change those interface for other uses.

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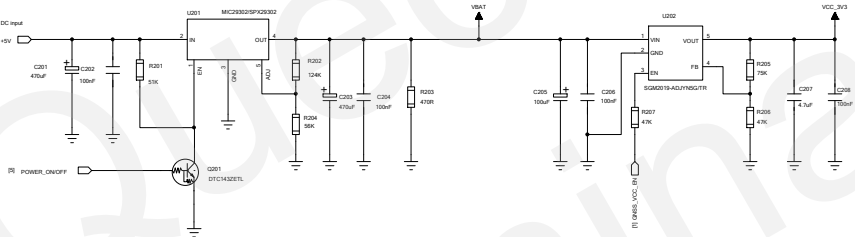
Power Supply

NOTE

The voltage converter should provide a minimum current of 2.0A.

LDO Application

It is used when the DC input voltage is below 7V.



DC-DC Application

1. It can be used when the input voltage is above 7V in vehicle application.
2. Use DCDC to convert high input voltage to 5V and LDO will generate 4V & 3.3V typical voltage for the module.

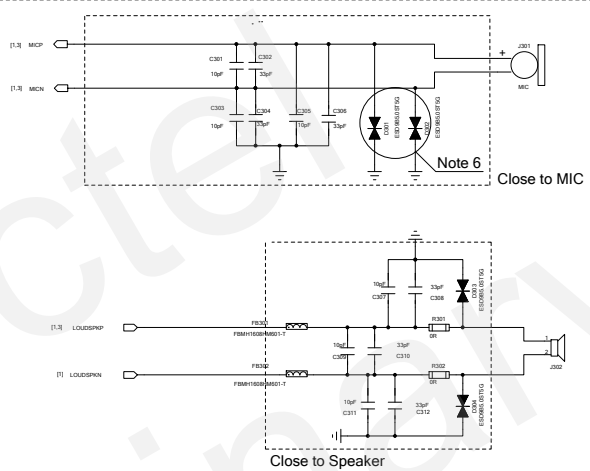


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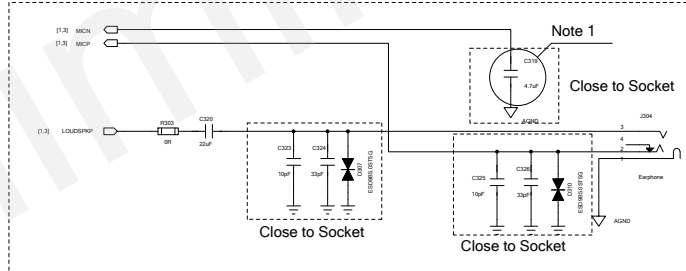
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Audio Design

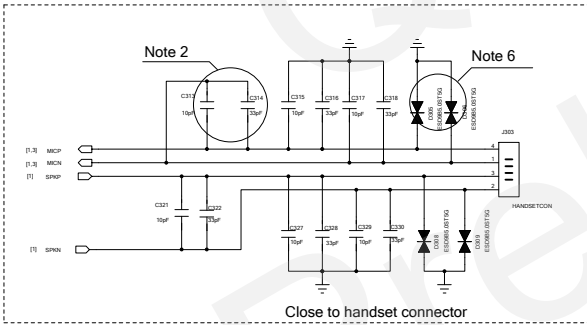
Hands-free Application of AIN/AOUT2



Earphone Application of AIN/AOUT2



Handsets Application of AIN/AOUT1

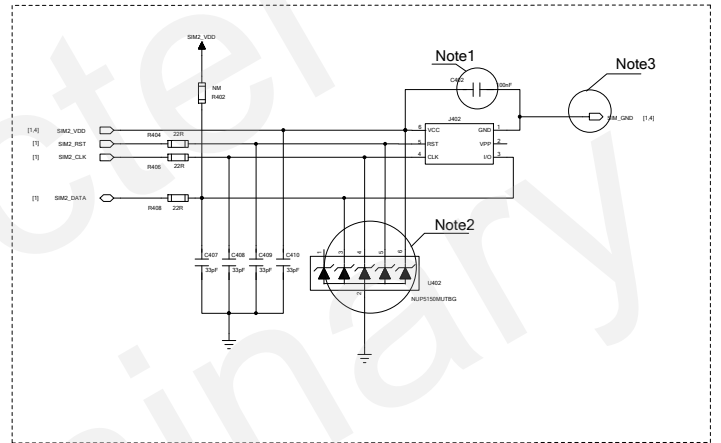
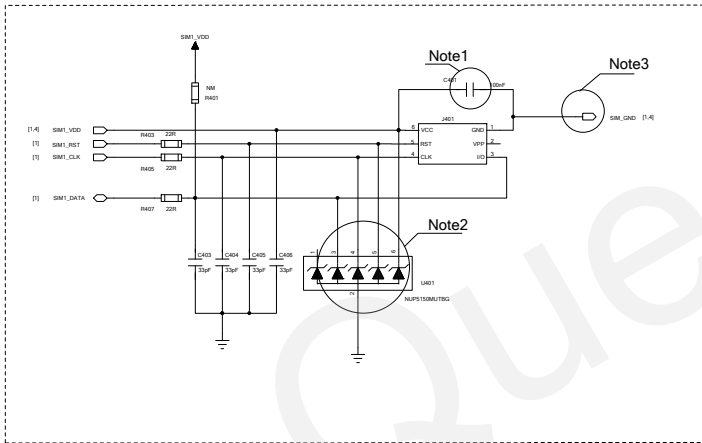


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SIM Card

SIM Card Interface



NOTES

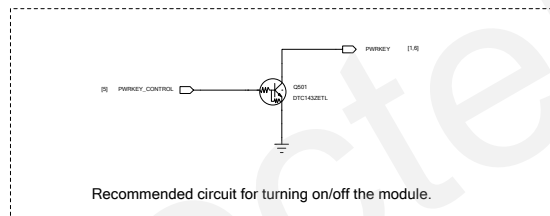
1. The value of C401 and C402 should be less than 1uF.
2. U401 and U402 is used for protecting SIM card against ESD, and the junction capacitance should be less than 50pF. It should be placed nearby SIM card holder.
3. For MC60 module, ground of SIM card is recommended to be routed to the Pin 16("SIM_GND") of the module separately.

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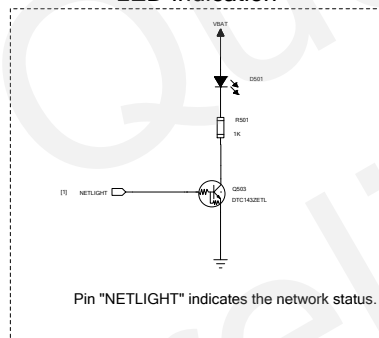
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Driver

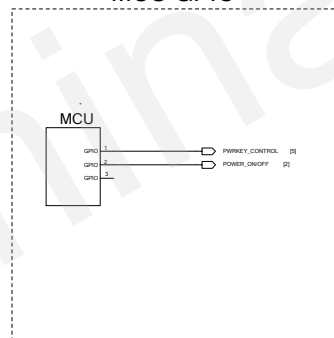
Turn on/off



LED Indication



MCU GPIO



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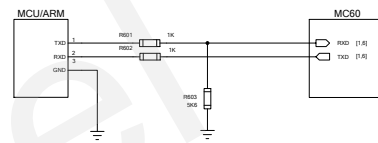
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Serial Interface

Electric characteristics of the module's input and output port:

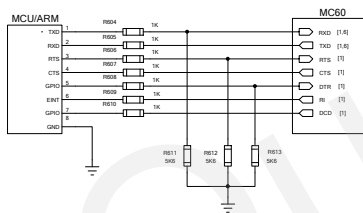
$V_{OHmin}=0.85 \times VDD_EXT$
 $V_{OLmax}=0.15 \times VDD_EXT$
 $V_{ILmax}=0.25 \times VDD_EXT$
 $V_{IHmin}=0.75 \times VDD_EXT$
 $V_{IHmax}=VDD_EXT+0.2V$
 $VDD_EXT=2.8V$ (typical value)

Connection of Three-line UART Port for 3.3V System



Please pay attention to the level match of UART in product application.

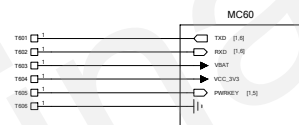
Connection of All Functional UART Port for 3.3V System



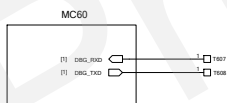
NOTES

1. CTS&RTS will be used for HW flow control when mass data has been sent.
2. When AT+QSCLK=1 is set on the module, customer's application can control the module to enter into or exit from the sleep mode through the pin DTR. When DTR is set to high level, and there is no on-air or hardware interrupt, such as GPIO interrupt or data on serial port, the module will enter into sleep mode automatically.
3. RI will output an indication signal when activity such as voice call or SMS is coming.
4. DCD is mainly applied in modem communication (PPP). The active status represents the communication link has been set up.
5. Please pay attention to the level match of UART in product application.

It is recommended to reserve the points for upgrading the firmware.



Please pay attention to the level match of UART in product application.



It is recommended to reserve the points for debug port.

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