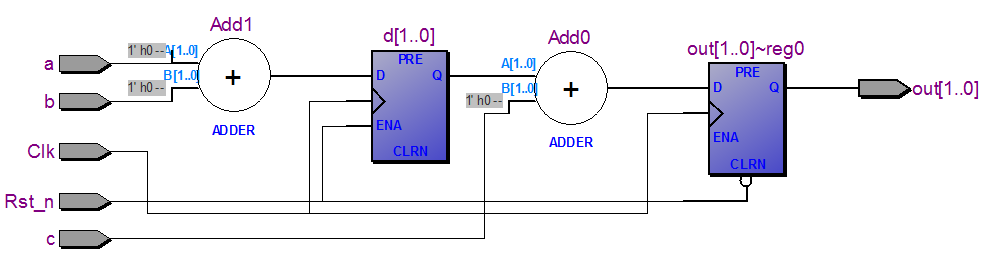
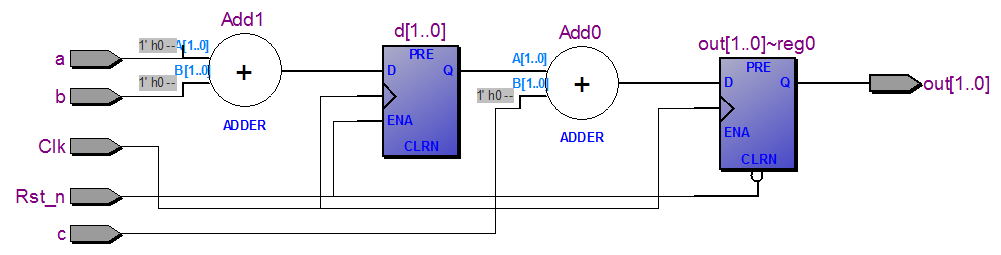


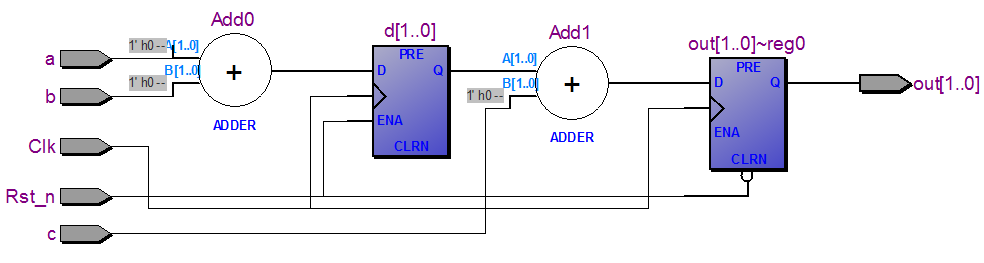
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| **module** block\_nonblock**(**Clk**,**Rst\_n**,**a**,**b**,**c**,**out**);**  **input** Clk**;**  **input** Rst\_n**;**  **input** a**,**b**,**c**;**  **output** **reg** **[**1**:**0**]**out**;**    //out = a + b + c;    //d = a+b;  //out = d + c;    **reg** **[**1**:**0**]** d**;**    **always@(posedge** Clk **or** **negedge** Rst\_n**)**  **if(!**Rst\_n**)**  out **=** 2'b0**;**  **else** **begin**  d **=** a **+** b**;**  out **=** d **+** c**;**  **end**    **endmodule** |
|  |



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| **module** block\_nonblock**(**Clk**,**Rst\_n**,**a**,**b**,**c**,**out**);**  **input** Clk**;**  **input** Rst\_n**;**  **input** a**,**b**,**c**;**  **output** **reg** **[**1**:**0**]**out**;**    //out = a + b + c;    //d = a+b;  //out = d + c;    **reg** **[**1**:**0**]** d**;**    **always@(posedge** Clk **or** **negedge** Rst\_n**)**  **if(!**Rst\_n**)**  out **=** 2'b0**;**  **else** **begin**  out **=** d **+** c**;**  d **=** a **+** b**;**  **end**    **endmodule** |



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| **module** block\_nonblock**(**Clk**,**Rst\_n**,**a**,**b**,**c**,**out**);**  **input** Clk**;**  **input** Rst\_n**;**  **input** a**,**b**,**c**;**  **output** **reg** **[**1**:**0**]**out**;**    //out = a + b + c;    //d = a+b;  //out = d + c;    **reg** **[**1**:**0**]** d**;**    **always@(posedge** Clk **or** **negedge** Rst\_n**)**  **if(!**Rst\_n**)**  out **<=** 2'b0**;**  **else** **begin**  out **<=** d **+** c**;**  d **<=** a **+** b**;**  **end**    **endmodule** |



|  |
| --- |
| **module** block\_nonblock**(**Clk**,**Rst\_n**,**a**,**b**,**c**,**out**);**  **input** Clk**;**  **input** Rst\_n**;**  **input** a**,**b**,**c**;**  **output** **reg** **[**1**:**0**]**out**;**    //out = a + b + c;    //d = a+b;  //out = d + c;    **reg** **[**1**:**0**]** d**;**    **always@(posedge** Clk **or** **negedge** Rst\_n**)**  **if(!**Rst\_n**)**  out **<=** 2'b0**;**  **else** **begin**  d **<=** a **+** b**;**  out **<=** d **+** c**;**  **end**    **endmodule** |

