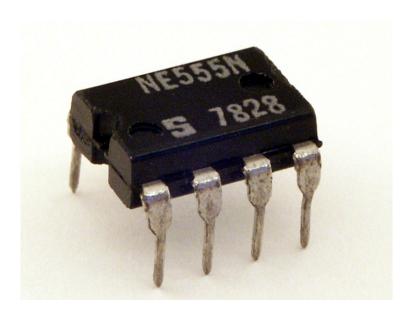
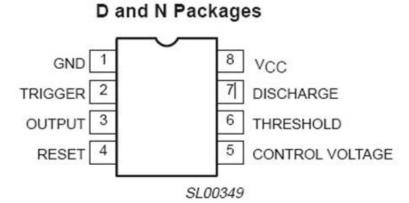
555 timer IC

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Logic Design Lab, Spring 2014.
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555 timer IC



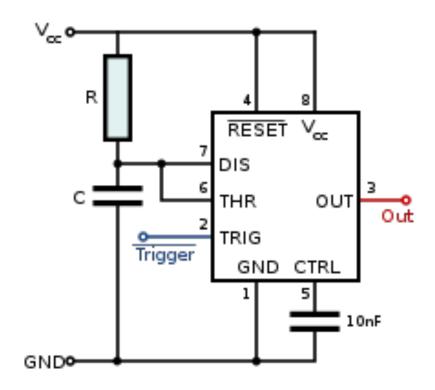


- Monolithic timing circuit
- Highly stable controller
- Frequency and the duty cycle are both accurately controlled with two extern al resistors and one capacitor
- http://en.wikipedia.org/wiki/555_timer_IC

Monostable Mode

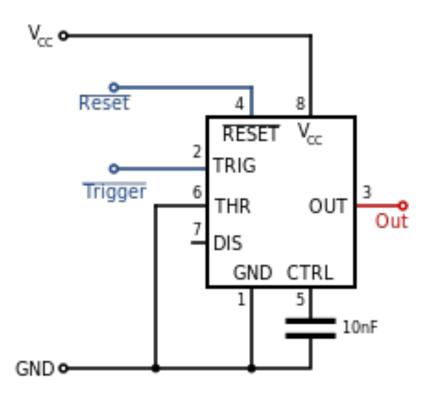
- The 555 timer acts as a "o ne-shot" pulse generator.
- The output pulse width of ti me t,

 $t = RC \ln(3) \approx 1.1RC \approx 1.1$ in ohms and C is in farads



Bistable Mode

- The 555 timer acts as a basic flip-flop.
- The trigger and reset input s are held high via Pull-up resistors while the threshol d input is simply floating



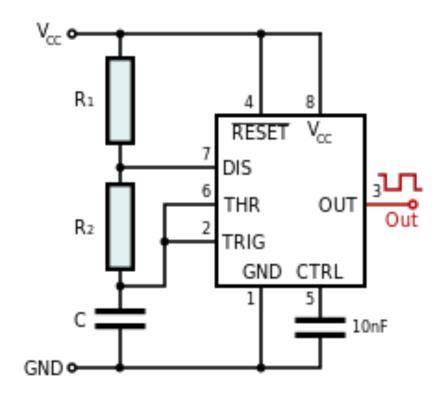
Astable Mode

- The 555 timer puts out a c ontinuous stream of rectan gular pulses having a spec ified frequency.
- The frequency of the pulse stream depends on the va lues of R₁, R₂ and C:

$$f = \frac{1}{\ln(2) \cdot C \cdot (R_1 + 2R_2)}$$

$$\text{high} = \ln(2) \cdot (R_1 + R_2) \cdot C$$

$$\text{low} = \ln(2) \cdot R_2 \cdot C$$



CAPLab

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