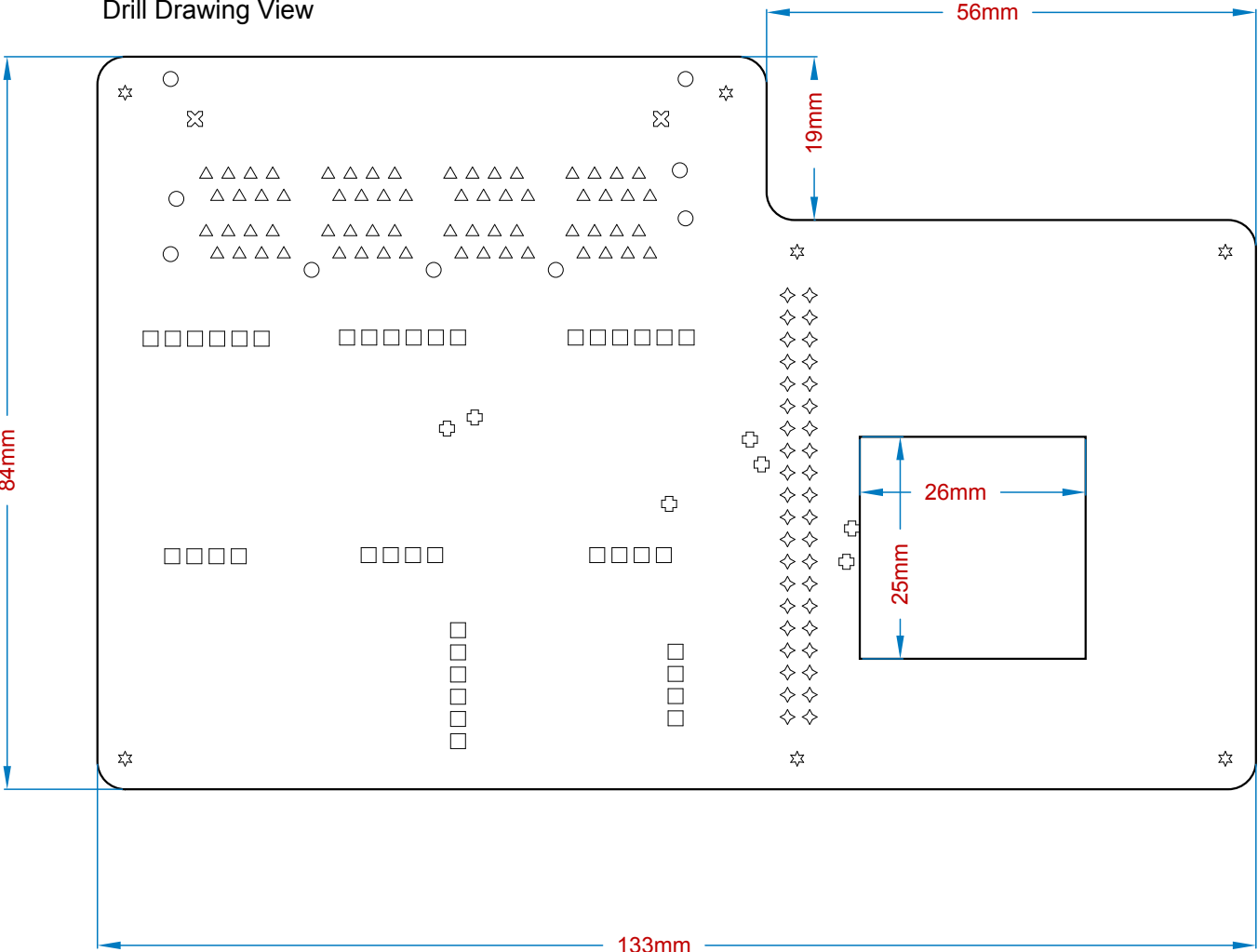


1

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Drill Drawing View



2

2

Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
◇	40	1.02	Plated	
□	40	1.02	Plated	
○	9	1.65	Plated	
△	64	0.97	Plated	
⊠	2	3.33	Non-Plated	
☆	7	3.20	Non-Plated	
⊞	7	0.71	Plated	

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Layer Stack Legend

	Material	Layer	Thickness	Dielectric Material	Type	Gerber
		Top Overlay			Legend	GTO
	Surface Material	Top Solder	0.01mm	Solder Resist	Solder Mask	GTS
	Copper	Top Layer	0.04mm		Signal	GTL
			0.52mm	FR-4	Dielectric	
	Copper	Bottom Layer	0.04mm		Signal	GBL
	Surface Material	Bottom Solder	0.01mm	Solder Resist	Solder Mask	GBS
		Bottom Overlay			Legend	GBO
Total thickness: 0.61mm						

		UNLESS OTHERWISE SPECIFIED:		NAME	DATE	AUSTINS ARS ELECTRICA		
		DIMENSIONS ARE IN INCHES	DRAWN		6/26/2020	TITLE FAB, HIVE MON, RASP-PI, HAT		
		TOLERANCES:	CHECKED					
		FRACTIONAL±	ENG APPR.					
		ANGULAR: MACH± BEND ±	MFG APPR.					
		TWO PLACE DECIMAL ±	Q.A.					
		THREE PLACE DECIMAL ±	COMMENTS:			SIZE	DWG. NO.	
		INTERPRET GEOMETRIC TOLERANCING PER:					310-000289-001	
		MATERIAL						
NEXT ASSY	USED ON	FINISH				SCALE:	1:1	WEIGHT:
APPLICATION		DO NOT SCALE DRAWING						SHEET 1 OF 1

A

B

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