

HowTo Set Channel Plan by Driver

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Realtek

1. Introduction

This document introduces how to set channel plan by Realtek wifi driver rather than modifies efuse 0xB8[6:0](domain code).

2. Set channel plan

Please chose a suitable Channel Plan Domain Code from document “WS-xxxxxx-Willis-Efuse_Channel_Plan-Rxx.xlsx” before setting your channel plan .The channel plane can be set by modifying module parameter or proc command

Case	Channel Plan Contents					
	Domain Code		EEPROM	Specific Regulatory Domain		
	2G RD	5G RD	0xB8 Bit[6:0]	2G Channels	5G Channels	2G Note
	Old Define		07h~19h	Old Define	Old Define	Old Define
1	2G_WORLD	5G_NULL	20h	1,2,3,4,5,6,7,8,9,10,11,12,13	NA	2G Worldwide 13 Active scan Ch01~11 Passive scan Ch12, 13
2	2G_ETSI1	5G_NULL	21h	1,2,3,4,5,6,7,8,9,10,11,12,13	NA	ETSI Ch01~13
3	2G_FCC1	5G_NULL	22h	1,2,3,4,5,6,7,8,9,10,11	NA	FCC Ch01~11
4	2G_MKK1	5G_NULL	23h	1,2,3,4,5,6,7,8,9,10,11,12,13,14	NA	MKK Ch01~14
5	2G_ETSI2	5G_NULL	24h	10,11,12,13	NA	ETSI Ch10~13
6	2G_FCC1	5G_FCC1	25h	1,2,3,4,5,6,7,8,9,10,11	36,40,44,48, 52,56,60,64, 100,104,108,112,116,120,124,128,132,136,140, 149,153,157,161,165	FCC Ch01~11
7	2G_WORLD	5G_ETSI1	26h	1,2,3,4,5,6,7,8,9,10,11,12,13	36,40,44,48, 52,56,60,64, 100,104,108,112,116,120,124,128,132,136,140	2G Worldwide 13 Active scan Ch01~11 Passive scan Ch12, 13

2.1. Modify module parameter

Please select one method listed below.

- Set `rtw_channel_plan` parameter when `insmod`
Ex: `insmod 8812bu.ko rtw_channel_plan=0x7F`
- Set `rtw_channel_plan` parameter in driver `/os_dep/linux/os_intfs.c`
Ex. `int rtw_channel_plan = 0x7F`
- Set `rtw_channel_plan` parameter in Makefile
Ex. `CONFIG_RTW_CHPLAN = 0x7F`

2.2. Proc command

Channel plan should be set before starting `wpa_supplicant`. Set channel plan for each adapter:

Ex: `echo 0x7F > /proc/net/rtl8812bu/wlan1/chan_plan`

The initial country code and channel plan setting will be replaced.

3. Check channel plan

Check channel plan for each adapter :

Ex: `cat /proc/net/rtl8812bu/wlan1/chan_plan`

Note : `rtl8812bu` and `wlan1` depend on your chip and interface.