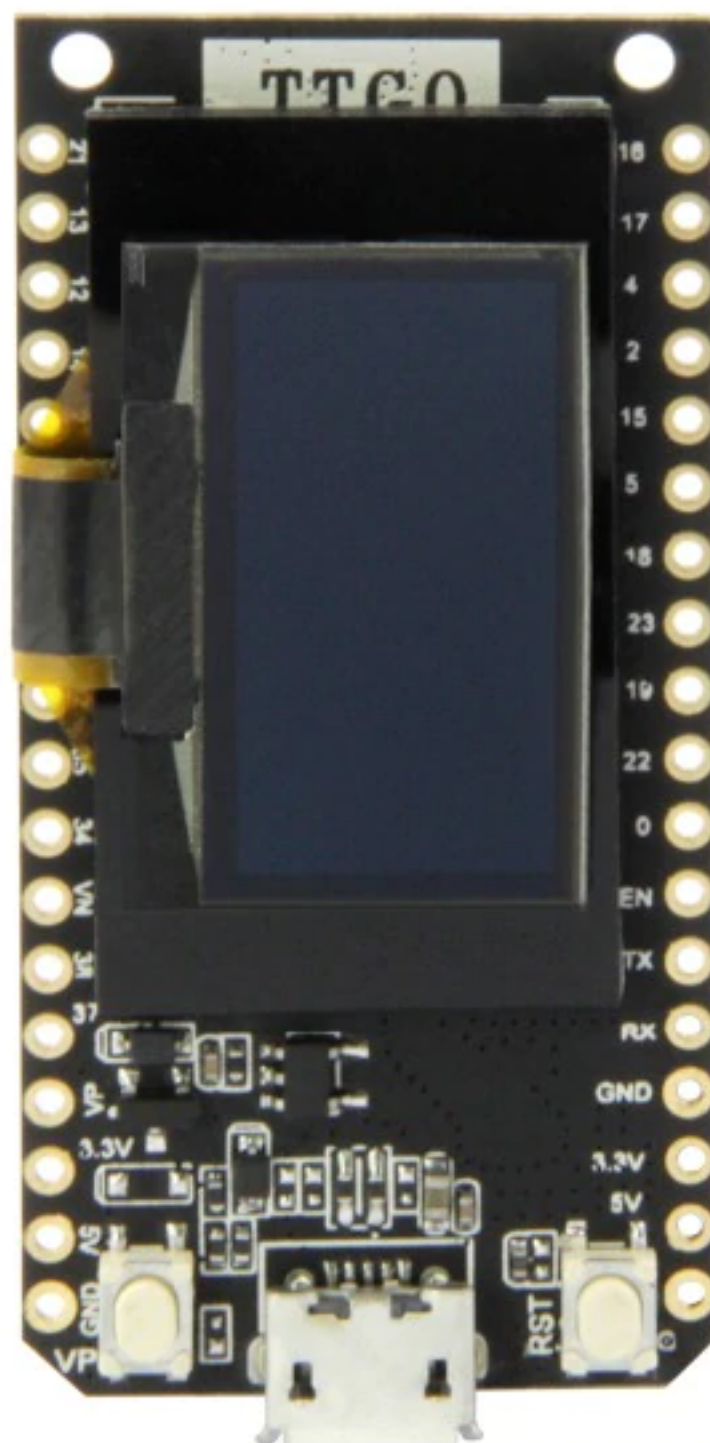


V_SPI_HD	GPIO21	SDA	21
TPUCH4	ADC2_4	GPIO13	13
TPUCH5	ADC2_5	GPIO12	12
TOUCH6	ADC2_6*	GPIO14	14
TOUCH7	ADC2_7*	GPIO27	27
DAC1	ADC2_9*	GPIO26	26
DAC2	ADC2_8*	GPIO25	25
TPUCH8	ADC1_4	GPIO33	33
TPUCH9	ADC1_5	GPIO32	32
	ADC1_7*	GPIO35	35
	ADC1_6*	GPIO34	34
ADC1_3*	GPIO39	S_VN	39
		GPIO38	38
		GPIO37	37
ADC1_0*	GPIO36	S_VP	36

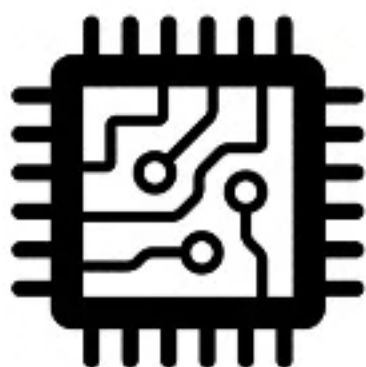
3V3
5V
GND



16	GPIO16		
17	GPIO17		
04	GPIO04	ADC2_0	HSPI_HD TOUCH0
02	GPIO02	ADC2_2	HSPI_WP TOUCH2
15	GPIO15	ADC2_3	HSPI_CS TOUCH3
05	GPIO05	VSPI_SS	
18	GPIO18	VSPI_SCK	
23	GPIO23	VSPI_MOSI	
19	GPIO19	VSPI_MISO	
22	SCL	GPIO22	
0	GPIO00	ADC2_1	TOUCH1
EN			
TXD	CLX3	GPIO01	UO_TXD
RXD	CLX2	GPIO03	UO_RXD
GND			
3V3			
5V			
GND			

LoRa RST is corrected from 23 to 14.  
Schematic diagram shows LoRa RST is 14.  
23 appears to be error in original pinout.

MOSI	27
SCLK	5
CS	18
DIO	26
RST	14
MISO	19



**LORA**  
**SX1276; 868/915**

## Wi-Fi+Bluetooth OLED LILYGO LORA V1.3 BOARD PINMAP

### OLED SSD1306

SDA	4
SCL	15

