Processor architecture

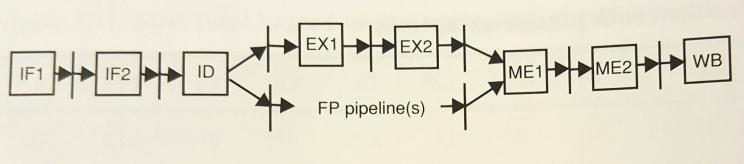


Figure 3.10.
Superpipelined CPU.

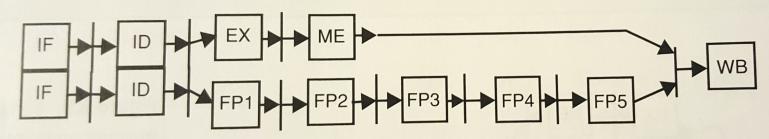


Figure 3.11. Two-way superscalar CPU.