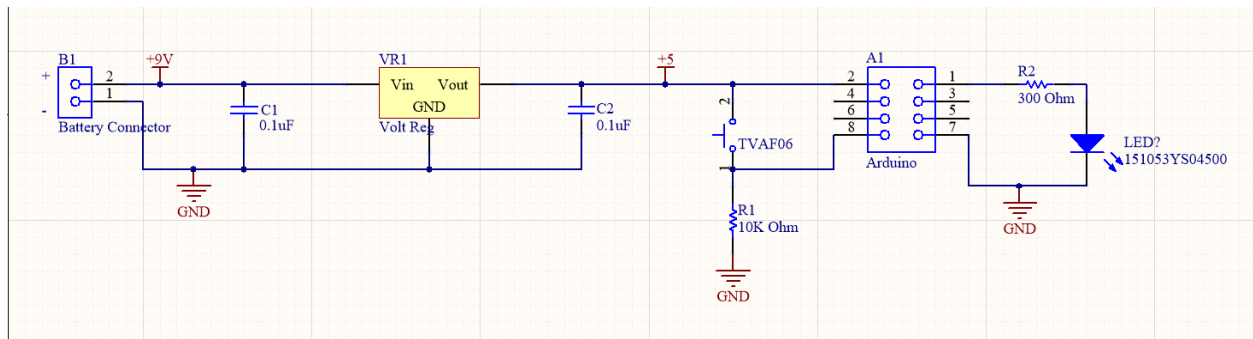


Schematic



PCB Layout

