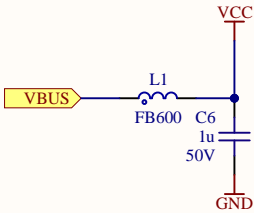


ICY40
FPGA learning platform
Author : Edgar Conzen

Product-Rev. : 3.0.0
PCB-Revision : A02
Date : 16.03.2024

Detail : Subsheet
USB Interface Converter and Power Supply Block
ICY40_A02_USB.SchDoc
Sheet 2 of 3

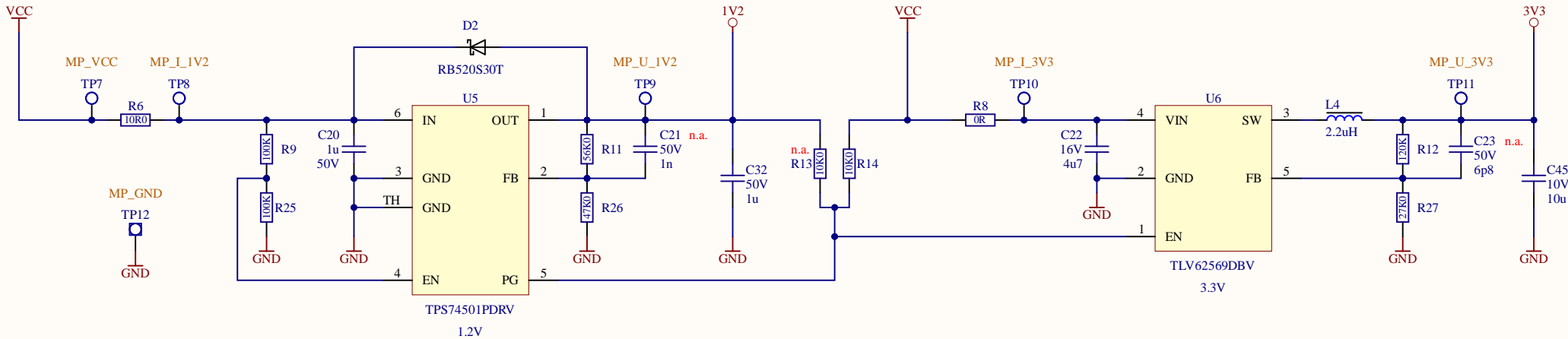


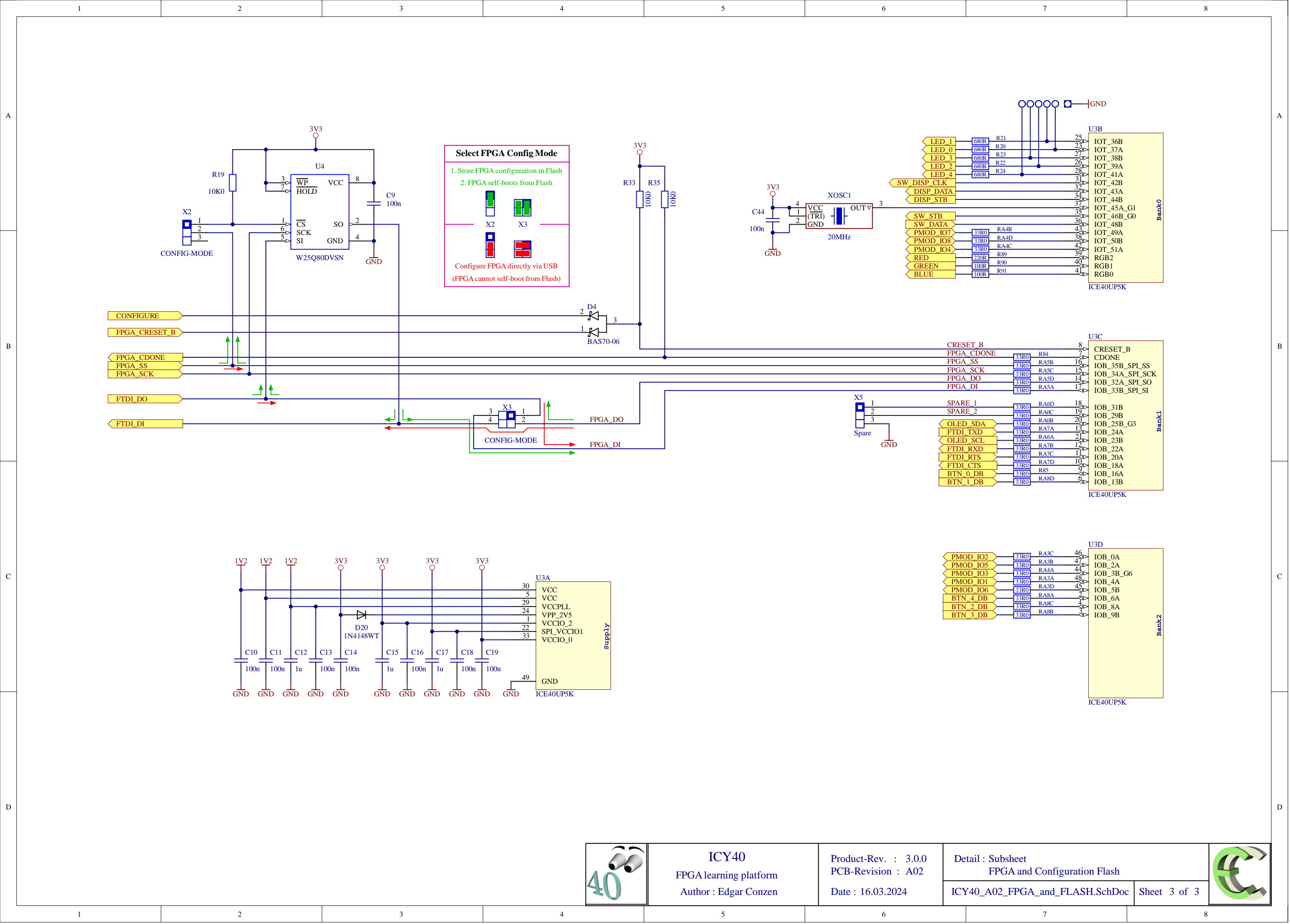


POWER SUPPLY, 1.2V & 3.3V

Power up sequence : 1.2V -> 3.3V

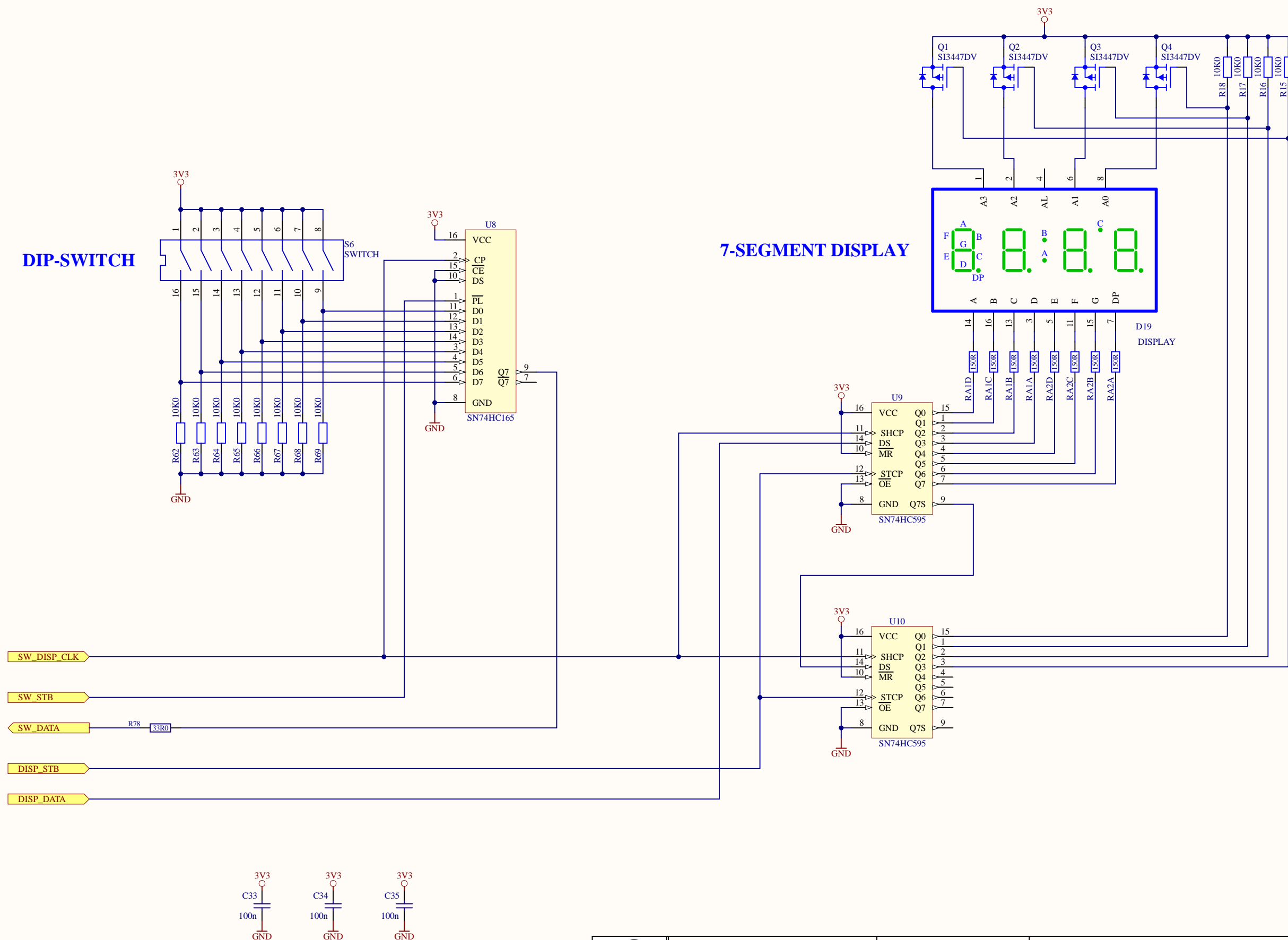
Slew rate : 0.6 .. 10 V/ms
Rise Time 1.2V : min 120us, max 2000us (1800us)
Rise Time 3.3V : min 330us, max 5500us (3800us)





DIP-SWITCH

7-SEGMENT DISPLAY



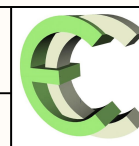
ICY40
FPGA learning platform
Author : Edgar Conzen

Product-Rev. : 3.0.0
PCB-Revision : A02
Date : 16.03.2024

Detail : SWITCH & DISPLAY
Dip-Switch and 7-Segment Display

ICY40_A02_DIPSW-DISPLAY.SchDoc

Sheet 3 of 5



90,00

80,00

