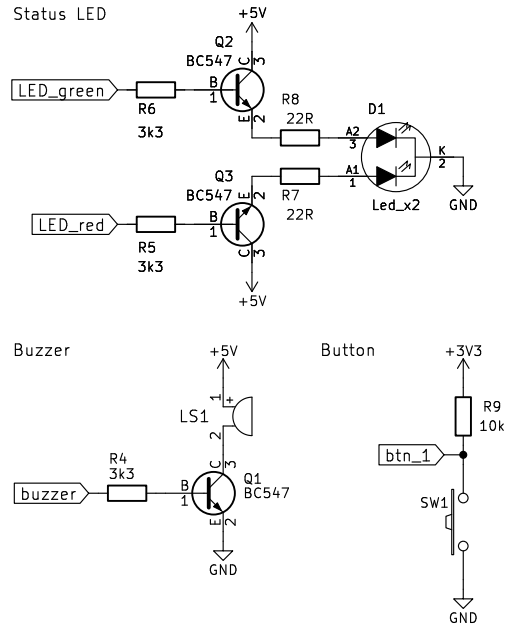
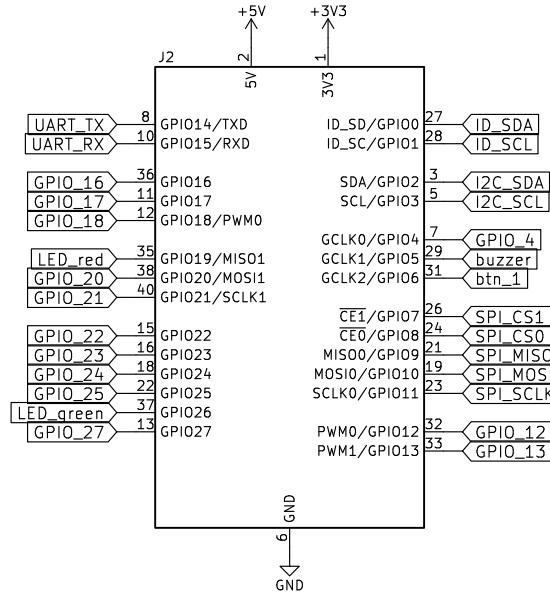


User interface



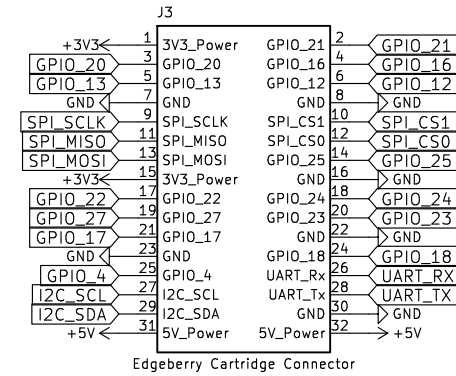
Raspberry Pi GPIO

Compatible with the 40-pin Raspberry Pi GPIO header.
Tested with RPi models 3B, 3B+, 4B, 5B



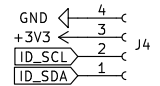
Edgeberry Cartridge connector

Expansion slot for application-specific hardware.



EXP

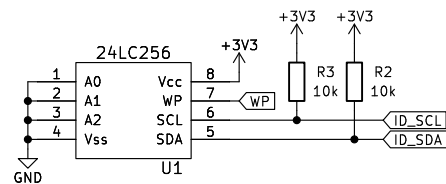
Brings the Raspberry Pi Zero's I2C0 port to the edge of the board



RPi HAT ID EEPROM

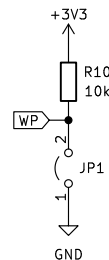
EEPROM I2C address

EEPROM I2C address must be set to 0x50 to be used as a Raspberry Pi 'hat' ID. With the 24LC256 this means pulling all Address pins low.



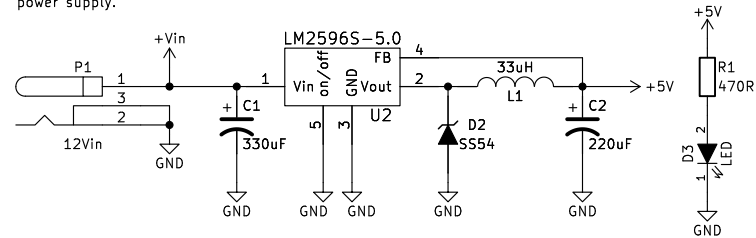
EEPROM Write protection

Only for programming the EEPROM with the 'hat' information the WP pin (Write Protection) must be pulled low by the manufacturer. For normal use, write protection is enabled by pulling the pull-up resistor

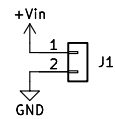


12V – 24V Power to 5V

The LM2596 buck convertor circuit is capable of delivering a 3A output current. Its input could be powered with up to 40VDC, but is optimized for 12VDC, delivered with a standard jack connector power supply.



Vin PWR



The settings file for the Edgeberry Hat EEPROM configuration is included in this repository.

More info on the Raspberry Pi Hat and using the Hat ID EEPROM:

<https://github.com/raspberrypi/hats>

EDGEBERRY ZERO

BASE BOARD Rev. 1.8, early 2025

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Sheet: /

File: Edgeberry-Zero.kicad_sch

Title: Edgeberry Zero

Size: A4

Date: 2024-05-15

KiCad E.D.A. 8.0.9

Rev: 1.8

Id: 1/1