



1. Description

1.1. Project

| | |
|-----------------|--------------------|
| Project Name | ADC Project |
| Board Name | NUCLEO-C031C6 |
| Generated with: | STM32CubeMX 6.14.1 |
| Date | 06/15/2025 |

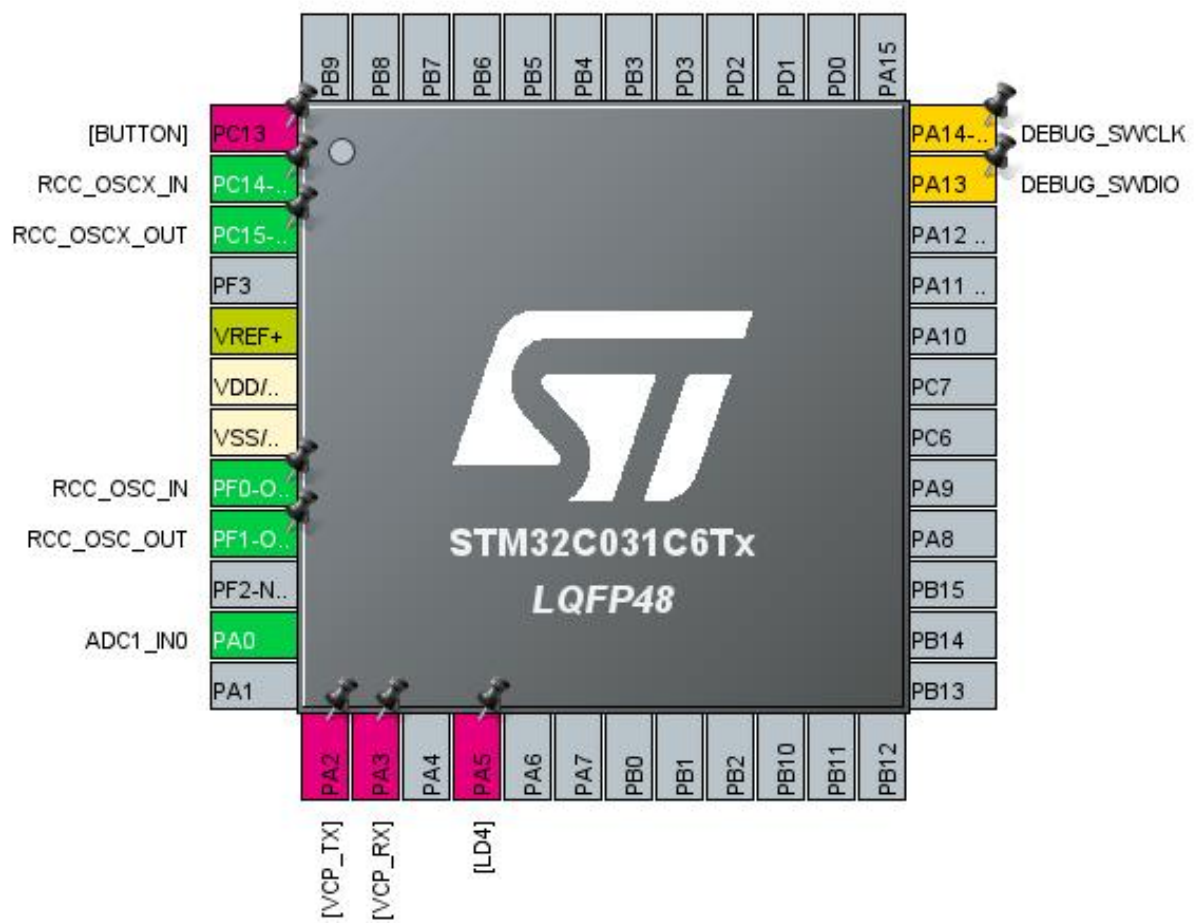
1.2. MCU

| | |
|----------------|---------------|
| MCU Series | STM32C0 |
| MCU Line | STM32C0x1 |
| MCU name | STM32C031C6Tx |
| MCU Package | LQFP48 |
| MCU Pin number | 48 |

1.3. Core(s) information

| | |
|---------|----------------|
| Core(s) | ARM Cortex-M0+ |
|---------|----------------|

2. Pinout Configuration

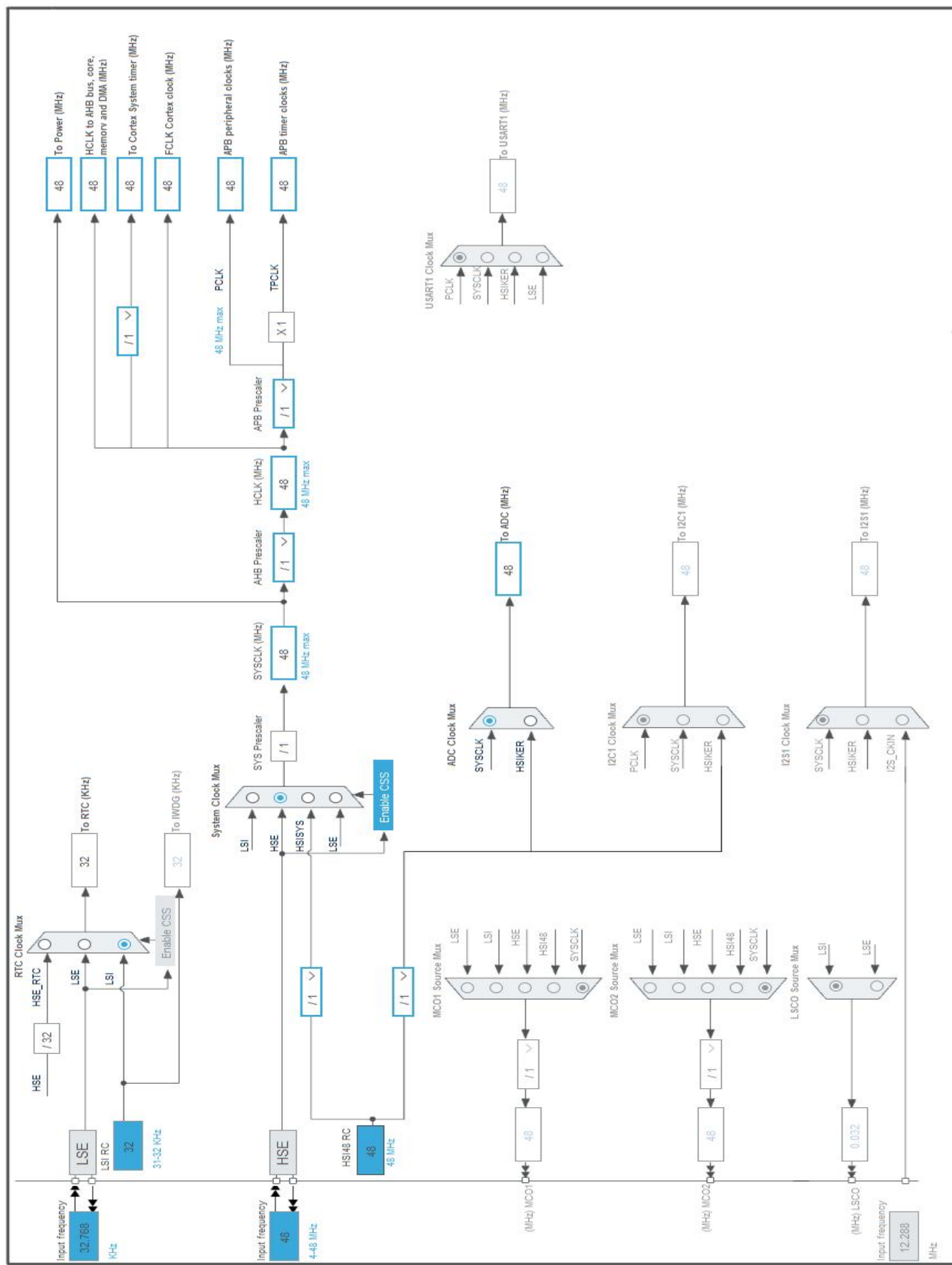


3. Pins Configuration

| Pin Number LQFP48 | Pin Name (function after reset) | Pin Type | Alternate Function(s) | Label |
|----------------------|---------------------------------------|----------|--------------------------|--------------|
| 1 | PC13 | I/O | | |
| 2 | PC14-OSCX_IN (PC14) | I/O | RCC_OSCX_IN | RCC_OSCX_IN |
| 3 | PC15-OSCX_OUT (PC15) | I/O | RCC_OSCX_OUT | RCC_OSCX_OUT |
| 5 | VREF+ | MonolO | | |
| 6 | VDD/VDDA | Power | | |
| 7 | VSS/VSSA | Power | | |
| 8 | PF0-OSC_IN (PF0) | I/O | RCC_OSC_IN | RCC_OSC_IN |
| 9 | PF1-OSC_OUT (PF1) | I/O | RCC_OSC_OUT | RCC_OSC_OUT |
| 11 | PA0 | I/O | ADC1_IN0 | |
| 13 | PA2 | I/O | | |
| 14 | PA3 | I/O | | |
| 16 | PA5 | I/O | | |
| 35 | PA13 * | I/O | DEBUG_SWDIO | DEBUG_SWDIO |
| 36 | PA14-BOOT0 * | I/O | DEBUG_SWCLK | DEBUG_SWCLK |

* The pin is affected with a peripheral function but no peripheral mode is activated

4. Clock Tree Configuration



1. Power Consumption Calculator report

1.1. Microcontroller Selection

| | |
|-----------|---------------|
| Series | STM32C0 |
| Line | STM32C0x1 |
| MCU | STM32C031C6Tx |
| Datasheet | DS00000_Rev0 |

1.2. Parameter Selection

| | |
|-------------|-----|
| Temperature | 25 |
| Vdd | 3.0 |

1.3. Battery Selection

| | |
|-------------------|------------------|
| Battery | Li-SOCL2(AAA700) |
| Capacity | 700.0 mAh |
| Self Discharge | 0.08 %/month |
| Nominal Voltage | 3.6 V |
| Max Cont Current | 10.0 mA |
| Max Pulse Current | 30.0 mA |
| Cells in series | 1 |
| Cells in parallel | 1 |

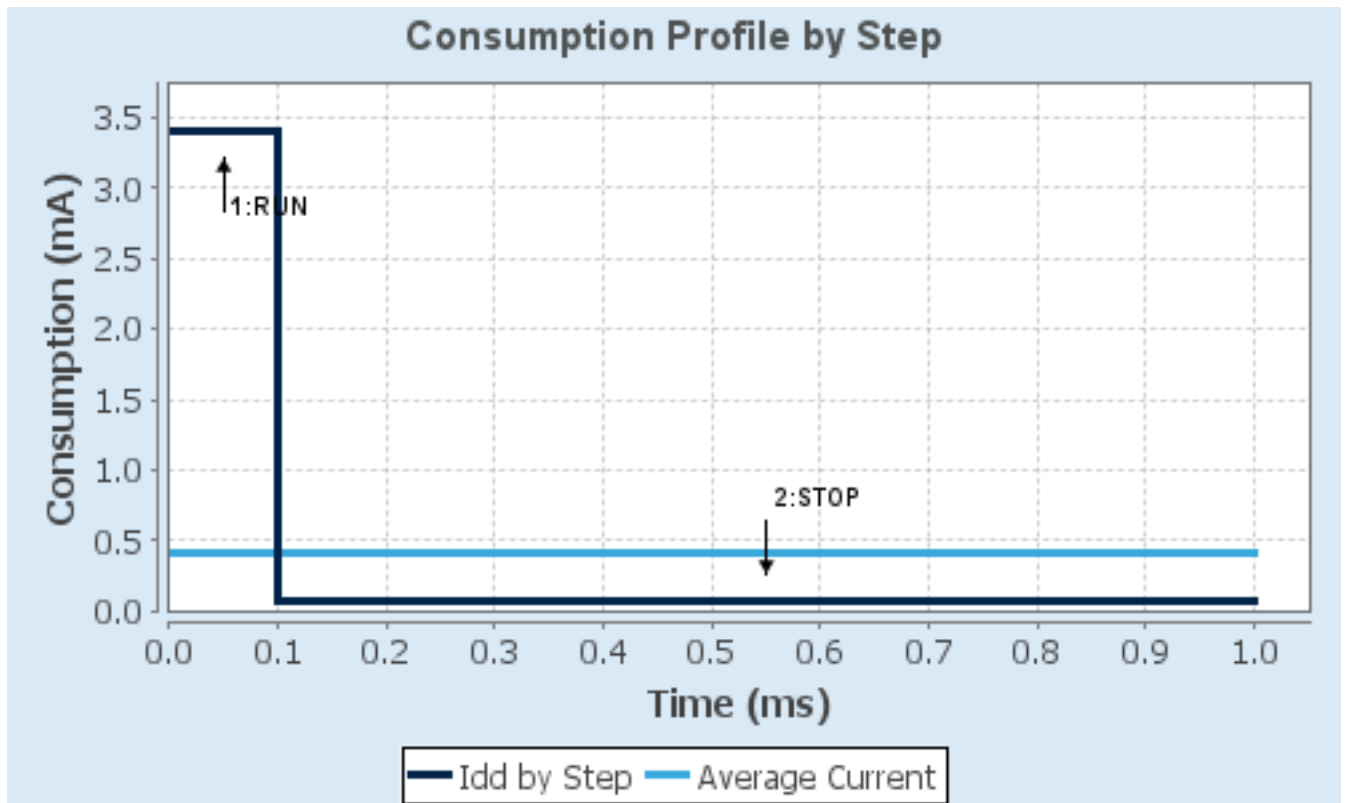
1.4. Sequence

| | | |
|-------------------------------|----------------|----------------------|
| Step | Step1 | Step2 |
| Mode | RUN | STOP |
| Vdd | 3.0 | 3.0 |
| Voltage Source | Battery | Battery |
| Range | NaN/SMPS | NaN/SMPS |
| Fetch Type | FLASH/PREFETCH | Flash-PowerDownSleep |
| CPU Frequency | 48 MHz | 0 Hz |
| Clock Configuration | HSE | ALL CLOCKS OFF |
| Clock Source Frequency | 48 MHz | 0 Hz |
| Peripherals | | |
| Additional Cons. | 0 mA | 0 mA |
| Average Current | 3.4 mA | 73.5 μ A |
| Duration | 0.1 ms | 0.9 ms |
| DMIPS | 60.0 | 0.0 |
| Ta Max | 104.53 | 104.99 |
| Category | In DS Table | In DS Table |

1.5. Results

| | | | |
|---------------|-----------------------------|-----------------|----------------|
| Sequence Time | 1 ms | Average Current | 406.15 μ A |
| Battery Life | 2 months, 10 days, 20 hours | Average DMIPS | 60.0 DMIPS |

1.6. Chart



2. Software Project

2.1. Project Settings

| Name | Value |
|-----------------------------------|---|
| Project Name | ADC Project |
| Project Folder | C:\Users\requiem07\Downloads\Quantum Leaps\Test Project\ADC Project |
| Toolchain / IDE | MDK-ARM V5.39 |
| Firmware Package Name and Version | STM32Cube FW_C0 V1.4.0 |
| Application Structure | Basic |
| Generate Under Root | No |
| Do not generate the main() | No |
| Minimum Heap Size | 0x200 |
| Minimum Stack Size | 0x400 |

2.2. Code Generation Settings

| Name | Value |
|---|---|
| STM32Cube MCU packages and embedded software | Copy all used libraries into the project folder |
| Generate peripheral initialization as a pair of '.c/.h' files | No |
| Backup previously generated files when re-generating | No |
| Keep User Code when re-generating | Yes |
| Delete previously generated files when not re-generated | Yes |
| Set all free pins as analog (to optimize the power consumption) | No |
| Enable Full Assert | No |

2.3. Advanced Settings - Generated Function Calls

| Rank | Function Name | Peripheral Instance Name |
|------|--------------------|--------------------------|
| 1 | MX_GPIO_Init | GPIO |
| 2 | MX_DMA_Init | DMA |
| 3 | MX_ADC1_Init | ADC1 |
| 4 | SystemClock_Config | RCC |
| 5 | MX_RTC_Init | RTC |

3. Peripherals and Middlewares Configuration

3.1. ADC1

mode: IN0

3.1.1. Parameter Settings:

ADC_Settings:

Clock Prescaler

Resolution

Data Alignment

Sequencer

Scan Conversion Mode

Continuous Conversion Mode

Discontinuous Conversion Mode

DMA Continuous Requests

End Of Conversion Selection

Overrun behaviour

Low Power Auto Wait

Auto Off

Oversampling Mode

Asynchronous clock mode divided by 32 *

ADC 12-bit resolution

Right alignment

Sequencer set to not fully configurable

Forward

Disabled

Disabled

Disabled

End of single conversion

Overrun data preserved

Disabled

Disabled

Disabled

ADC_Regular_ConversionMode:

SamplingTime Common 1

External Trigger Conversion Source

External Trigger Conversion Edge

Trigger Frequency

79.5 Cycles *

Regular Conversion launched by software

None

High frequency

Analog Watchdog 1:

Enable Analog WatchDog1 Mode

false

Analog Watchdog 2:

1st Channel

None

Analog Watchdog 3:

1st Channel

None

3.2. NUCLEO-C031C6

mode: Human Machine Interface

3.2.1. Human Machine Interface:

Led:

USER LED GREEN (LD4)

true *

Button:

USER BUTTON

Mode EXTI *

VCOM:

Virtual Com Port

true *

Demonstration code:

Generate demonstration code

Disabled

3.3. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

Low Speed Clock (LSE) : Crystal/Ceramic Resonator

3.3.1. Parameter Settings:

System Parameters:

| | |
|-------------------|--------------------|
| VDD voltage (V) | 3.3 |
| Instruction Cache | Enabled |
| Prefetch Buffer | Disabled |
| Data Cache | Enabled |
| Flash Latency(WS) | 1 WS (2 CPU cycle) |

RCC Parameters:

| | |
|--------------------------------|------|
| HSI Calibration Value | 64 |
| HSE Startup Timeout Value (ms) | 100 |
| LSE Startup Timeout Value (ms) | 5000 |

3.4. RTC

mode: Activate Clock Source

mode: Activate Calendar

Alarm A: Internal Alarm A

3.4.1. Parameter Settings:

General:

| | |
|-------------------------------|---------------|
| Hour Format | Hourformat 24 |
| Asynchronous Predivider value | 127 |
| Synchronous Predivider value | 255 |

Calendar Time:

| | |
|-------------|-----------------|
| Data Format | BCD data format |
| Hours | 0 |

| | |
|--|----------------------|
| Minutes | 0 |
| Seconds | 0 |
| SubSeconds | 0 |
| Day Light Saving: value of hour adjustment | Daylightsaving None |
| Store Operation | Storeoperation Reset |

Calendar Date:

| | |
|----------|---------|
| Week Day | Monday |
| Month | January |
| Date | 1 |
| Year | 0 |

Alarm A:

| | |
|--------------------------|---------------------------------|
| Hours | 0 |
| Minutes | 0 |
| Seconds | 1 * |
| Sub Seconds | 0 |
| Alarm Mask Date Week day | Disable |
| Alarm Mask Hours | Disable |
| Alarm Mask Minutes | Disable |
| Alarm Mask Seconds | Disable |
| Alarm Sub Second Mask | All Alarm SS fields are masked. |
| Alarm Date Week Day Sel | Date |
| Alarm Date | 1 |

3.5. SYS

Timebase Source: SysTick

*** User modified value**

4. System Configuration

4.1. GPIO configuration

| IP | Pin | Signal | GPIO mode | GPIO pull/up pull down | Max Speed | User Label |
|-----------------------|----------------------|--------------|-------------|-----------------------------|-----------|--------------|
| ADC1 | PA0 | ADC1_IN0 | Analog mode | No pull-up and no pull-down | n/a | |
| RCC | PC14-OSCX_IN (PC14) | RCC_OSCX_IN | n/a | n/a | n/a | RCC_OSCX_IN |
| | PC15-OSCX_OUT (PC15) | RCC_OSCX_OUT | n/a | n/a | n/a | RCC_OSCX_OUT |
| | PF0-OSC_IN (PF0) | RCC_OSC_IN | n/a | n/a | n/a | RCC_OSC_IN |
| | PF1-OSC_OUT (PF1) | RCC_OSC_OUT | n/a | n/a | n/a | RCC_OSC_OUT |
| Single Mapped Signals | PA13 | DEBUG_SWDIO | n/a | n/a | n/a | DEBUG_SWDIO |
| | PA14-BOOT0 | DEBUG_SWCLK | n/a | n/a | n/a | DEBUG_SWCLK |

4.2. DMA configuration

| DMA request | Stream | Direction | Priority |
|-------------|---------------|----------------------|----------|
| ADC1 | DMA1_Channel1 | Peripheral To Memory | Low |

ADC1: DMA1_Channel1 DMA request Settings:

Mode: Normal
Peripheral Increment: Disable
Memory Increment: **Enable ***
Peripheral Data Width: Half Word
Memory Data Width: Half Word

4.3. NVIC configuration

4.3.1. NVIC

| Interrupt Table | Enable | Preenmption Priority | SubPriority |
|---|--------|----------------------|-------------|
| Non maskable interrupt | true | 0 | 0 |
| Hard fault interrupt | true | 0 | 0 |
| System service call via SWI instruction | true | 0 | 0 |
| Pendable request for system service | true | 0 | 0 |
| System tick timer | true | 3 | 0 |
| RTC interrupts through EXTI lines 19 and 21 | true | 0 | 0 |
| EXTI line 4 to 15 interrupts | true | 0 | 0 |
| DMA1 channel 1 interrupt | true | 0 | 0 |
| ADC1 interrupt | true | 0 | 0 |
| Flash global interrupt | unused | | |
| RCC global interrupt | unused | | |

4.3.2. NVIC Code generation

| Enabled interrupt Table | Select for init sequence ordering | Generate IRQ handler | Call HAL handler |
|---|-----------------------------------|----------------------|------------------|
| Non maskable interrupt | false | true | false |
| Hard fault interrupt | false | true | false |
| System service call via SWI instruction | false | true | false |
| Pendable request for system service | false | true | false |
| System tick timer | false | true | true |
| RTC interrupts through EXTI lines 19 and 21 | false | true | true |
| EXTI line 4 to 15 interrupts | false | true | true |
| DMA1 channel 1 interrupt | false | true | true |
| ADC1 interrupt | false | true | true |

* User modified value

5. System Views

5.1. Category view

5.1.1. Current

Middlewares

| System Core | Analog | Timers | Connectivity | Multimedia | Computing | Trace and Debug | Power and Thermal | Bsp |
|--------------|--------|--------|--------------|------------|-----------|-----------------|-------------------|------------------|
| CORTEX_M0+ ✓ | ADC1 ✓ | RTC ✓ | | | | | | NUCLEO-C031... ✓ |
| DMA ✓ | | | | | | | | |
| GPIO ⚠ | | | | | | | | |
| IIVIC ✓ | | | | | | | | |
| RCC ✓ | | | | | | | | |
| SYS ✓ | | | | | | | | |

6. Docs & Resources

| Type | Link |
|----------------------------|---|
| IBIS models | https://www.st.com/resource/en/ibis_model/stm32c0-ibis.zip |
| System View Description | https://www.st.com/resource/en/svd/stm32c0-svd.zip |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_embedded_software_solutions.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32_eval-tools_portfolio.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-stm8_software_development_tools.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/microcontrollers-stm32c0-series-overview.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-family-overview.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/microcontrollers-stm32-entry-level-graphics.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-graphics-solution-overview.pdf |
| Presentations | https://www.st.com/resource/en/product_presentation/stm32-graphics-solutions-detailed.pdf |
| Brochures | https://www.st.com/resource/en/brochure/products-and-solutions-for-plcs-and-smart-i-os.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32nucleo.pdf |
| Flyers | https://www.st.com/resource/en/flyer/fldpstpf11120.pdf |
| Flyers | https://www.st.com/resource/en/flyer/flstm32c0.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an1709-emc-design-guide-for-stm8-stm32-and-legacy-mcus-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an2606-stm32-microcontroller-system-memory-boot-mode-stmicroelectronics.pdf |
| Application Notes | https://www.st.com/resource/en/application_note/an3126-audio-and- |

waveform-generation-using-the-dac-in-stm32-products-
stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3155-usart-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an3156-usb-dfu-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4655-virtually-increasing-the-number-of-serial-communication-peripherals-in-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4750-handling-of-soft-errors-in-stm32-applications-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4776-generalpurpose-timer-cookbook-for-stm32-microcontrollers-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4803-highspeed-si-simulations-using-ibis-and-boardlevel-simulations-using-hyperlynx-si-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4989-stm32-microcontroller-debug-toolbox-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5027-interfacing-pdm-digital-microphones-using-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4899-stm32-microcontroller-gpio-hardware-settings-and-lowpower-consumption-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5612-esd-protection-of-stm32-mcus-and-mpus-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an4991-how-to-wake-up-an-stm32-microcontroller-from-lowpower-mode-with-the-usart-or-the-lpuart-stmicroelectronics.pdf

Application Notes https://www.st.com/resource/en/application_note/an5780-migration-of-applications-from-atmega328-family-to-stm32c0-series-microcontrollers-stmicroelectronics.pdf

- Application Notes https://www.st.com/resource/en/application_note/an5857-using-xcuberccalib-software-to-calibrate-stm32c0-series-internal-rc-oscillator-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4838-introduction-to-memory-protection-unit-management-on-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5225-introduction-to-usb-typec-power-delivery-for-stm32-mcus-and-mpus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4894-how-to-use-eeeprom-emulation-on-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5036-guidelines-for-thermal-management-on-stm32-applications-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an2548-introduction-to-dma-controller-for-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4013-introduction-to-timers-for-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4277-how-to-use-pwm-shutdown-for-motor-control-and-digital-power-conversion-on-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4759-introduction-to-using-the-hardware-realtime-clock-rtc-and-the-tamper-management-unit-tamp-with-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an4908-getting-started-with-uart-automatic-baud-rate-detection-for-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5156-introduction-to-security-for-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5224-introduction-to-dmamax-for-stm32-mcus-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application_note/an5543-guidelines-for-enhanced-spi-communication-on-stm32-mcus-and-mpus-stmicroelectronics.pdf

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| Datasheet | https://www.st.com/resource/en/datasheet/dm00836012.pdf |
| Programming Manuals | https://www.st.com/resource/en/programming_manual/pm0223-stm32-cortexm0-mcus-programming-manual-stmicroelectronics.pdf |
| Reference Manuals | https://www.st.com/resource/en/reference_manual/rm0490-stm32c0-series-advanced-armbased-32bit-mcus-stmicroelectronics.pdf |
| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1163-description-of-wlcsp-for-microcontrollers-and-recommendations-for-its-use-stmicroelectronics.pdf |
| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1204-tape-and-reel-shipping-media-for-stm32-microcontrollers-in-bga-packages-stmicroelectronics.pdf |
| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1205-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-fpn-packages-stmicroelectronics.pdf |
| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1206-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-qfp-packages-stmicroelectronics.pdf |
| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1207-tape-and-reel-shipping-media-for-stm8-and-stm32-microcontrollers-in-so-packages-stmicroelectronics.pdf |
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| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1433-reference-device-marking-schematics-for-stm32-microcontrollers-and-microprocessors-stmicroelectronics.pdf |
| Technical Notes & Articles | https://www.st.com/resource/en/technical_note/tn1489-security-bulletin-tn1489stpsirt-physical-attacks-on-stm32-and-stm32cube-firmware-stmicroelectronics.pdf |

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