## HW4 (due 20 April before the recitation)

- 1- Design a UTM using a RATM that has the capacity to simulate any other RATM as its input. Describe your UTM in detail, namely the contents and structure of all its registers and its external tape.
- **2-** *Questions from main text 5.4.1, 5.4.2, 5.4.3*