

89BSD Calculation Method

FUNCTIONAL DESCRIPTION

GENERAL

The 89BSD consists of a piezoresistive sensor and a sensor interface IC. The main function of the sensor interface IC is to convert the uncompensated analogue output voltage from the piezoresistive pressure sensor to a 24-bit digital value, as well as providing a 24-bit digital value for the temperature of the sensor.

FACTORY CALIBRATION

Every module is individually factory calibrated at seven points of various temperature and pressures. As a result, 10 coefficients necessary to compensate for process variations and temperature variations are calculated and stored in the 128-bit PROM of each module. These bits partitioned into 7 pressure coefficients (C0 to C6) and 3 temperature (A0 to A2) which must be read by the microcontroller software and used in the program converting D1 and D2 into compensated pressure and temperature values.

I²C MODE

The external microcontroller clocks in the data through the input SCLK (Serial CLock) and SDA (Serial DAta). The sensor responds on the same pin SDA which is bidirectional for the I²C bus interface. So this interface type uses only 2 signal lines and does not require a chip select, which can be favorable to reduce board space. In I²C-Mode the complement of the pin CSB (Chip Select) represents the LSB of the I²C address.

	Address (7 bits)
CSB PIN	0x77 (1110111 b)

COMMANDS

The 89BSD has only five basic commands:

1. Reset
2. Read PROM (128 bit of calibration words)
3. D1 conversion
4. D2 conversion
5. Read ADC result (24 bit pressure / temperature)

Size of each command is 1 byte (8 bits) as described in the table below. After ADC read commands the device will return 24 bit result and after the PROM read 16bit result. The address of the PROM is embedded inside of the PROM read command using the a2, a1 and a0 bits.

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	Command byte								hex value
Bit number	0	1	2	3	4	5	6	7	
Bit name	PR M	COV	-	Typ	Ad2/ Os2	Ad1/ Os1	Ad0/ Os0	Stop	
Command									
Reset	0	0	0	1	1	1	1	0	0x1E
Convert D1 (OSR=256)	0	1	0	0	0	0	0	0	0x40
Convert D1 (OSR=512)	0	1	0	0	0	0	1	0	0x42
Convert D1 (OSR=1024)	0	1	0	0	0	1	0	0	0x44
Convert D1 (OSR=2048)	0	1	0	0	0	1	1	0	0x46
Convert D1 (OSR=4096)	0	1	0	0	1	0	0	0	0x48
Convert D2 (OSR=256)	0	1	0	1	0	0	0	0	0x50
Convert D2 (OSR=512)	0	1	0	1	0	0	1	0	0x52
Convert D2 (OSR=1024)	0	1	0	1	0	1	0	0	0x54
Convert D2 (OSR=2048)	0	1	0	1	0	1	1	0	0x56
Convert D2 (OSR=4096)	0	1	0	1	1	0	0	0	0x58
ADC Read	0	0	0	0	0	0	0	0	0x00
PROM Read	1	0	1	0	Ad2	Ad1	Ad0	0	0xA0 to 0xAE

Figure 1: Command structure

CONVERSION SEQUENCE

The conversion command is used to initiate uncompensated pressure (D1) or uncompensated temperature (D2) conversion. The chip select can be disabled during this time to communicate with other devices.

After the conversion, using ADC read command the result is clocked out with the MSB first. If the conversion is not executed before the ADC read command, or the ADC read command is repeated, it will give 0 as the output result. If the ADC read command is sent during conversion the result will be 0, the conversion will not stop and the final result will be wrong. Conversion sequence sent during the already started conversion process will yield incorrect result as well.

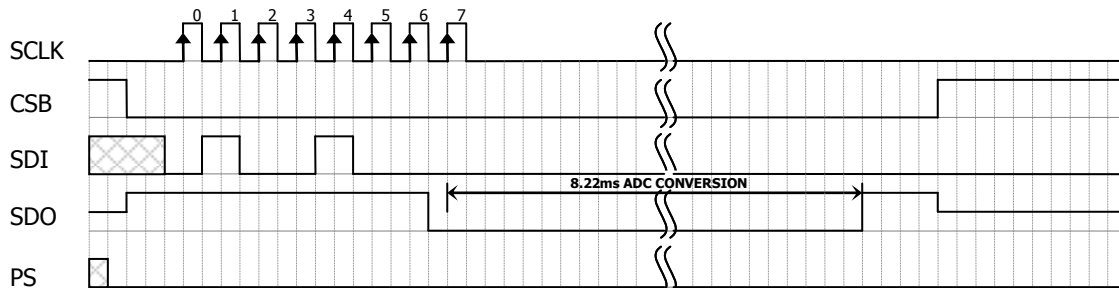


Figure 2: Conversion out sequence, Typ=d1, OSR = 4096

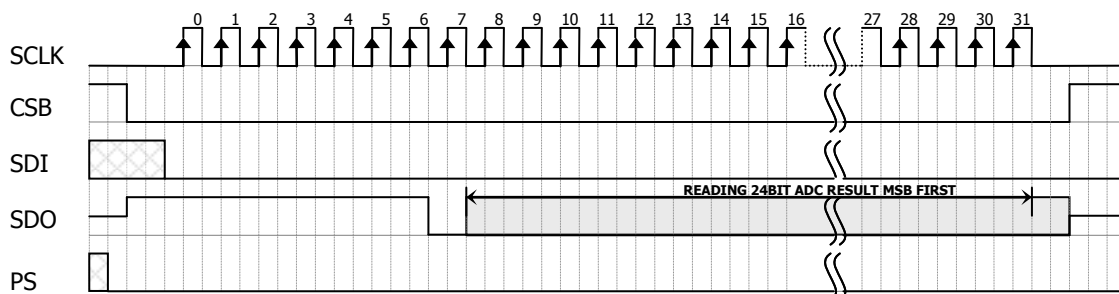


Figure 3: ADC Read sequence

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PROM READ SEQUENCE

The read command for PROM shall be executed once after reset by the user to read the content of the calibration PROM and to calculate the calibration coefficients. There are in total 8 addresses resulting in a total memory of 128 bit. Address 0 contains factory data and the setup, addresses 1-6 calibration coefficients and address 7 contains the serial code and CRC. The command sequence is 8 bits long with a 16 bit result which is clocked with the MSB first.

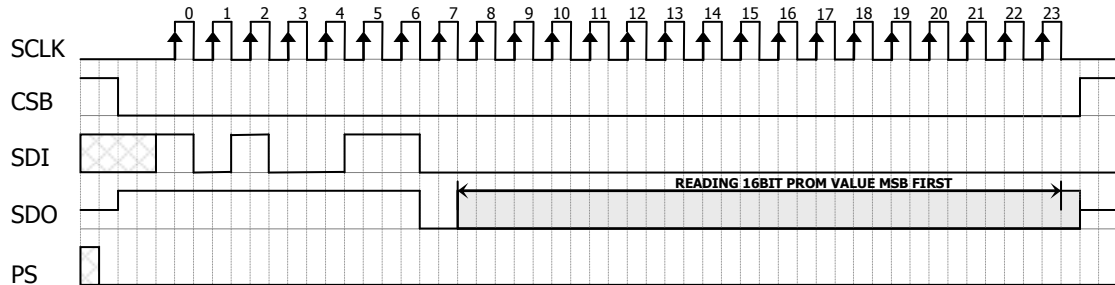


Figure 4: PROM Read sequence, address = 011 (Coefficient 3).

I²C INTERFACE

COMMANDS

Each I²C communication message starts with the start condition and it is ended with the stop condition. The 89BSD address is 111011Cx, where C is the complementary value of the pin CSB.

RESET SEQUENCE

The reset can be sent at any time. In the event that there is not a successful power on reset this may be caused by the SDA being blocked by the module in the acknowledge state. The only way to get the 89BSD to function is to send several SCLKs followed by a reset sequence or to repeat power on reset.

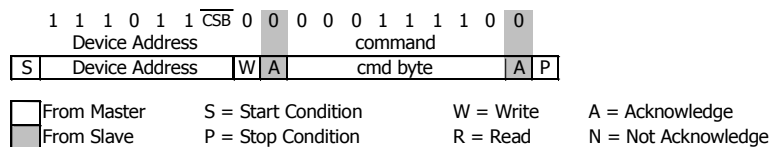


Figure 5: I²C Reset Command

CONVERSION SEQUENCE

A conversion can be started by sending the command to 89BSD. When command is sent to the system it stays busy until conversion is done. When conversion is finished the data can be accessed by sending a Read command, when an acknowledge appears from the 89BSD 24 SCLK cycles may be sent to receive all result bits. Every 8 bit the system waits for an acknowledge signal.

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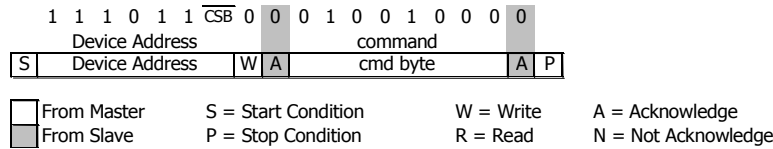


Figure 6: I²C Command to initiate a pressure conversion (OSR=4096, typ=D1)

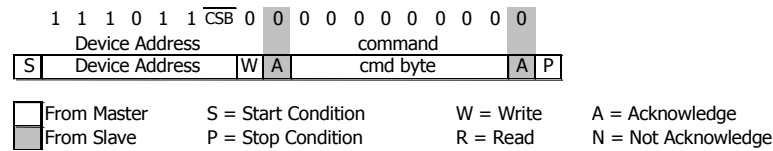


Figure 7: I²C ADC read sequence

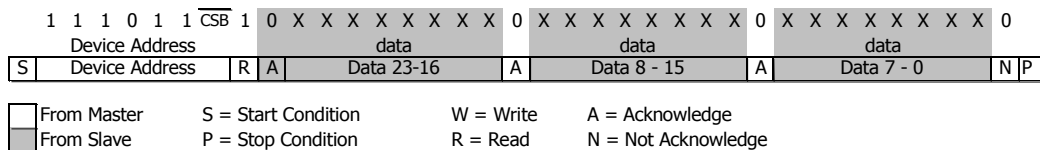


Figure 8: I²C pressure response (D1) on 24 bit from 89BSD

PROM READ SEQUENCE

The PROM Read command consists of two parts. First command sets up the system into PROM read mode. The second part gets the data from the system.

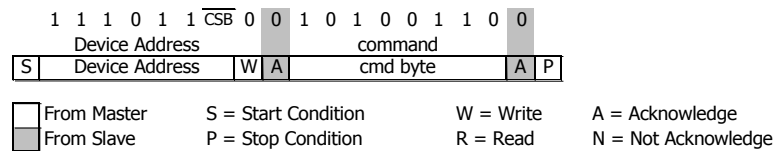


Figure 9: I²C Command to read memory address= 011 (Coefficient 3)

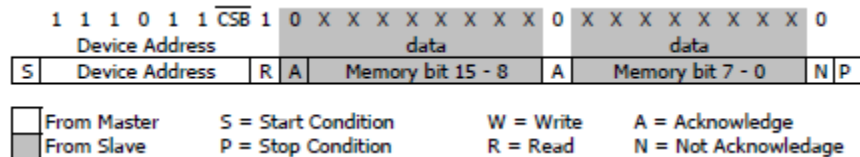


Figure 10: I²C answer from 89BSD

CYCLIC REDUNDANCY CHECK (CRC)

89BSD contains a PROM memory with 128-Bit. A 4-bit CRC has been implemented to check the data validity in memory. The application note AN520 describes in detail CRC-4 code used.

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PRESSURE AND TEMPERATURE CALCULATION

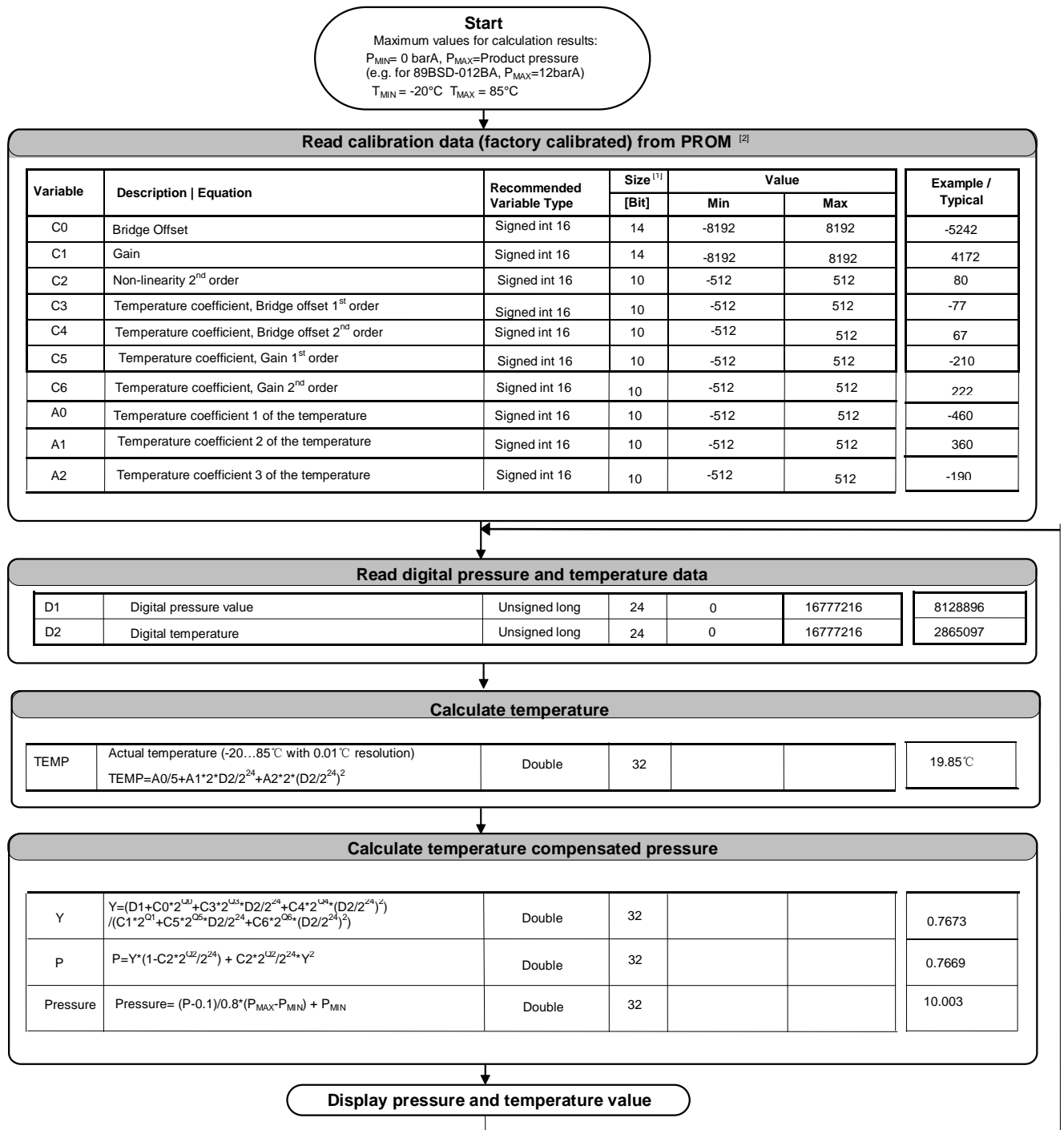


Figure 11: Flow chart for pressure and temperature reading and software compensation.

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MEMORY MAPPING ^[2]

Memory mapping																
Address	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
1	C0 ₁₃	C0 ₁₂	C0 ₁₁	C0 ₁₀	C0 ₀₉	C0 ₀₈	C0 ₀₇	C0 ₀₆	C0 ₀₅	C0 ₀₄	C0 ₀₃	C0 ₀₂	C0 ₀₁	C0 ₀₀	C1 ₁₃	C1 ₁₂
2	C1 ₁₁	C1 ₁₀	C1 ₀₉	C1 ₀₈	C1 ₀₇	C1 ₀₆	C1 ₀₅	C1 ₀₄	C1 ₀₃	C1 ₀₂	C1 ₀₁	C1 ₀₀	C2 ₀₉	C2 ₀₈	C2 ₀₇	C2 ₀₆
3	C2 ₀₅	C2 ₀₄	C2 ₀₃	C2 ₀₂	C2 ₀₁	C2 ₀₀	C3 ₀₉	C3 ₀₈	C3 ₀₇	C3 ₀₆	C3 ₀₅	C3 ₀₄	C3 ₀₃	C3 ₀₂	C3 ₀₁	C3 ₀₀
4	C4 ₀₉	C4 ₀₈	C4 ₀₇	C4 ₀₆	C4 ₀₅	C4 ₀₄	C4 ₀₃	C4 ₀₂	C4 ₀₁	C4 ₀₀	C5 ₀₉	C5 ₀₈	C5 ₀₇	C5 ₀₆	C5 ₀₅	C5 ₀₄
5	C5 ₀₃	C5 ₀₂	C5 ₀₁	C5 ₀₀	C6 ₀₉	C6 ₀₈	C6 ₀₇	C6 ₀₆	C6 ₀₅	C6 ₀₄	C6 ₀₃	C6 ₀₂	C6 ₀₁	C6 ₀₀	A0 ₀₉	A0 ₀₈
6	A0 ₀₇	A0 ₀₆	A0 ₀₅	A0 ₀₄	A0 ₀₃	A0 ₀₂	A0 ₀₁	A0 ₀₀	A1 ₀₉	A1 ₀₈	A1 ₀₇	A1 ₀₆	A1 ₀₅	A1 ₀₄	A1 ₀₃	A1 ₀₂
7	A1 ₀₁	A1 ₀₀	A2 ₀₉	A2 ₀₈	A2 ₀₇	A2 ₀₆	A2 ₀₅	A2 ₀₄	A2 ₀₃	A2 ₀₂	A2 ₀₁	A2 ₀₀	CRC			

Figure 12: Memory mapping.

Q factor

Q factor for temperature Compensated pressure calculation	
Q0	9
Q1	11
Q2	9
Q3	15
Q4	15
Q5	16
Q6	16

Notes

[1] Maximal size of intermediate result during evaluation of variable

[2] All coefficients are 2's complement format

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APPLICATION CIRCUIT

The 89BSD is a circuit that is to be used in conjunction with a microcontroller and a 3V DC supply.

I²C protocol communication

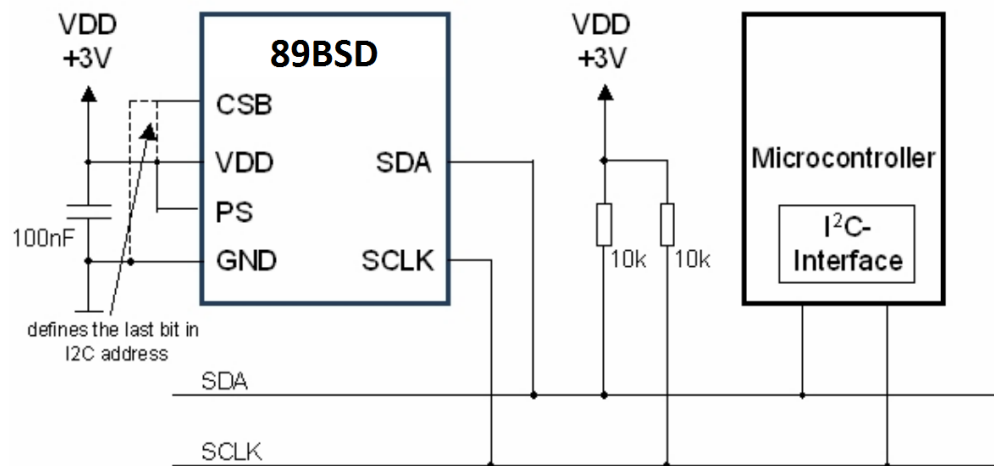


Figure 13: Typical application circuit for I²C protocol communication