

CE – 65v2 (ER1)

Type: Modified w/ Gap

Pitch: 15µm

 $V_{\text{sub}} = V_{\text{pwell}} = 0$, T = 20°C

 $I_{mat} = 3mA$, $I_{col} = 200\mu A$, $V_{offset} = 1V$

AC amp.: $I_{pmos} = 100\mu A$, HV = 10V

Method: Window

Threshold: 170 e