

PIN 6.7 & 8.9.10 MUST BE CONNECTED ON PCB

## **ELECTRICAL CHARATERISTICS:**

1. DCR : PIN  $1-2 = 48m\Omega$  MAX PIN  $2-3 = 56m\Omega$  MAX PIN  $4-5 = 264m\Omega$  MAX PIN  $6.7-9.10 = 18m\Omega$  MAX

2. INDUCTANCE : (@10kHz, 1.0Vrms) : PIN  $1-3 = 35uH \pm 10\%$ 

3. LEAKAGE INDUCTANCE (@100kHz, 1.0Vrms) PIN 1-3 (PIN 4-5, 6.7-9.10 SHORT) = 1.8uH MAX

4. TURN RATIO : (@100kHz, 0.1Vrms)

PIN 1-3 :  $4-5 = 1.19 \pm 2\%$ PIN 1-3 :  $9.10-6.7 = 2.71 \pm 2\%$ 

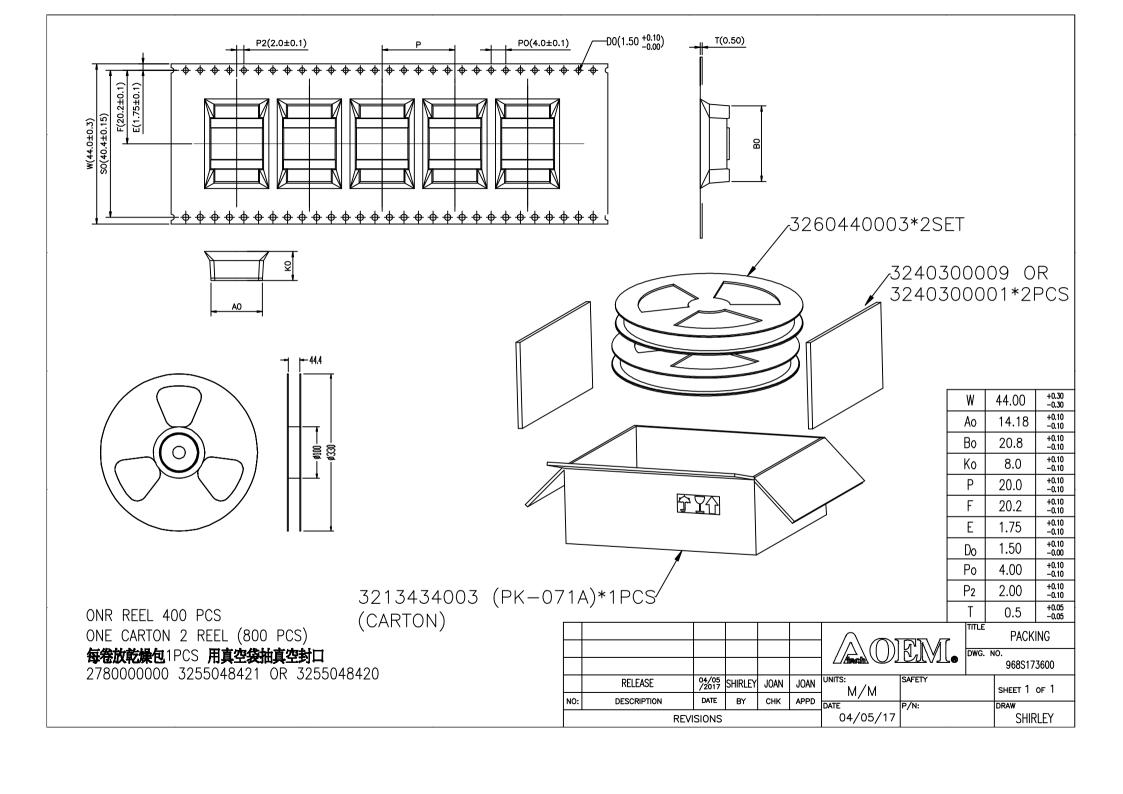
5. HI-POT: (@1500VAC, 3mA, 60SEC): PIN 1.4 - PIN 9.10

6. OPERATING TEMPERATURE : -40°C~+125°C

7. ROHS COMPLIANT

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	OLIANOE DIM	12/10	OLUBI BY	BOLLAN	DOMAN			DWG. N	o. ATS-2079R
LA	CHANGE DIM.	12/10 /2020	SHIRLEY						7110 207311
	RELEASE	07/14 /2020	SHIRLEY	RONAN	RONAN	UNITS: M/M	SAFETY		SHEET 1 OF 1
NO	: DESCRIPTION	DATE	BY	снк	APPD	,	P/N:		DRAW
	F		07/14/20			Shirley			

UNIT: mm





## Pb-free Soldering IR Reflow(SMD)



1,MSL Grade: 1 Level 2,Floor life: 2years 3,Condition:≤30°C RH 85% 2,Form-1(Reference JEDEC J-STD-020D Table 5-2)

	IR reflow profile	Pb-free	
step#	Profile Feature	Condition/Duration	
step1	Ramp-up rate	3°C/second max	
step2	Preheat:150°C-200°C(Ta-Tb)	t1-t2:60-120seconds	
step3	Ramp-up rate(TL to Tp)	3°C/second max	
sieps	Temperature maintained above 217°C	60-150seconds	
ston/	Peak temperature(Tp)	260+0/-5°C	
step4	Time within 5°C of actual peak temperature	30seconds max	
	Ramp-down rate(Tp to TL)	6°C/second max	

## 3,Form-2(Reference JEDEC J-STD-020D Table 4-2)

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Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>			
Thickness	<350	350-2000	>2000			
<1.6mm	260+0/-5°C	260+0/-5°C	260+0/-5℃			
1.6mm-2.5mm	260+0/-5°C	250+0/-5°C	245+0/-5℃			
>2.5mm	250+0/-5° <b>C</b>	245+0/-5°C	245+0/-5℃			