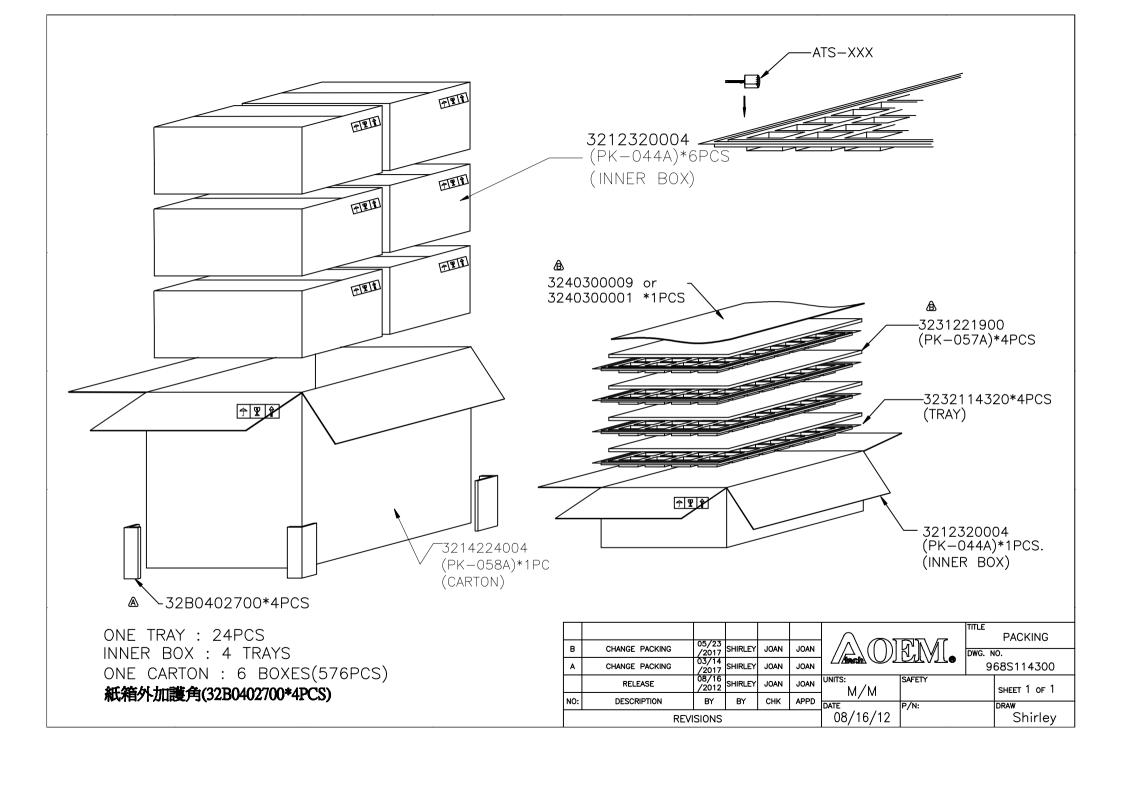


ELECTRICAL CHARACTERISTICS:

Δ 1. DCR : PIN
$$(1-12) = 250mΩ$$
 MAX
PIN $(4+5+6)$ -PIN $(7+8+9) = 30mΩ$ MAX
PIN $(2-11) = 250mΩ$ MAX
PIN $(3-10) = 450mΩ$ MAX

- 2. INDUCTANCE (@10KHz, 0.1Vrms): PIN $(1+2) - (11+12) = 26uH \pm 10\%$
- 3. TURN RATIO (@10KHz, 0.1Vrms) $1-12: 2-11 = 1 \pm 5\%$ $1-12: 3-10 = 2.17 \pm 5\%$ $1-12: 4-9 = 1.3 \pm 5\%$ $1-12: 5-8 = 1.3 \pm 5\%$ $1-12: 6-7 = 1.3 \pm 5\%$
- 4. HI-POT (@1500Vrms ,1mA, 1min) = PRI-SEC
- 5. RoHS COMPLIANT

							\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \		TITLE	FLYBACK TRANSFORMER
	В	CHANGE ELECTRICAL CHARACTERISTICS	07/15 /2013	SHIRLEY	DICK	DICK			DWG. N	io.
	Α	CHANGE LOGO LOCATION	08/20 /2012	SHIRLEY	DICK	DICK				ATS-1143R
Ī		RELEASE	05/16 /2012	SHIRLEY	DICK	DICK	UNITS: M/M	SAFETY		SHEET 1 OF 1
Ī	NO:	DESCRIPTION	DATE	BY	СНК	APPD	DATE	P/N:		DRAW
	REVISIONS						05/16/12			SHIRLEY





Pb-free Soldering IR Reflow(SMD)



1,MSL Grade: 1 Level 2,Floor life: 2years 3,Condition:≤30°C RH 85% 2,Form-1(Reference JEDEC J-STD-020D Table 5-2)

	IR reflow profile	Pb-free	
step#	Profile Feature	Condition/Duration	
step1	Ramp-up rate	3°C/second max	
step2	Preheat:150°C-200°C(Ta-Tb)	t1-t2:60-120seconds	
step3	Ramp-up rate(TL to Tp)	3°C/second max	
sieps	Temperature maintained above 217°C	60-150seconds	
ston/	Peak temperature(Tp)	260+0/-5℃	
step4	Time within 5°C of actual peak temperature	30seconds max	
	Ramp-down rate(Tp to TL)	6°C/second max	

3,Form-2(Reference JEDEC J-STD-020D Table 4-2)

	,					
Package	Volume mm ³	Volume mm ³	Volume mm ³			
Thickness	<350	350-2000	>2000			
<1.6mm	260+0/-5°C	260+0/-5°C	260+0/-5℃			
1.6mm-2.5mm	260+0/-5°C	250+0/-5°C	245+0/-5℃			
>2.5mm	250+0/-5° C	245+0/-5°C	245+0/-5℃			