ETR0305_004b

Low ESR Cap. Compatible Positive Voltage Regulators

■GENERAL DESCRIPTION

The XC6206 series are highly precise, low power consumption, 3 terminal, positive voltage regulators manufactured using CMOS and laser trimming technologies. The series provides large currents with a significantly small dropout voltage.

The XC6206 consists of a current limiter circuit, a driver transistor, a precision reference voltage and an error correction circuit. The series is compatible with low ESR ceramic capacitors. The currrent limiter's foldback circuit operates as a short circuit protection as well as the output current limiter for the output pin.

Output voltages are internally by laser trimming technologies. It is selectable in 0.1V increments within a range of 1.2V to 5.0V.

SOT-23, SOT-89, TO-92 and USP-6B packages are available.

■APPLICATIONS

- Battery powered equipment
- Reference voltage sources
- Cameras, video cameras
- Portable AV systems
- Mobile phones
- Portable games
- Cordless phones,
 wireless communication equipment

■FEATURES

Maximum Output Current : 200mA (3.0V type)

Dropout Voltage : 250mV @ 100mA (3.0V type)

Maximum Operating Voltage : 6.0V

Output Voltage Range : 1.2V ~ 5.0V (0.1V increments)

Highly Accurate $:\pm 2\%@V_{OUT} \ge 1.5V$

<u>+</u>30mV@VouT<1.5V (<u>+</u>1% @VouT≥2.0V)

Low Power Consumption : 1.0μ A (TYP.)

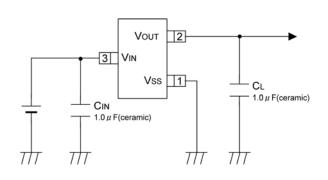
Low ESR Capacitor : Ceramic capacitor compatible
Protection : Current Limit Circuit Built-in

Operating Ambient Temperature: -40° C~ $+85^{\circ}$ C Packages : SOT-23

SOT-89 TO-92 USP-6B

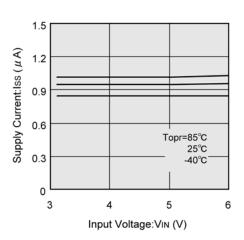
Environmentally Friendly : EU RoHS Compliant, Pb Free

■TYPICAL APPLICATION CIRCUIT

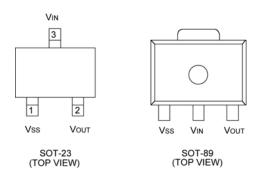


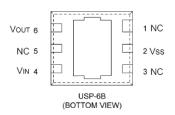
■TYPICAL PERFORMANCE CHARACTERISTICS

XC6206P302

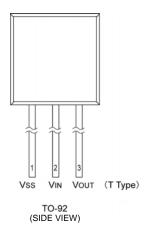


■PIN CONFIGURATION





*The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release. If the pad needs to be connected to other pins, it should be connected to the pin number 4 (V_{IN}).



■PIN ASSIGNMENT

	PIN NU	MBER		PIN NAME	FUNCTIONS
SOT-23	SOT-89	USP-6B	TO-92	FININAME	FUNCTIONS
1	1	2	1	Vss	Ground
3	2	4	2	Vin	Power Input
2	3	6	3	Vout	Output
-	-	1, 3, 5	-	NC	No Connection

■ PRODUCT CLASSIFICATION

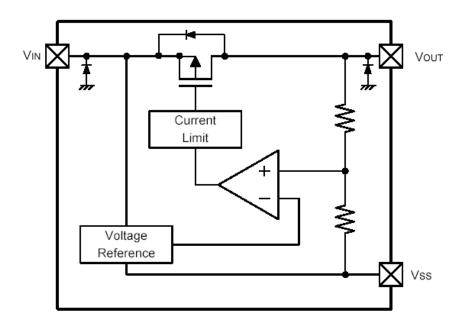
Ordering Information

 $\underline{XC6206P \ \ (1)(2)(3)(4)(5)-(6)}^{(^*1)}$

DESIGNATOR	ITEM	SYMBOL	DESCRIPTION
12	Output Voltage	12~50	e.g. Vouт: 3.0V→①=3, ②=0
3	Accuracy	2	± 2% (V _{OUT} ≥1.5V), ±30mV (VouT<1.5V)
3	Accuracy	1	±1% (Vout≧2.0V)
		MR	SOT-23 (3,000/Reel)
		MR-G	SOT-23 (3,000/Reel)
	Packages	PR	SOT-89 (1,000/Reel)
45-6		PR-G	SOT-89 (1,000/Reel)
		DR	USP-6B (3,000/Reel)
4.5	(Order Unit)	DR-G	USP-6B (3,000/Reel)
		TH	TO-92 (T type), Paper type (2,000/Tape)
		TH-G	TO-92 (T type), Paper type (2,000/Tape)
		TB	TO-92 (T type), Bag type (500/Bag)
		TB-G	TO-92 (T type), Bag type (500/Bag)

^{(&}lt;sup>11)</sup> The "-G" suffix denotes Halogen and Antimony free as well as being fully RoHS compliant.

■BLOCK DIAGRAM



^{*}Diodes inside the circuit are an ESD protection diode and a parasitic diode.

■ ABSOLUTE MAXIMUM RATINGS

Ta=25°C

PARAMETER		SYMBOL	RATINGS	UNITS
Input Voltag	е	Vin	7.0	V
Output Curre	ent	lout	500 *	mA
Output Voltag	ge	Vout	Vss - 0.3 ~ ViN + 0.3	V
	SOT-23	Pd	250	
Dower Dissipation	SOT-89		500	mW
Power Dissipation	USP-6B		100	IIIVV
	TO-92		300	
Operating Ambient Temperature		Topr	- 40 ~ + 85	°C
Storage Temper	ature	Tstg	- 55 ~ + 125	°C

^{*} IOUT=Pd / (VIN-VOUT)

■ELECTRICAL CHARACTERISTICS

●XC6206P series Ta=25 °C

PARAMETER	SYMBOL	CONDITIONS(*1)	MIN.	TYP.	MAX.	UNIT S	CIRCUIT
Output Voltage (*4)	VOUT(E) ^(*3)	Iout=30mA	x 0.98 VOUT(T) (*2) x 1.02 E-1		V	1	
Maximum Output Current	IOUTMAX	-	E-2	-	-	mA	1
Load Regulation	ΔVουτ	VOUT(T)>1.8V: $1mA \le IOUT \le 100mA$ VOUT(T) $\le 1.8V$: $1mA \le IOUT \le 50mA$	-	-	E-3	mV	1
	Vdif1	Iout=30mA	-	E-4	ļ	mV	
Dropout Voltage ^(*5) Vdif2		Vout(t)>1.8V: Iout=100mA Vout(t)≤1.8V: Iout=60mA		E-5		mV	1
Supply Current	IDD	VCE=VIN	-	1.0	3.0	μΑ	2
Line Regulation	∆Vout ∆Vin·Vout	$VOUT(T)<4.5V:VOUT(T)+1.0V \le VIN \le 6.0V$ $VOUT(T)\ge 4.5V:5.5V \le VIN \le 6.0V$ IOUT=30mA	-	0.05	0.25	%/V	1)
Input Voltage	VIN	-	1.8	-	6.0	V	-
Output Voltage Temperature Characteristics	∆Vouт _∆Topr∙Vouт	Iout=30mA -40 °C≦Topr≦85 °C	-	<u>+</u> 100	-	ppm/ °C	1
Short Circuit Current	Ishort	VIN=Vout+1.5V, Vout=Vss	-	E-6	-	mA	1)

NOTE:

Vout1 :A voltage equal to 98% of the output voltage whenever an amply stabilized $\{Vout(T) + 1.0V\}$ is input with each I_{OUT} . VIN1 :The input voltage when Vout1 appears as input voltage is gradually decreased.

^{* 1} Unless otherwise stated, VIN = VOUT(T) + 1.0V

^{* 2} Vout(t) :Nominal voltage

^{* 3} VOUT(E) :Effective output voltage (le. The output voltage when "Vout(T)+1.0V" is provided at the VIN pin while maintaining a certain lout value.)

^{* 4} For output voltage accuracy, Please refer to E-1 table.

^{* 5} Vdif =VIN1 -VOUT1

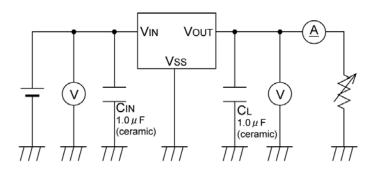
■ ELECTRICAL CHARACTERISTICS (Continued)

Electrical Characteristics Chart

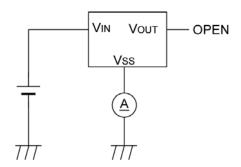
	E-1		E-2	E-3	E	-4	E	-5	E-6		
PARAMETER	C	UTPUT	VOLTAGI		MAX.	LOAD	DD.C.	DOL 17	DD.C.	DOL 17	OUCST
NONE	20			%	OUTPUT	REGULATIO		POUT		POUT	SHORT
NOMINAL VOLTAGE	ACCU			RACY	CURRENT	CURRENT N		VOLTAGE 1		AGE 2	CURRENT
					IOUTHAN (mA)	△Vout	Vo	dif1	Vo	lif2	Ishort
Vout(t)	Vout(E) (V)	VOUT	(E) (V)	IOUTMAX (mA)	(mV)	(m	ıV)	(m	ıV)	(mA)
	MIN.	MAX.	MIN.	MAX.	MIN.	MAX.	TYP.	MAX.	TYP.	MAX.	TYP.
1.2	1.170	1.230					460	760	700	960	
1.3	1.270	1.330			60	40	400	650			180
1.4	1.370	1.430					350	590	580	860	
1.5	1.470	1.530	Not Av	ailable			300	510			455
1.6	1.568	1.632				45	250	450	450	810	155
1.7	1.666 1.764	1.734 1.836			80	45	200 150	410 390			
1.9	1.862	1.938					130	390		780	
2.0	1.960	2.040	1.980	2.020						700	130
2.1	2.058	2.042	2.079	2.121							100
2.2	2.156	2.244	2.178	2.222	120	50					
2.3	2.254	2.346	2.277	2.323	_						
2.4	2.352	2.448	2.376	2.424			100	370	350		
2.5	2.450	2.550	2.475	2.525						710	
2.6	2.548	2.652	2.574	2.626	150	55					
2.7	2.646	2.754	2.673	2.727							
2.8	2.744	2.856	2.772	2.828							
2.9	2.842	2.958	2.871	2.929							
3.0	2.940	3.060	2.970	3.030							
3.1	3.038	3.162	3.069	3.131							
3.2	3.136	3.264	3.168	3.232		60					
3.3	3.234	3.366	3.267	3.333							
3.4	3.332	3.468	3.366	3.434	200		75	350	250	680	
3.5	3.430 3.528	3.570 3.672	3.465 3.564	3.535 3.636							
3.7	3.626	3.774	3.663	3.737		65					100
3.8	3.724	3.876	3.762	3.838		05					
3.9	3.822	3.978	3.861	3.939							
4.0	3.920	4.080	3.960	4.040							
4.1	4.018	4.182	4.059	4.141							
4.2	4.116	4.284	4.158	4.242		70					
4.3	4.214	4.386	4.257	4.343							
4.4	4.312	4.488	4.356	4.444			60	320	200	630	
4.5	4.410	4.590	4.455	4.545	250		00	320	200	030	
4.6	4.508	4.692	4.554	4.646							
4.7	4.606	4.794	4.653	4.747		75					
4.8	4.704	4.896	4.752	4.848							
4.9	4.802	4.998	4.851	4.949							
5.0	4.900	5.100	4.950	5.050		80	50	290	175	600	

■TEST CIRCUITS

Circuit ①

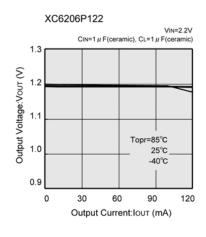


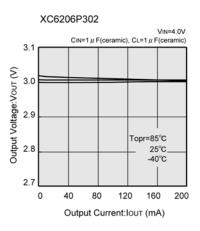
Circuit ②

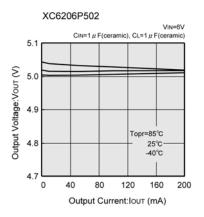


■TYPICAL PERFORMANCE CHARACTERISTICS

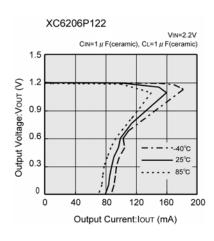
(1) Output Voltage vs. Output Current

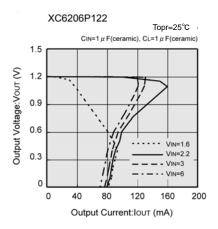


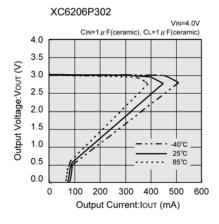


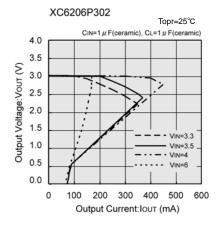


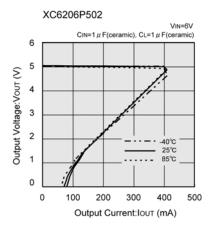
(2) Current Limit

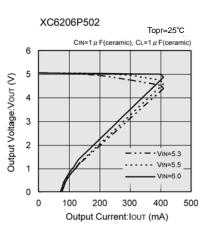




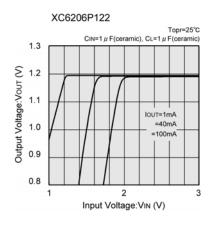


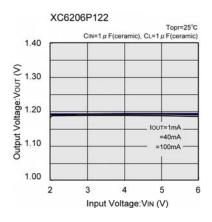


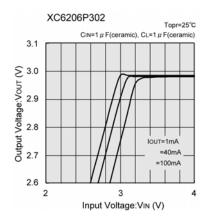


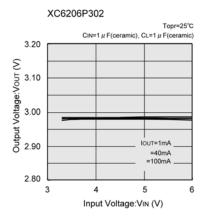


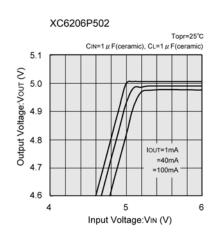
(3) Output Voltage vs. Input Voltage

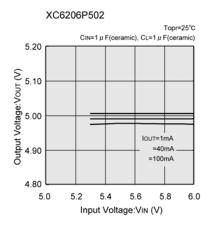




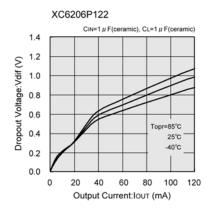


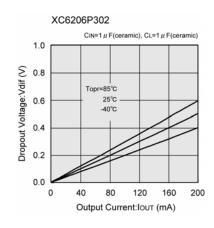


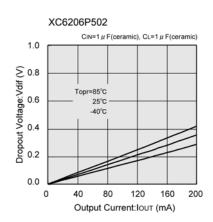




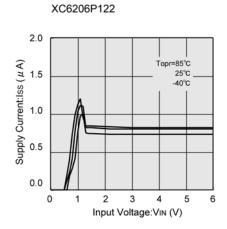
(4) Dropout Voltage vs. Output Current

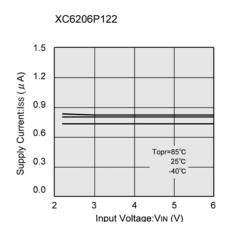


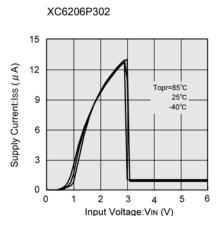


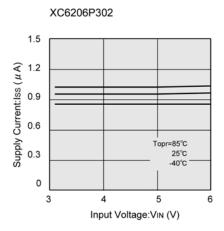


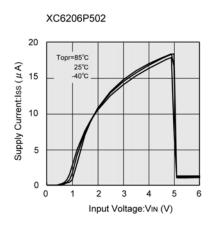
(5) Supply Current vs. Input Voltage

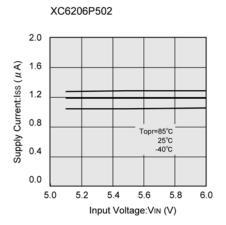




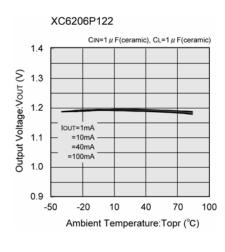


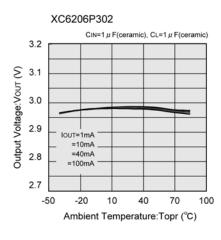


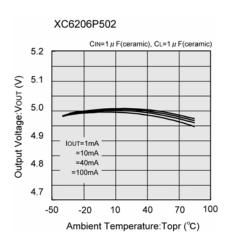




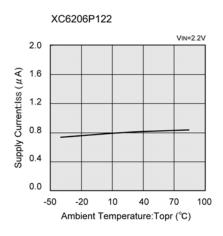
(6) Output Voltage vs. Ambient Temperature

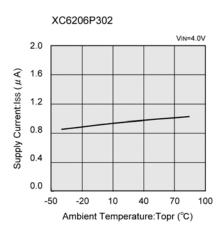


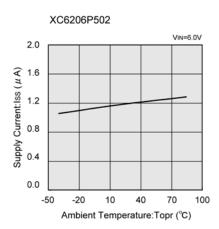




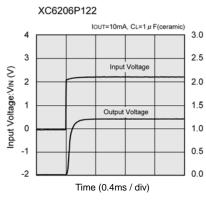
(7) Output Voltage vs. Ambient Temperature

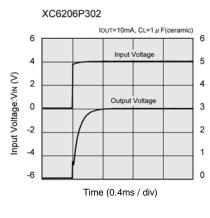




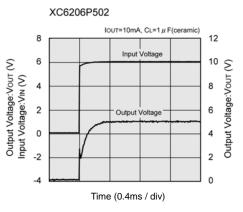


(8) Input Transient Response 1

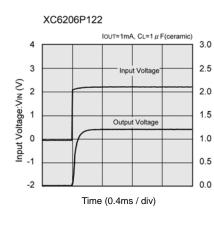


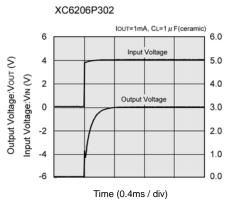


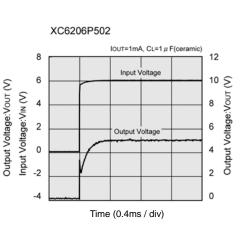
Output Voltage: Vour (V)



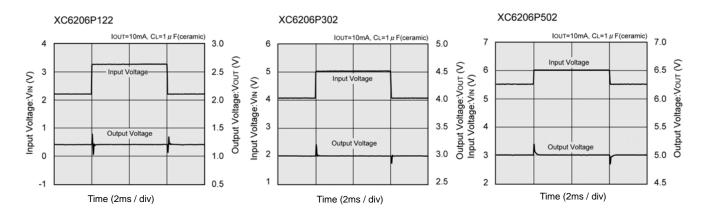


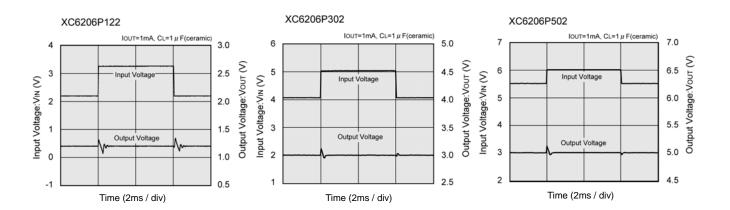




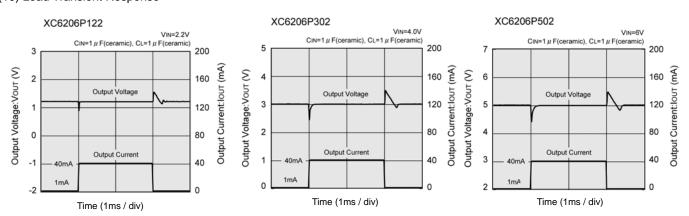


(9) Input Transient Response 2

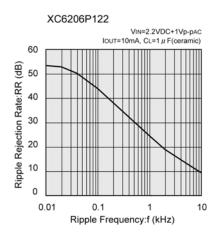


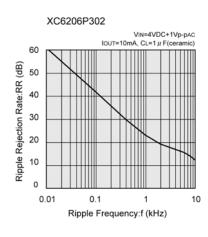


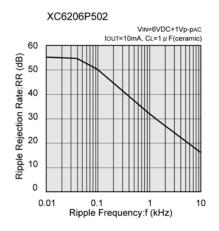
(10) Load Transient Response

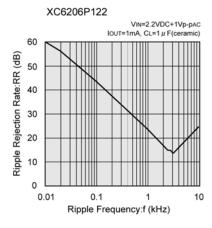


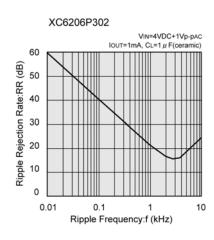
(11) Ripple Rejection Rate

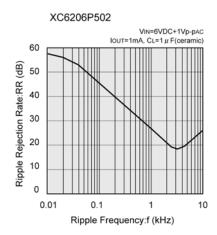








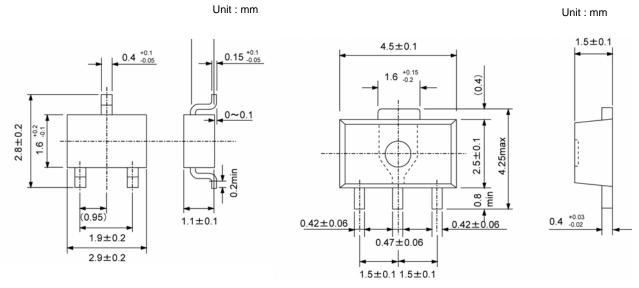


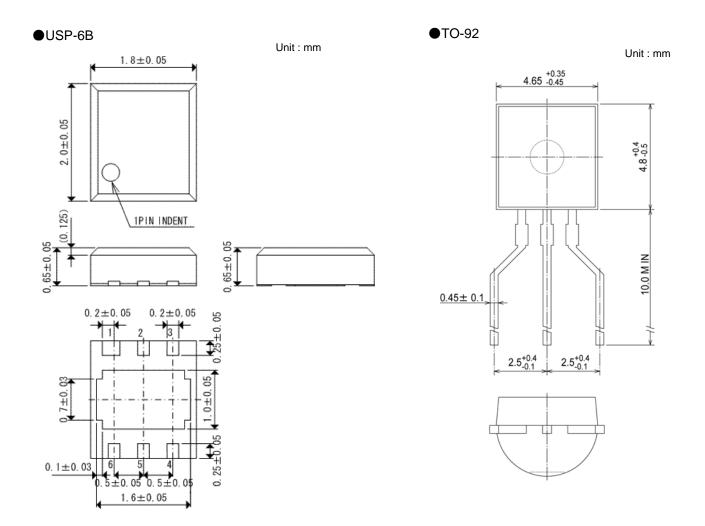


■PACKAGING INFORMATION

●SOT-23

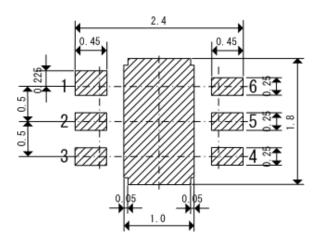
●SOT-89



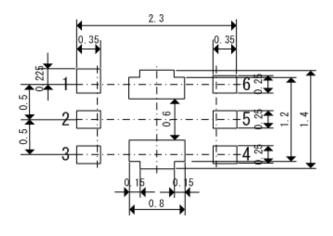


■ PACKAGING INFORMATION (Continued)

●USP-6B Reference Pattern Layout

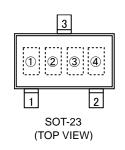


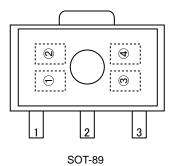
●USP-6B Reference Metal Mask Design



■MARKING RULE

●SOT-23, SOT-89





(TOP VIEW)

① represents product number

MARK	PRODUCT SERIES
6	XC6206P****

2 represents 3 pins regulator

MA	PRODUCT SERIES	
VOLTAGE=0.1 ~ 3.0V	VOLTAGE=3.1 ~ 6.0V	PRODUCT SERIES
5	6	XC6206P****

3 represents output voltage

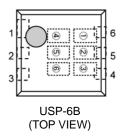
MARK	VOLTAGE (V)			MARK	OUTPL	JT VOLTA	GE (V)
0	-	3.1	-	F	1.6	4.6	-
1	-	3.2	-	Н	1.7	4.7	-
2	-	3.3	-	K	1.8	4.8	-
3	-	3.4	-	L	1.9	4.9	-
4	-	3.5	-	M	2.0	5.0	1
5	-	3.6	-	N	2.1	-	1
6	-	3.7	-	Р	2.2	-	1
7	-	3.8	-	R	2.3	-	-
8	-	3.9	-	S	2.4	-	-
9	-	4.0	-	T	2.5	-	-
Α		4.1	-	U	2.6	-	1
В	1.2	4.2	-	V	2.7	-	1
С	1.3	4.3	-	X	2.8	-	-
D	1.4	4.4	-	Υ	2.9	-	-
E	1.5	4.5	-	Z	3.0	-	-

4 represents production lot number

0 to 9, A to Z, and inverted 0 to 9, A to Z repeated. (G, I, J, O, Q, W excepted.)

■MARKING RULE (Continued)

●USP-6B



①② represents product number

MA	PRODUCT SERIES		
1	2	PRODUCT SERIES	
0	6	XC6206P***D*	

3 represents 3 pins regulator

MARK	PRODUCT SERIES
Р	XC6206P***D*

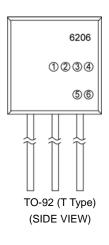
45 represents output voltage

MAF	RK		PRODUCT SERIES
4	5	OUTPUT VOLTAGE(V)	PRODUCT SERIES
3	3	3.3	XC6206P33*D*
5	0	5.0	XC6206P50*D*

6 represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

● TO-92



① represents type of regulator

MARK	PRODUCT SERIES
Р	XC6206P****

23 represents output voltage

MARK		\/OLTACE (\/)	PRODUCT SERIES
2	3	VOLTAGE (V)	FRODUCT SERIES
3	3	3.3	XC6206P33***
5	0	5	XC6206P50***

4 represents output voltage accuracy

MARK	OUTPUT VOLTAGE ACCURACY	PRODUCT SERIES
1	±1%	XC6206P**1**
2	±2%	XC6206P**2**

⑤ represents least significant digit of the production year

	,
MARK	PRODUCTION YEAR
3	2003
4	2004

6 represents production lot number

0 to 9, A to Z repeated. (G, I, J, O, Q, W excluded)

^{*}No character inversion used.

^{*}No character inversion used.

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