

PRODUCT : **TFT TOUCH MODULE**

MODULE NO. : **ATK-1018**

SUPPLIER: **A L I E N T E K**

DATE: **May 31, 2017**

SPECIFICATION

Revision: 0.0

ATK1018

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact ALIENTEK department for updated specification and product status before design for this product or release of this order.

WRITTEN BY	CHECKED BY	APPROVED BY
Jason	Liwen	Henry

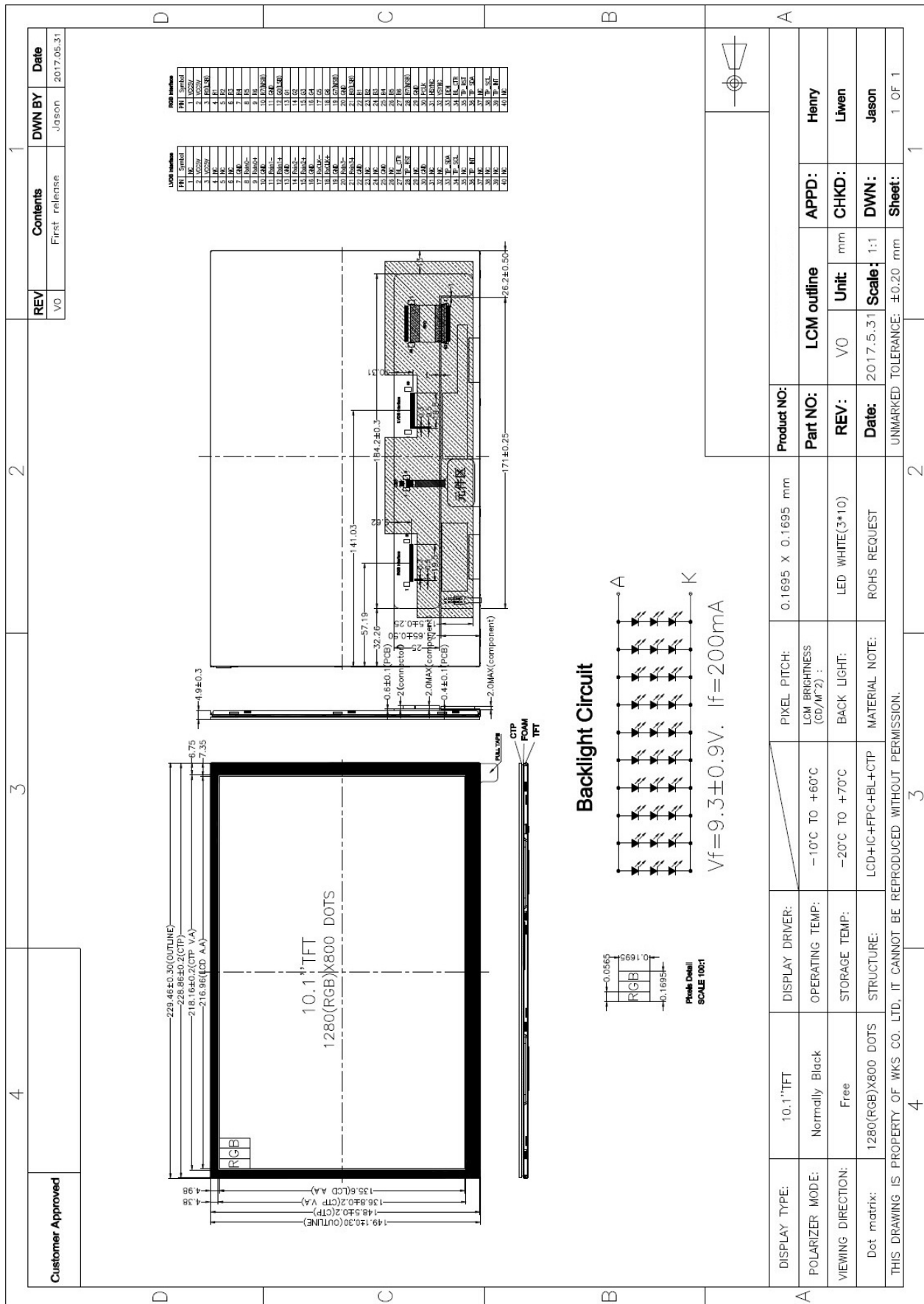
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1、GENERAL INFORMATION

Item of general information	Contents	Unit
LCD Display Size(Diagonal)	10.1	inch
LCD Display Type	TFT/TRANSMISSIVE	-
LCD Display Mode	Normally Black	-
Recommended Viewing Direction	Free	-
Gray inversion Direction	Free	-
Module size (W×H×T)	229.46×149.10×4.90	mm
Active area (W×H)	216.96×135.60	mm
Number of pixels(Resolution)	1280RGB×800	pixel
Pixel pitch (W×H)	0.1695×0.1695	mm
Color Pixel Arrangement	RGB Stripe	-
LCD Driver IC	-	-
Module Interface Type	RGB or LVDS Interface	-
Module Input voltage	5.0V	V
Module Power consumption	600	mA
Color Numbers	16.7M	-
Backlight Type	White LED	-

2、EXTERNAL DIMENSIONS



3、ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
LCD supply voltage	VDD	-0.3	3.9	V
Operating temperature	Top	-10	60	°C
Storage temperature	Tst	-20	70	°C
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4、ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Typ.	Max.	Unit
PCB operating voltage	VCC5V	-	5.0	-	V
PCB Board Input Current	Idd	-	600	-	mA
LCD I/O operating voltage	VDD	2.3	2.5	2.7	V
Input voltage 'H' level	VIH	0.8*VDD	-	VDD	V
Input voltage 'L' level	VIL	VSS	-	0.2*VDD	V
Output voltage 'H' level	VOH	VDD-0.4	-	VDD	V
Output voltage 'L' level	VOL	VSS	-	VSS+0.4	V

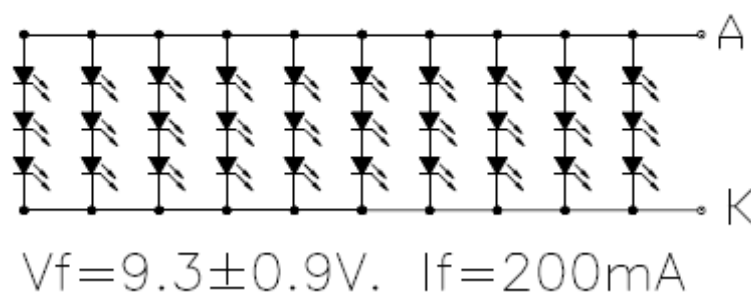
5、BACKLIGHT CHARACTERISTICS

Item of backlight characteristics	Symbol	Min.	Typ.	Max.	Unit	Condition
Forward Voltage	Vf	8.4	9.3	10.2	V	If=200mA Ta=25℃
Forward Current	If	-	200	-	mA	
Luminance	Lv	-	-	-	cd/m2	
Number of LED	-	-	3*10=30	-	Piece	-
LED Connection mode	P/S	-	Serial/Parallel	-	-	-
Lifetime of LED	-	-	TBD	-	hour	-

Note:

- Using condition: constant current driving method If=200mA(+/-10%).
- Backlight control via the BL_CTR pin or PWM signal.
- Backlight circuit:

Backlight Circuit



6、CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	1280 × 800	pixel	-
Surface Hardness	6H	-	-
Transparency	≥86%	-	-
Driver IC	GT9271	-	-
Interface Type	I2C	-	-
Support Points	10	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-

7、ELECTRO-OPTICAL CHARACTERISTICS

Item of electro-optical characteristics		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response time		Tr+Tf	$\theta=0$ $\phi=0$ Ta=25℃	-	25	50	ms	FIG 1.	4
Contrast Ratio		CR		-	600	-	-	FIG 2.	1
Luminance uniformity		δ WHITE		-	80	-	%	FIG 2.	3
Surface Luminance		Lv		-	200	-	cd/m2	FIG 2.	2
CIE (x, y) chromaticity	White	White x	$\theta=0$ $\phi=0$ Ta=25℃	0.27	0.31	0.35	-	FIG 2.	5
		White y	0.28	0.32	0.36				
Viewing angle range	$\phi=90(12\text{ o'clock})$		CR ≥ 10	75	85	-	deg	FIG 3.	6
	$\phi=270(6\text{ o'clock})$			75	85	-	deg		
	$\phi=0(3\text{ o'clock})$			75	85	-	deg		
	$\phi=180(9\text{ o'clock})$			75	85	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

$$\text{Contrast Ratio(CR)} = \frac{\text{Average Surface Luminance with all white pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}{\text{Average Surface Luminance with all black pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4, P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (δWHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}$$

Note 4. The response time is defined as the LCD optical switching time interval between “White” state and “Black” state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For more information see FIG 1.

Note 5. CIE (x, y) chromaticity, The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers’s ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

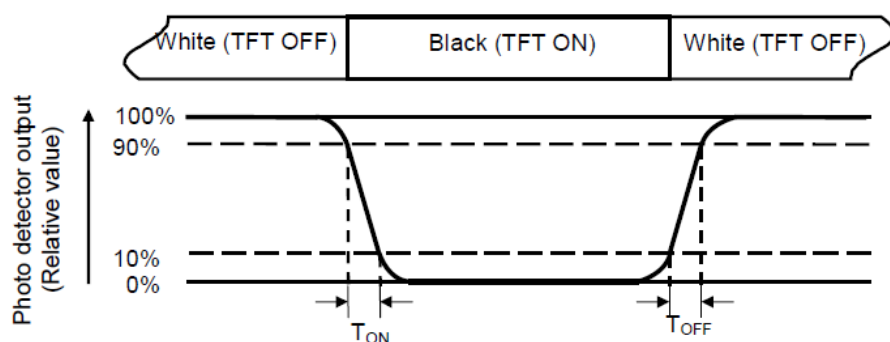


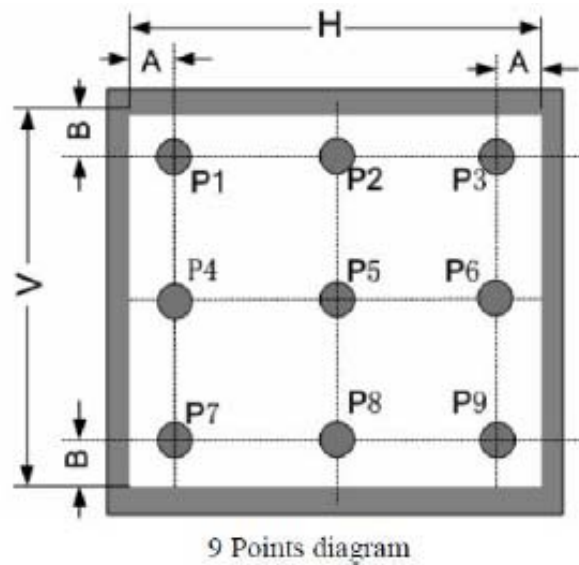
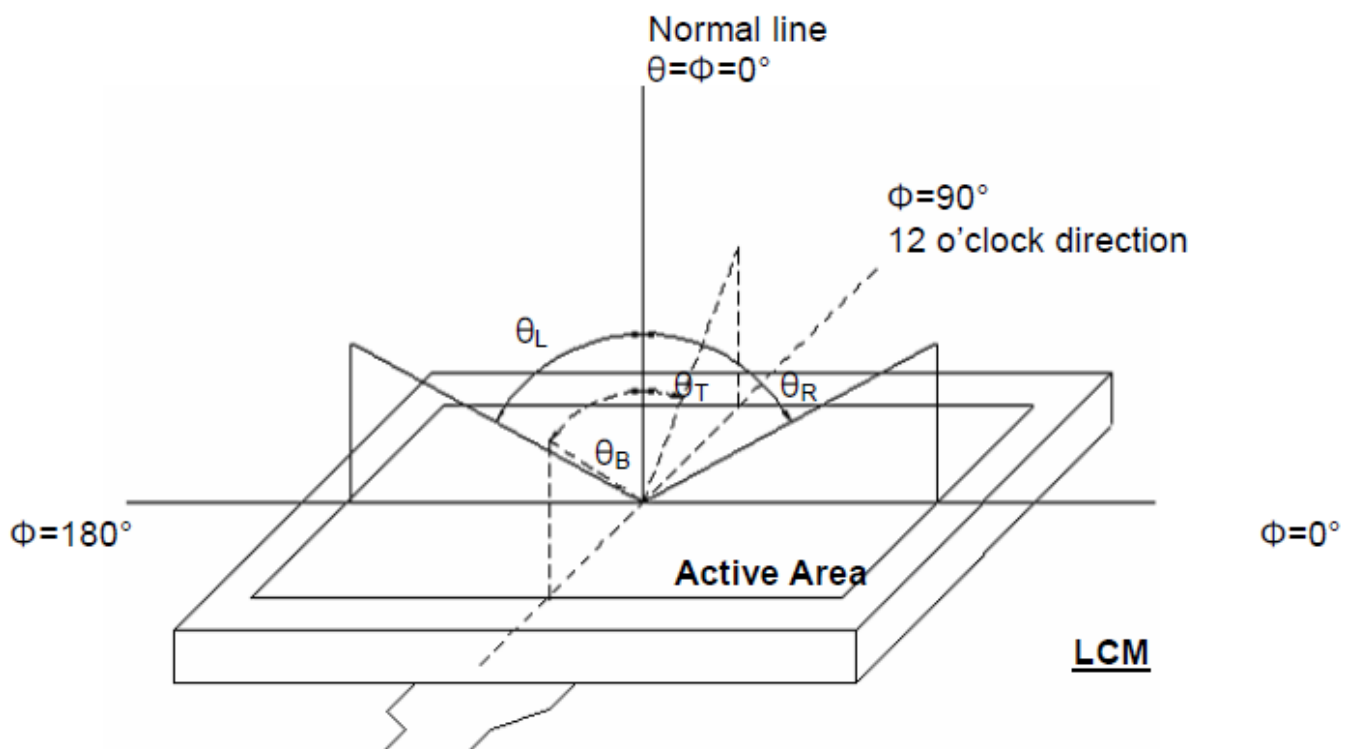
FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance**uniformity, CIE (x , y) chromaticity**

$A : H/6 ;$

$B : V/6 ;$

$H, V : \text{Active Area(AA) size}$

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

**FIG.3. The definition of viewing angle**

8、INTERFACE DESCRIPTION

A、RGB Input Interface Description(J2)

NO.	Symbol	I/O	DESCRIPTION
1~2	VCC5V	P	Module Power supply (5V Typ.)
3~10	R0~R7	I	8bit digital Red data input(R0:LSB; R7:MSB)
11	GND	P	Power ground
12~19	G0~G7	I	8bit digital Green data input(G0:LSB; G7:MSB)
20	GND	P	Power ground
21~28	B0~B7	I	8bit digital Blue data input(B0:LSB; B7:MSB)
29	GND	P	Power ground
30	DCLK	I	Clock signal.
31	HSYNC	I	Horizontal Sync input.
32	VSYNC	I	Vertical Sync input.
33	DEN	I	Data input Enable.
34	BL_CTR	I	Backlight control pin
35	TP_RST	I	CTP external reset signal, Low is active
36	TP_SDA	I/O	CTP I2C data input and output
37	NC	-	No connection
38	TP_SCL	I	CTP I2C clock input
39	TP_INT	I/O	CTP External interrupt to the host
40	NC	-	No connection

Application Note:

For RGB interface input:

- 1、 For RGB565 Input Format: R3~R7, G2~G7, B3~B7 be used.
- 2、 For RGB666 Input Format: R2~R7, G2~G7, B2~B7 be used.

For LVDS interface input:

Please remove RP8~RP12 on the PCB as follows:



B、LVDS Input Interface Description(J3)

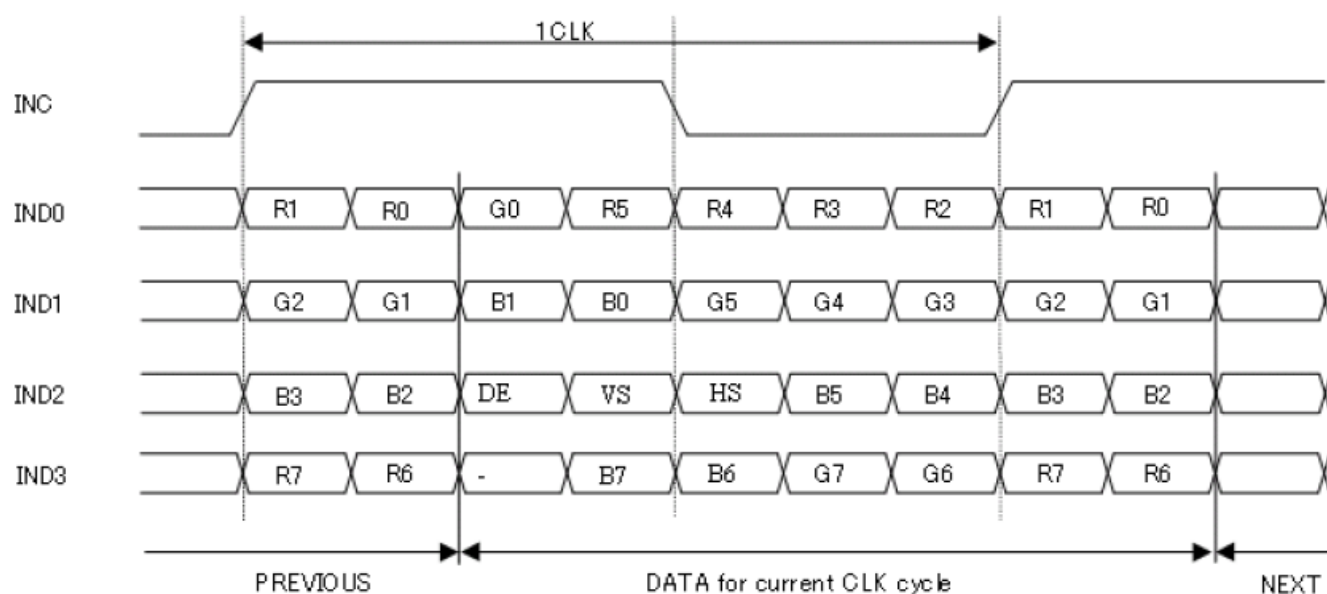
NO.	Symbol	I/O	DESCRIPTION	Remark
1	NC	-	No connection	
2~3	VCC5V	P	Module Power supply (5V Typ.)	
4~6	NC	-	No connection	
7	GND	P	Ground	
8	Rxin0-	I	-LVDS Differential Data Input	R0~R5,G0
9	Rxin0+	I	+LVDS Differential Data Input	
10	GND	P	Ground	
11	Rxin1-	I	-LVDS Differential Data Input	G1~G5,B0,B1
12	Rxin1+	I	+LVDS Differential Data Input	
13	GND	P	Ground	
14	Rxin2-	I	-LVDS Differential Data Input	B2~B5,HS,VS,DE
15	Rxin2+	I	+LVDS Differential Data Input	
16	GND	P	Ground	
17	RxCLK-	I	-LVDS Differential Clock Input	LVDS CLK
18	RxCLK+	I	+LVDS Differential Clock Input	
19	GND	P	Ground	
20	Rxin3-	I	-LVDS Differential Data Input	R6,R7,G6,G7,B6,B7
21	Rxin3+	I	+LVDS Differential Data Input	
22	GND	P	Ground	
23~24	NC	-	No connection	
25	GND	P	Ground	
26	NC	-	No connection	
27	BL_CTR	I	Backlight control pin	
28	CTP_RST	I	CTP external reset signal, Low is active	
29	NC	-	No connection	
30	GND	P	Ground	
31~32	NC	-	No connection	
33	CTP_SDA	I/O	CTP I2C data input and output	
34	CTP_SCL	I	CTP I2C clock input	
35	NC	-	No connection	
36	CTP_INT	I/O	CTP External interrupt to the host	
37~40	NC	-	No connection	

9、INPUT TIMING

RGB Input Timing Table

Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency@ Frame rate=60Hz	DCLK	68.9	71.1	73.4	MHz
Horizontal display area	thd	1280			DCLK
1 Horizontal Line	th	1340	1440	1470	DCLK
HSYNC pulse width	thpw	-	10	-	DCLK
HSYNC Back Porch(Blanking)	thb	-	80	-	DCLK
HSYNC Front Porch	thfp	-	70	-	DCLK
Vertical display area	tvd	800			H
VSYSN period time	tv	815	823	833	H
VSYSN pulse width	tvpw	-	3	-	H
VSYSN Back Porch(Blanking)	tvb	-	10	-	H
VSYSN Front Porch	tvfp	-	10	-	H

LVDS Data Input Format



10、RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	70℃/120 hours
2	Low Temperature Storage	-20℃/120 hours
3	High Temperature Operating	60℃/120 hours
4	Low Temperature Operating	-10℃/120 hours
5	Temperature Cycle Storage	-10℃(30min.)~25(5min.)~60℃(30min.)×10cycles

A、Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- Air bubble in the LCD;
- Sealleak;
- Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

B、Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11、INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

11.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

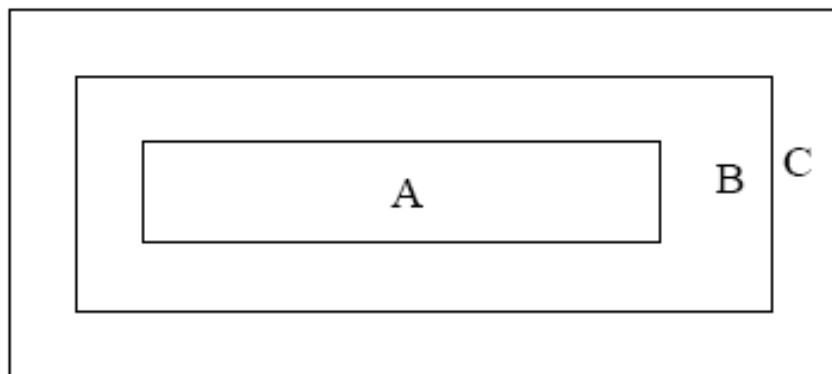
Minor defect: AQL 1.5

11.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25℃ and normal humidity 60 ±5%RH)

11.3 Definition of Inspection Item.

A、 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

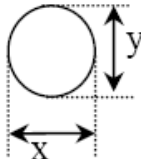
B、 Definition of some visual defect

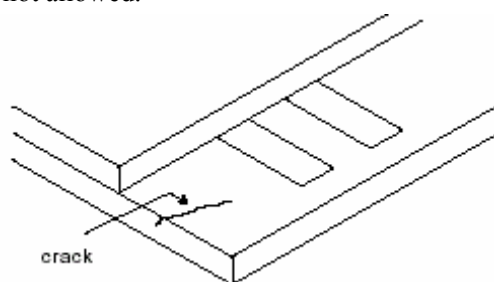
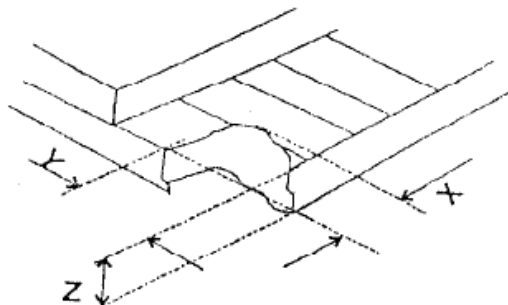
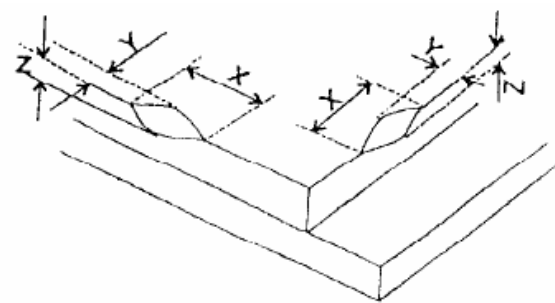
Bright dot	Because of losing all or part function, bad pixel dots appear bright and the size is more than 50% of one dot in which LCD panel is displaying under black pattern.
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.

11.4 Major Defect


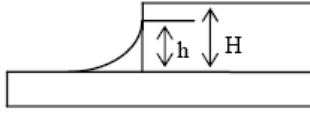
Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	

5、Minor Defect

Item No.	Items to be inspected	Inspection standard					Classification of defects	
1	Bright dot /dark dot defect	<div>Zone</div>		Acceptable Qty			C	Minor
				A+B				
				4.3''~7''	7~10.1''	>10.1''		
		Bright pixel dot		1	2	3	Acceptable	
		Dark pixel dot		4	4	4		
		2bright dots adjacent		0	0	0		
		2dark dots adjacent		0	0	0		
		Total bright and dark dots		5	6	7		
Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign material and other reasons are judged by the dot defect of 5.2.								
2	<div>Dot defect</div> <div></div> <div>Φ=(x+y) /2</div>	<div>Zone</div>		Acceptable Qty			C	Minor
				A+B				
				4.3''~7''	7~10.1''	>10.1''		
		Φ ≤0.2		Acceptable	Acceptable	Acceptable	Acceptable	
		0.2 < Φ ≤0.5		4	5	6		
		Φ >0.5		0	0	0		
Note: 1. Minimum distance between defective dots is more than 5 mm; 2. The quantity of defect is zero in operating condition.								
3	Linear defect	<div>Zone</div>		Acceptable Qty			C	Minor
				A+B				
				Length	Width	4.3''~7''		
		Ignore	W≤0.05	Acceptable	Acceptable	Acceptable	Acceptable	
		L≤5.0	0.05 < W≤0.1	4	5	6		
		L>5.0	W>0.1	0	0	0		

6	Glass defect	<div>(i) Crack Cracks are not allowed.</div> <div></div>	Minor								
		<div>(ii) TFT chips on corner</div> <div></div> <table><tr><td>X</td><td>Y</td><td>Z</td><td>Acceptable</td></tr><tr><td>≤3.0</td><td>≤3.0</td><td>Not more than the thickness of glass</td><td>N≤3</td></tr></table> <div>Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.</div>	X	Y	Z	Acceptable	≤3.0	≤3.0	Not more than the thickness of glass	N≤3	Minor
		X	Y	Z	Acceptable						
≤3.0	≤3.0	Not more than the thickness of glass	N≤3								
<div>(iii) Usual surface crack</div> <div></div> <table><tr><td>X</td><td>Y</td><td>Z</td><td>Acceptable</td></tr><tr><td>≤1.5</td><td>≤1.5</td><td>Not more than the thickness of glass</td><td>N≤4</td></tr></table> <div>It is only applicable to the upper glass of LCD.</div>	X	Y	Z	Acceptable	≤1.5	≤1.5	Not more than the thickness of glass	N≤4	Minor		
X	Y	Z	Acceptable								
≤1.5	≤1.5	Not more than the thickness of glass	N≤4								

11.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil ($\Phi 0.5$ mm or more) on substrate pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed $\Phi 0.2$ mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
11	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder. 	Minor
	3. Chips	$(3/2) H \geq h \geq (1/2) H$ 	Minor
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \geq 0.13$ mm. The diameter of solder ball $d \leq 0.15$ mm.	Minor
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm ² .	Minor
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major