



Title			
Xilinx CPLD Board			
Size	Number	Revision	
A4			
Date:	2011/11/19	Sheet of	
File:	E:\Lsoft\PCB\XC9500\Sheet1.SchDoc	Drawn By L STUDIO	

Comment	Description	Designator	Footprint	LibRef	Quantity
10uF	Polarized Capacitor (Surface Mount)	C1, C3	CAPC3216L	Cap Pol3	2
0.1uF	Capacitor (Semiconductor SIM Model)	C2, C4, C7, C8	CAPC2012L	Cap Semi	4
LED2	Typical RED, GREEN, YELLOW, AMBER GaAs LED	D1, D2, D3, D4, D5	LED(0805)	LED2	5
Inductor	Inductor	L1	INDC2012L	Inductor	1
Header 20	Header, 20-Pin	P1, P2	HDR1X20	Header 20	2
JTAG	Header, 5-Pin, Dual row	P3	IDC-10	Header 5X2	1
Header 4X2	Header, 4-Pin, Dual row	P4	HDR2X4	Header 4X2	1
PWR2.5	Low Voltage Power Supply Connector	PWR	POWER	PWR2.5	1
1K	Semiconductor Resistor	R1, R5, R6, R7, R8	RESC2012L	Res Semi	5
0	Semiconductor Resistor	R3, R4, R9, R10	RESC2012L	Res Semi	4
10K	Semiconductor Resistor	R11, R12, R13, R14	RESC2012L	Res Semi	4
SW DPDT	Switch	S1	SWITCH	SW DPDT	1
XC9572	XC9500, 5V CPLD with 36 Macrocells, 44-Pin VQFP, Commercial Grade, Pb-Free	U1	VQ44_N	XC9536- 10VQG44C	1
AMS1117-3.3	Voltage Regulator	U2	AMS1117	Volt Reg	1
25M	Header, 2-Pin, Dual r	Y1	OSC_5X7	MHDR2X2	1