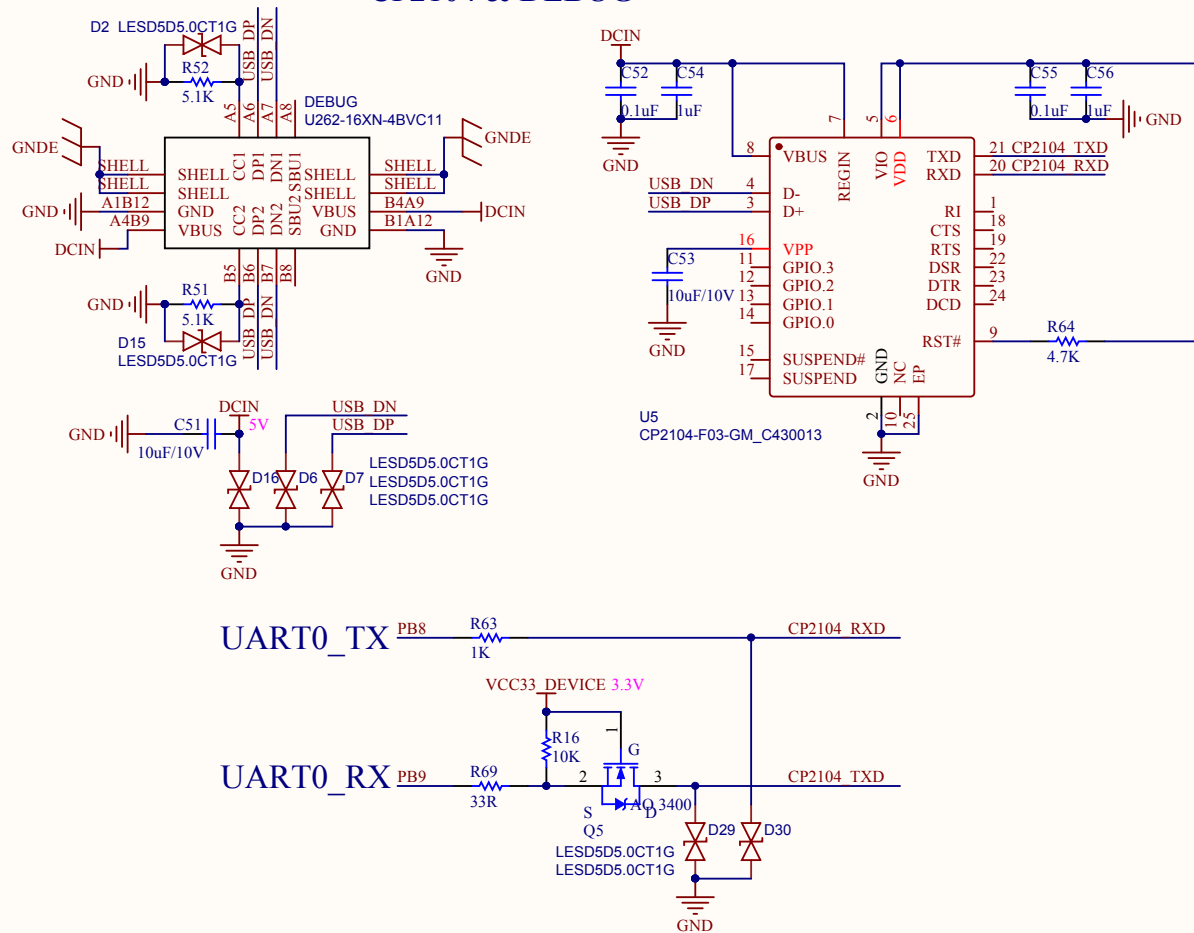
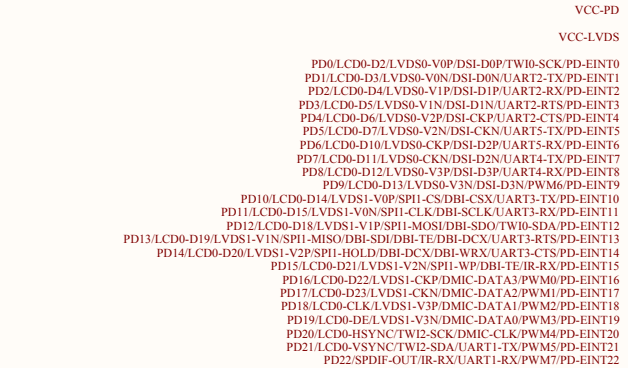
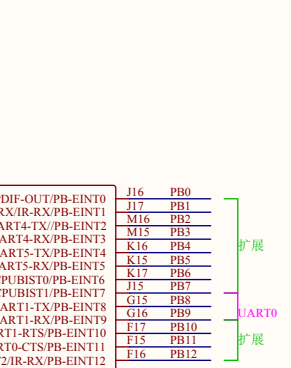
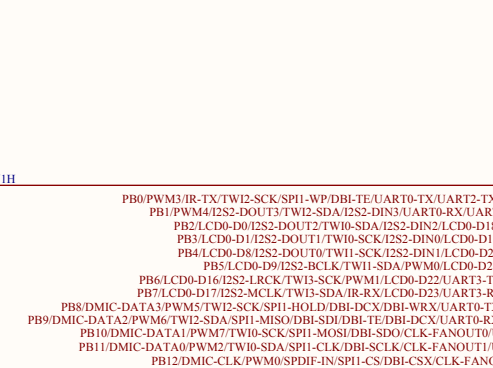
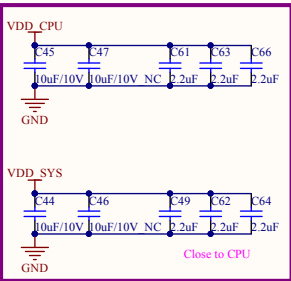
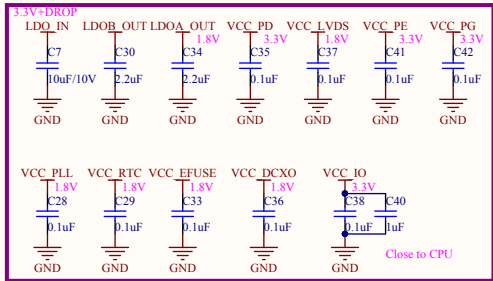
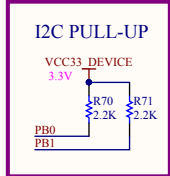
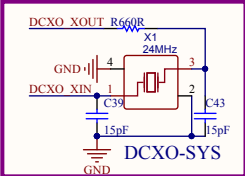
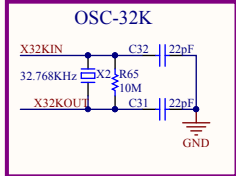


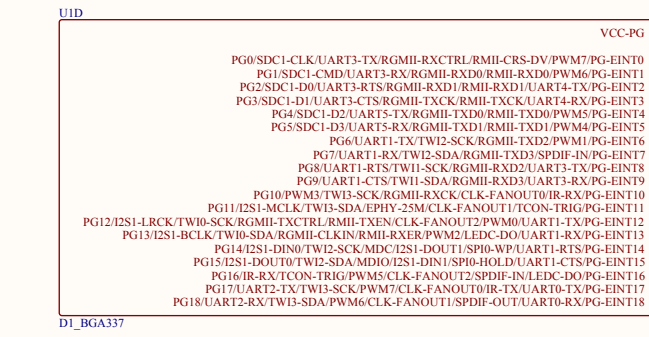
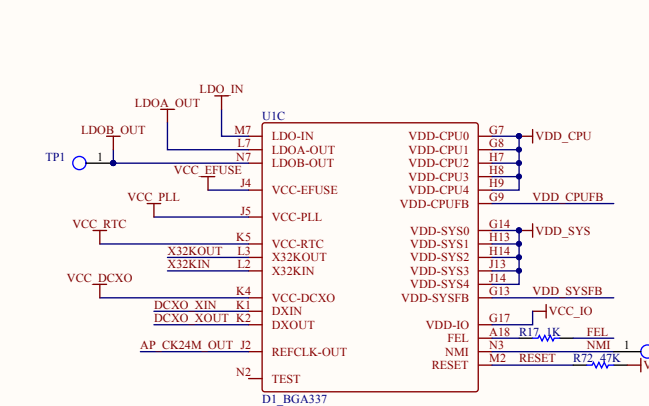
CP2104 & DEBUG



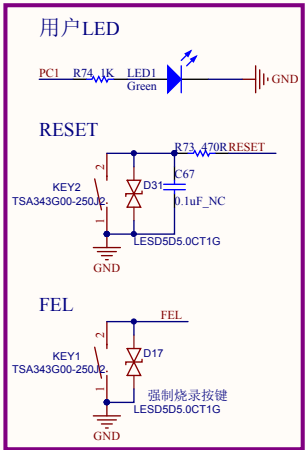
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D1_BGA337



D1_BGA337



Title		
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D1 DDR3

UIB

SDQ0	W7	SDQ0	SA0	V13	SA2
SDQ1	V7	SDQ1	SA1	W13	SA1
SDQ2	W8	SDQ2	SA2	V14	SA3
SDQ3	V8	SDQ3	SA3	U13	SA6
SDQ4	W4	SDQ4	SA4	U15	SA8
SDQ5	Y4	SDQ5	SA5	V11	SA0
SDQ6	V5	SDQ6	SA6	W11	SBA0
SDQ7	W5	SDQ7	SA7	Y13	SA11
SDQ8	U4	SDQ8	SA8	W14	SBA1
SDQ9	U7	SDQ9	SA9	Y15	SA13
SDQ10	U5	SDQ10	SA10	W12	SA12
SDQ11	T6	SDQ11	SA11	W15	SA7
SDQ12	U6	SDQ12	SA12	W16	SA14
SDQ13	R7	SDQ13	SA13	R15	SA5
SDQ14	T4	SDQ14	SA14	V16	SA9
SDQ15	R6	SDQ15	SA15	T15	SA4

SDQS0P	W6	SDQS0P	SRAS	U9	SRAS
SDQS0N	Y6	SDQS0N	SCAS	T9	SCAS
SDQS1P	W3	SDQS1P	SWE	R9	SA10
SDQS1N	Y3	SDQS1N			

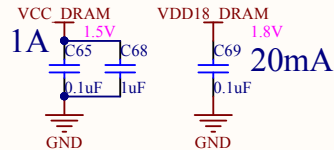
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SDQM1	W2	SDQM1	SCS1	R13	SCS1
			SBA0	V10	SBA2
			SBA1	W10	SWE
			SBA2	Y11	SA15
			SODT0	T12	SODT0
			SODT1	U12	SODT1
			SCKP	T10	SCKP
			SCKN	U10	SCKN
			SCKE0	W9	SCKE0
			SCKE1	Y9	SCKE1
			SRST	Y17	SRST
			SZQ	U17	
			SVREF	U16	SVREF

VDD18_DRAM P14

VCC_DRAM N8
N9
N10
P8
P9
P10

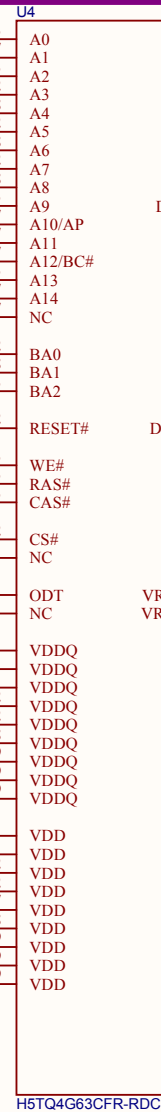
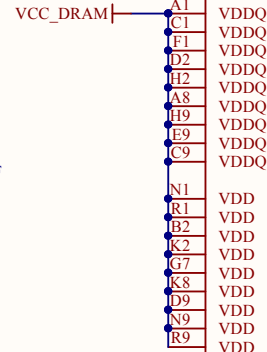
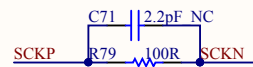
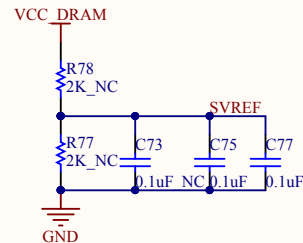
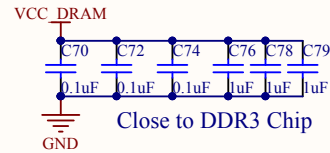
VDD18-DRAM
VCC-DRAM0
VCC-DRAM1
VCC-DRAM2
VCC-DRAM3
VCC-DRAM4
VCC-DRAM5

D1_BGA337



Close to AP

DDR3



H5TQ4G63CFR-RDC

DQL0	E3	SDQ5
DQL1	F7	SDQ7
DQL2	F2	SDQ4
DQL3	F8	SDQ6
DQL4	H3	SDQ3
DQL5	H8	SDQ0
DQL6	G2	SDQ2
DQL7	H7	SDQ1
DQSL	F3	SDQS0P
DQSL#	G3	SDQS0N
DML	E7	SDQM0

DQU0	D7	SDQ9
DQU1	C3	SDQ8
DQU2	C8	SDQ10
DQU3	C2	SDQ11
DQU4	A7	SDQ12
DQU5	A2	SDQ13
DQU6	B8	SDQ14
DQU7	A3	SDQ15
DQSU	C7	SDQS1P
DQSU #	B7	SDQS1N
DMU	D3	SDQM1

WE#	K9	SCKE0
RAS#	J9	SCKE1
CAS#		
CK	J7	SCKP
CK#	K7	SCKN

VREFCA	M8	SVREF
VREFDQ	H1	

NC	L9	
ZQ	L8	

VSSQ	B1	
VSSQ	D1	
VSSQ	G1	
VSSQ	E2	
VSSQ	D8	
VSSQ	E8	
VSSQ	B9	
VSSQ	F9	
VSSQ	G9	
VSSQ	E1	
VSSQ	M1	
VSSQ	P1	
VSSQ	T1	
VSSQ	J2	
VSSQ	B3	
VSSQ	G8	
VSSQ	J8	
VSSQ	A9	
VSSQ	M9	
VSSQ	P9	
VSSQ	T9	

VDDQ	C1	
VDDQ	F1	
VDDQ	D2	
VDDQ	H2	
VDDQ	A8	
VDDQ	H9	
VDDQ	E9	
VDDQ	C9	

VDD	N1	
VDD	R1	
VDD	B2	
VDD	K2	
VDD	G7	
VDD	K8	
VDD	D9	
VDD	N9	
VDD	R9	

VSS	N1	
VSS	R1	
VSS	B2	
VSS	K2	
VSS	G7	
VSS	K8	
VSS	D9	
VSS	N9	
VSS	R9	

VSS	N1	
VSS	R1	
VSS	B2	
VSS	K2	
VSS	G7	
VSS	K8	
VSS	D9	
VSS	N9	
VSS	R9	

VSS	N1	
VSS	R1	
VSS	B2	
VSS	K2	
VSS	G7	
VSS	K8	
VSS	D9	
VSS	N9	
VSS	R9	

VSS	N1	
VSS	R1	
VSS	B2	
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VSS	N9	
VSS	R9	

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Size

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Number

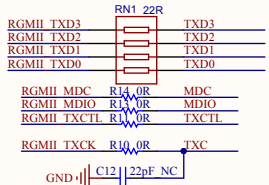
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Date: 2021/12/30

Sheet of

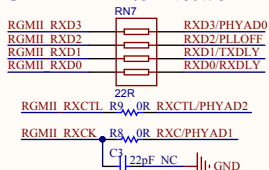
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RGMII TX Filter Network



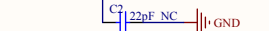
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RGMII RX Filter Network



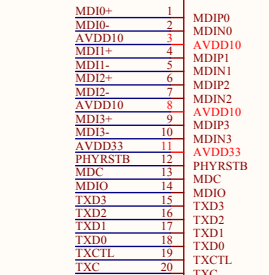
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RGMII TX Filter Network



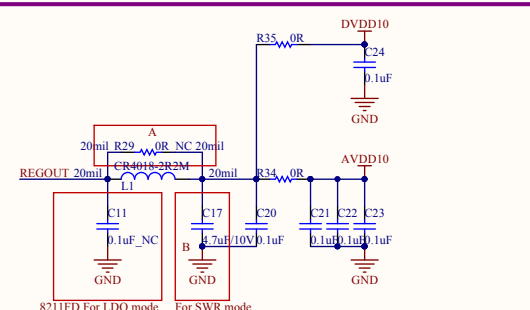
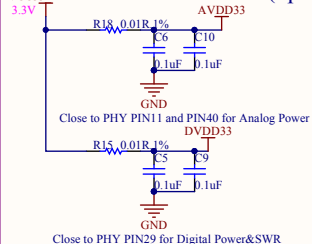
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RGMII RX Filter Network

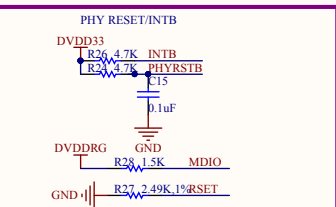
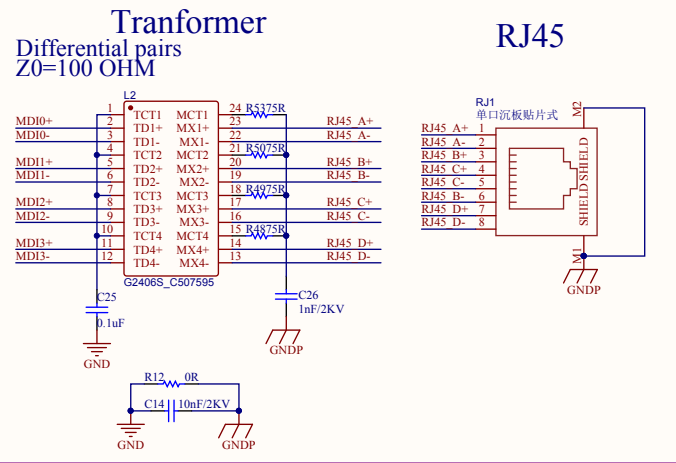


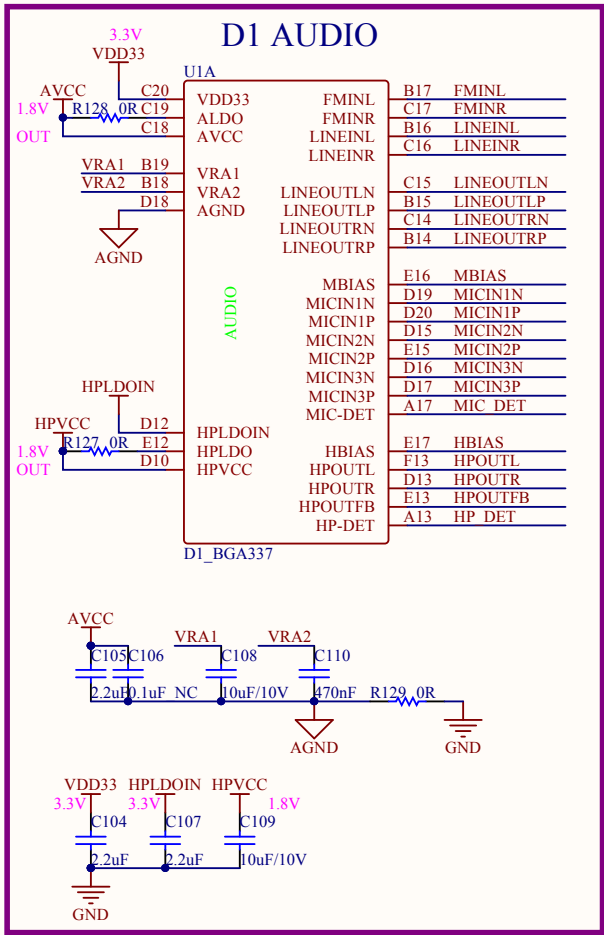
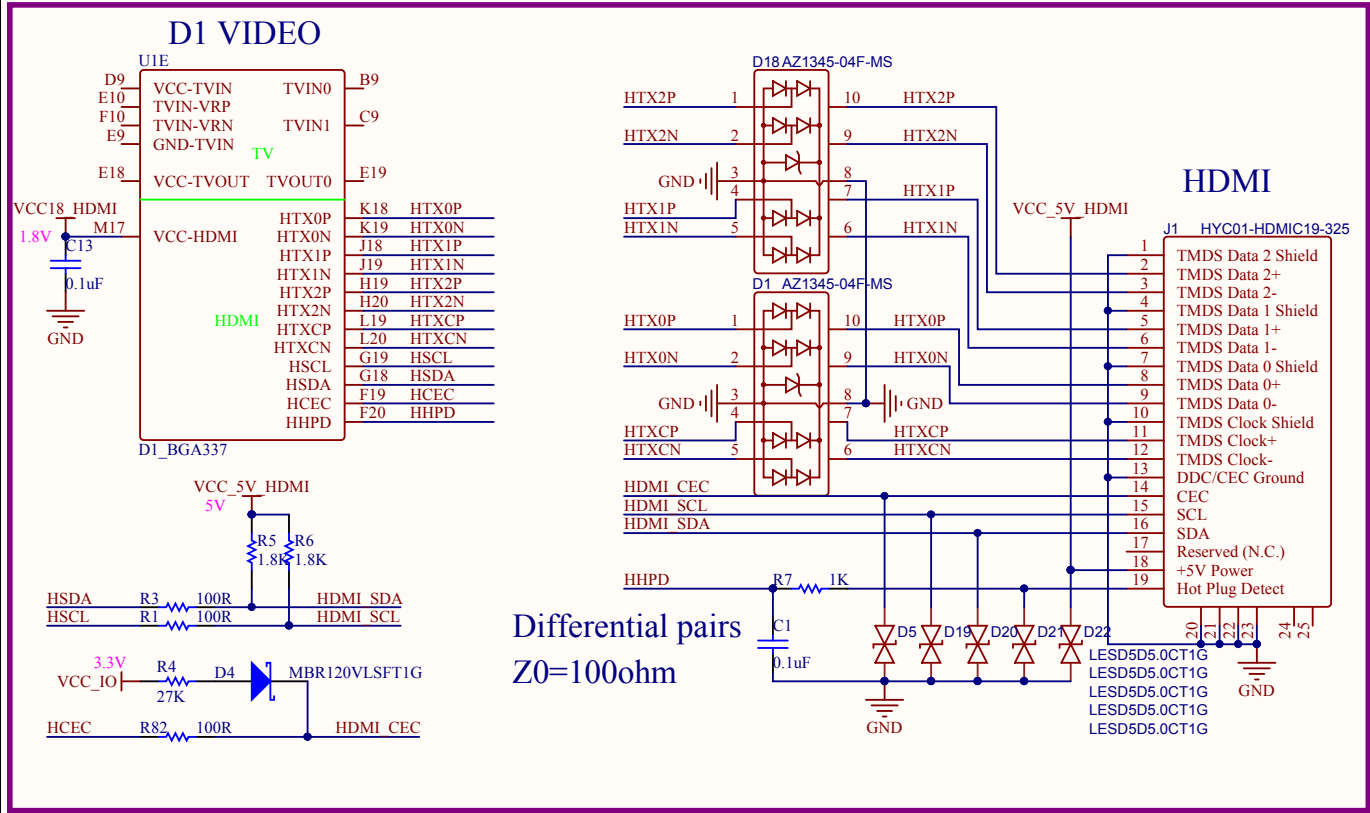
RTL8211F-CG

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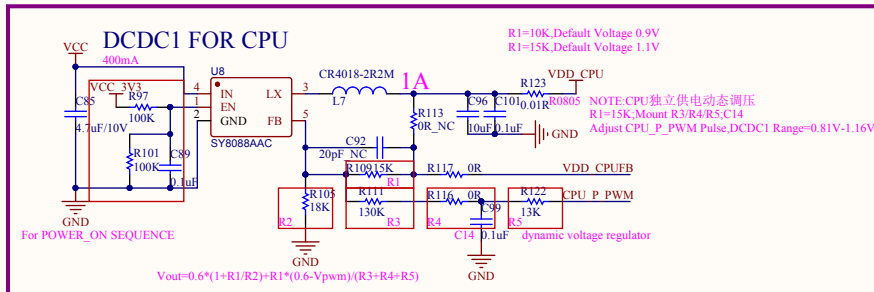
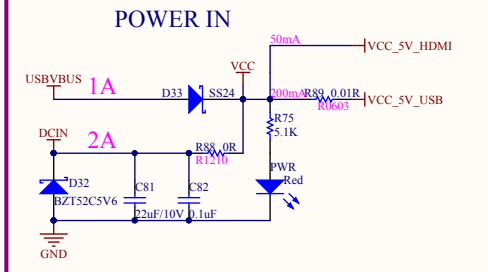
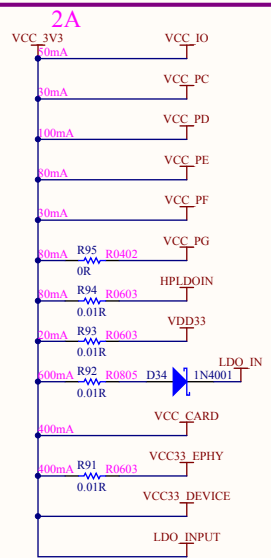
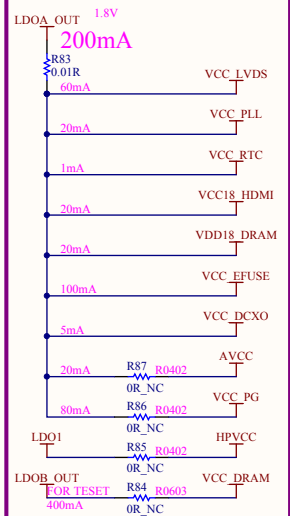
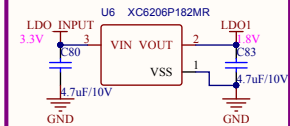


Note1:Bypass CAPs close to PHY DVDD10/AVDD10 power pins
Note2:Any inductance or bead except L1 is not allowed on the path from REGOUT to DVDD10/AVDD10
Note3:A is reserved to change th DVDD10/AVDD10 supply source to LDO mode(RTL8211FD)
Note4:No design change of PCB model is needed if A is reserved.If only RTL8211FD used for particular PCB model,directly short REGOUT to DVDD10/AVDD10
Note5:If RTL8211F1 is selected,the B should be replaced as 10uF X7R capacitor for industrial grade application,Please refer to the datasheet for other industrial grade information

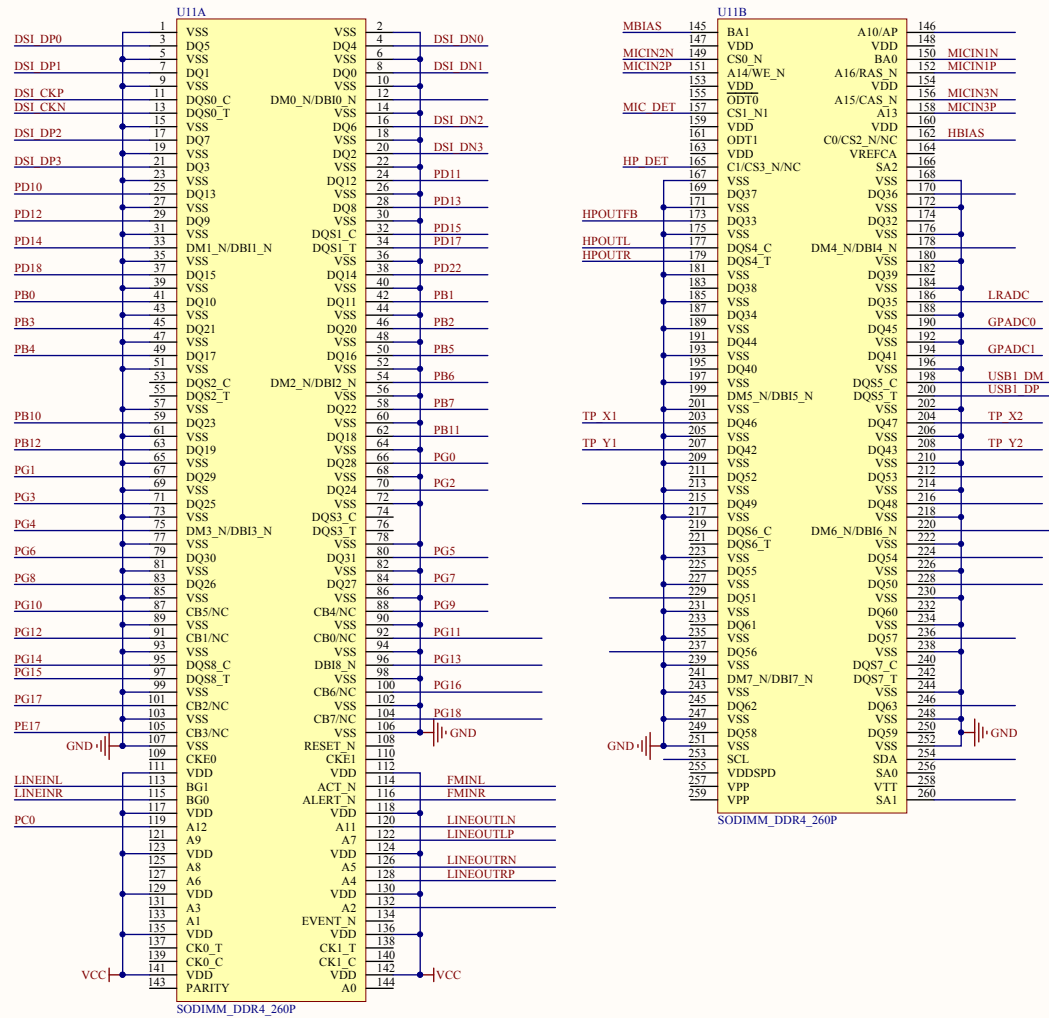




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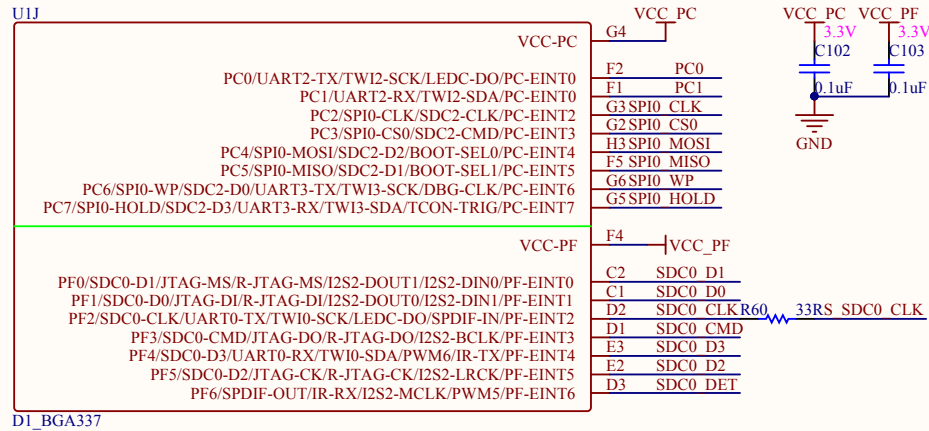


SODIMM_DDR4 260P

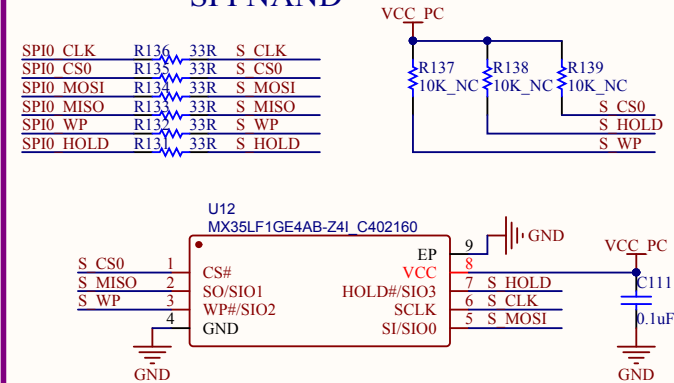


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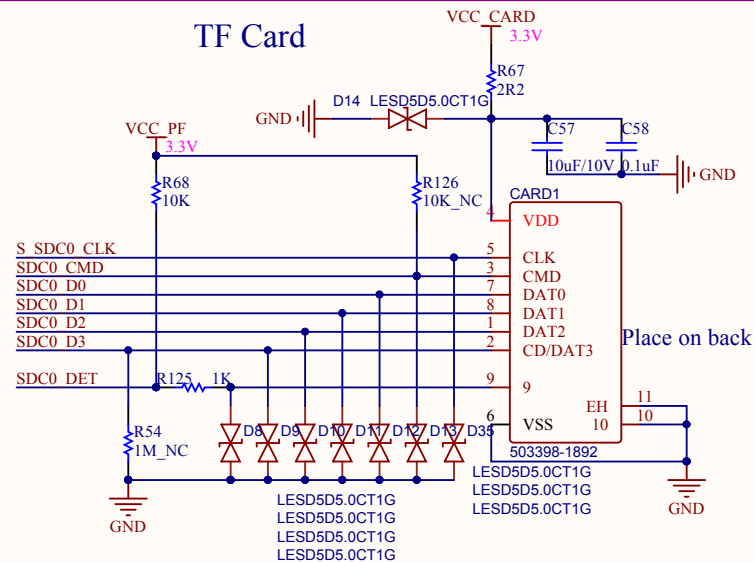
D1 SDIO



SPI NAND



TF Card



Title		
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